Linn



LM-1 Drum Computer Service Schematics

SERVICE NOTES FOR LM-1 DRUM COMPUTER

There are 3 versions of the LM-1 Drum Computer:

Version 1: These first units have the master volume control in the upper left corner of the front panel, and only 1 LED in the "Record Drumbeat" section.

Version 2: These units have the master volume control in the top center of the front panel, and 13 LEDS in the "Record Rhythm Pattern" section. All boards were redesigned, schematics redrawn.

Version 3: Same as Version 2, except:

- a) Internal clock rotary switch removed from rear panel internal clock frequency is instead set by "Auto Correct" setting.
- b) Record/Safe Switch removed from rear panel.
- c) "External Clock in" jack removed from rear panel
- d) "Main Output Lo" jacks removed from rear panel.

There is a pair of schematics and service notes for Version 1, and another pair of schematics and service notes for Versions 2 and 3. Each version has it's own software. The correct software revisions are 1,1, 2.1, and 3.1, respectively.

SERVICE NOTES FOR LM-1 DRUM COMPUTER VERSION 1

(Units with master volume control in upper left corner of front panel)

The LM-1 is a microcomputer-based device using the Zilog Z80 microprocessor. It consists of 5 circuit boards:

LM1-A/Keyboard--Contains key switches, discrete leds, and tempo pot.

LM1-B/Display board--Contains two dual-digit numeric led displays. LM1-C/Mixer board--Contains 13 slide pots, master volume, summing and output amps.

LM1-D/CPU board--Contains microprocessor, program roms, rams, and all input/output driver circuitry.

LM1-E/Voice board--Contains all drum voice generators.

ALL CIRCUITRY IS CONTAINED IN TWO SCHEMATICS:

- #1) Includes LM1-D, LM1-A and LM1-B,
- #2) Includes LM1-E, LM1-C.

Circuit description of schematic: Looking at left side of drawing, IC5 is the microprocessor, A ZILOG Z80 (or equivalent). IC11 and IC12 buffer the address buss lines AO-A11. IC13 and IC49 split the Z80's bi-directional data buss into an 8-BIT buffered input buss, and an 8-BIT buffered output buss. IC's 1, 2, 3 and 4 are used to decode the memory and I/Q enable lines from the Z80's A10, A11, A12, A13 and memory lines. The computer's control program is contained in roms IC30, IC31, and IC59. M1-M8 are 4k X 1 CMOS rams, and M9-M16 are sockets for 8 more (memory expansion). IC51, IC52 and IC53 (on the fix board) comprise the power-fail, memory protect, and reset circuits, which work as follows: The +10V signal at the power connector is taken from the input to the main +5V regulator, which is also provided at the power connector. The idea behind the power-fail circuit is that if the +5V logic supply is going to fail, this +10V signal will fall first, giving a warning. The power-fail signal is produced by pin 8 of IC53--when it goes high, the chip enable and write signals to the rams are disabled, protecting volatile memory during power down. At this point, the rams are kept active by the nickel cadmium batteries (totaling approximately +3.6V) which are trickle charged by the 7805--the CMOS +5V supply. The reset signal to the Z80 is also forced low, causing a CRU re-initialization when power is re-applied. The power-fail trimpot (connected to pin 9 of IC53 sets the threshold at which the power-fail signal sets.

The system clock is a rat-race type, oscillating at 14.318 MHZ, which is divided in 8 by counter 1C48. Tempo oscillator, foot switch, record/safe switch, tape storage PLL output, and internal clock out select switch are sensed by input port IC44. Keyboard status is provided by input port IC37. Discrete led's

on keyboard are driven by octal latch IC10 and buffer IC36. Dual digit displays are driven by octal latches IC8 and IC9, and 7-segment decoders IC32, IC33, IC34, and IC35. Enable lines for aforementioned input port (IC44) and output latches (IC latches (IC8, IC9 and IC10) as well as PLAY DRUM enable signals are provided by IC40 and IC41 (3 to 8 line decoders)

IC45 and IC47 are identical single chip phase locked loops (XR 2211) used to decode frequency shift keying (FSK) signals from the tape sync and tape storage inputs, respectively. Both signals shift between 1.3 KHZ and 2.1 KHZ, so the PLL's VCOS should be set to 1.7 KHZ (halfway between 1.3 KHZ and 2.1 KHZ). Both FSK signals are generated by a single chip FSK oscillator with sine wave output, IC43 (XR 2206).

Another XR 2206 (IC42 is used as the tempo oscillator. It's square wave output, divided down by counter IC48, is sampled by input port IC44.

Setting of the trimpots(these should not require adjustment in the field):

- 1) Power fail threshold (located on "fix" board mounted to rear of CPU board) --This requires a variable voltage AC power source. Plug the LM-1 into the variable voltage source and set to the normal 120V (or 240V). Now, while typing numbers on the front panel, (to see if the computer is still responding) slowly decrease the voltage. The computer should shut off at approximately 90VAC (180VAC)--the displays will no longer echo the numbers typed. Now, slowly increase the voltage. The computer should re-initialize at approximately 100VAC (200VAC). This 10VAC of hysteresis is provided by the 62k resistor around IC56. If your LM-1 does not shut off and on at these voltages, carefully adjust trimpot near IC56.
- 2) FSK oscillator freq: IC43 (XR 2206) provides the sine wave output for tape storage and tape sync FSK (frequency shift keying) data. The lower frequency should be 1.3KHZ. This can be adjusted by the trimpot next to IC43 which is closer to the rear of the LM-1. Pushing load on the front panel will cause the oscillator to produce its higher frequency, which is 2.1.KHZ. This can be adjusted with the other of the two trimpots.
- 3) Phase locked loop freq: IC45 and IC47 are two identical circuits using the single chip FSK decoder, the XR 2211, to decode the tape sync and tape storage signals, respectively, The freq of its free running VCO should be exactly between the FSK frequencies, or 1.7 KHZ. To check this, short pin 2 to pin 10 and measure the freq at pin 3. It can be adjusted with the trimpot nearby.

Operation of drum generators:

There are eight drum generators, and one click generator on the voice board LM1-C2. The click generator is simply a 556 dual one-shot (one for the loud click and one for the soft)--IC72.

The eight drum generators are:

- 1) Clave/Cowbell
- 2) Toms
- 3) Claps
- 4) Hi-Hat
- 5) Cabasa/Bass
- 6) Tambourine
- 7) Snare (identical to tambourine)
- 8) Congas (identical to toms)

The generators are basically the same with subtle differences tailored to each particular drum. The most straightforward is the claps generator:

A play claps strobe is generated on the CPU board and arrives at pin 3 of IC24, the clock input of a 74LS74 dual latch. On its rising edge, the data input (pin 2) is sampled, which is connected to data output buss bit 0. If a 1 is sampled, pin 6 goes lo (Q output). This starts the count of the 74LS393's (dual 4-bit ripple counters) at a rate determined by IC10 (an XR2206 oscillator, controlled by the rear panel clap pitch pot). The counters are connected for a 12 bit count (11 bits to address all 2048 words contained in a 2716 2048-word prom; and a 12th bit which is inverted by IC8 and resets latch IC24 to disable counters. The output of the prom is passed to the multiplying D/A converter IC50 (AM6070DC), whose output is the product of the digital input and the current input, pin 11. The differential current outputs, pins 16 and 17, are converted to a voltage by IC73 (4558 OPAMP), and the output is routed to the output connector.

Clave/Cowbell--These generators use only one 2716 for each sound (2048 words, 11 bits each), so are able to share a 74LS393.

Tambourine--In this generator, the latch, IC7, also samples data out buss bit 1. The corresponding output, pin 9, sums into the current input of the D/A converter, IC70 to provide a selection of loud or soft play volume.

Snare--Identical to tambourine.

Cabasa/Bass--Similar to clave/cowbell, except 2 volume circuitry used in tamb is implemented.

Toms--This generator can play a tom-tom at one of two pitches. Operation is as follows: Data buss bit 1 contains the pitch data (Hi=High pitch, Lo=Lo pitch). This data is passed to the output (pin 9) of Yatch IC5 on the rising edge of the play strobe at pins 3411. This signal causes oscillator IC6 to select between the pitch control resistors connected to pins 7 and 8. This signal also causes the D/A converter, IC58, to select either output pins 14 and 15 or output pins 16 and 17. This produces a high tom-tom at IC70 pin 1 and a low tom-tom at IC70 pin 7.

Congas--Same as toms.

Hihat--In this generator, the counters (IC25 and IC26 are always counting, so digital words are always being received at the D/A converter (IC62). To produce output, IC75 (TL082) pin 1 must To produce output, IC75 (TL082) pin 1 must produce an output positive voltage to feed the current input of the D/A, pin 11. The envelope produced by IC61 and IC75 can be small or large (loud or soft hihat) depending on the state of IC61 pin 5. the true output of a 74LS74 dual latch. The input of this latch is connected to data output buss bit 1, providing volume information. In addition, the envelope can be long or short decay (open or closed hi-hat), depending on the state of The data input to this half of the latch pin 12, is IC74 pin 8. connected to the data output buss bit 2, which provides open/closed hihat information. If a O is sampled at pin 12.IC74, a 1 will appear at pin 8 (Q not output), turning transistor ON and discharging envelope capacitor quickly through hihat decay pot. If state at pin 12 were 1, capacitor would be discharged slowly through 2.2 meg resistor (open hihat).

GETTING TO P.C. BOARDS FOR REPAIR:

CPU board--All that is necessary is to remove top by removing 6 black screws, then removing CPU board (if necessary) by removing 4 retaining screws. Important: If power connector is removed from CPU board, all memory will be lost! Also, if power connector is removed and reconnected to CPU board, power switch may have to be turned on and off up to 4 times before LM-1 will initialize properly.

Front panel boards-- Remove top, then remove wood sides with 8 screws, then remove front panel with 6 screws and nuts located at bottom front. NOTE: Nuts are loose inside. NOTE: On earliest units with ventilation holes on top, front panel does not remove.

Voice board--Remove top, unscrew CPU board, place insulating sheet over power supply, and fold CPU board over and rest it on insulating sheet. If voice board needs to be removed., remove 4 hex spacers, unplug all connectors from voice board, pull forward, and lift out.

POSSIBLE PROBLEMS AND SOLUTIONS:

Memory loss:

- 1) Check voltage at batteries. It should read approximately 3.6V. If lower, turn the LM-1 on for a day or two to charge them back up.
- 2) Check battery drain from rams--with power off, drain from battery should be no more than 200 microamperes. If it is, find the high current ram and replace it.
- 3) Check power fail circuitry:
 - A) IC5 pin 26 should be high with power on, low with power off.
 - B) Rams pins 10 and 8 should show battery voltage with

power off.

- 4) Test for hard memory errors:
 - A) Turn on LM-1
 - B) Press chain on/off
 - C) While holding record, press last entry, then release
 - D) If drumbeat # display blinks, it is blinking the number the bad ram (M1-M16). Otherwise no hard errors were found.

Tape won't load:

- 1) Read tape storage section of manual throughly.
- 2) Check PLL frequency (described earlier) IC47.
- 3) Make sure signal is getting to pin 2, IC47.

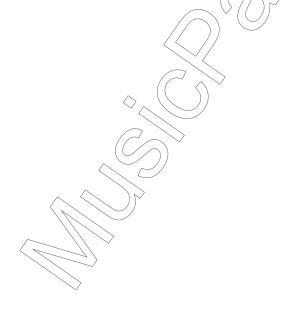
LM-1 won't initialize:

Proper power initialization is indicated by all leds turning off except drumbeat # display, which should show 00. If it doesn't (after powering on and off 3 or 4 times):

- 1) Check reset signal: Pin 26, IC5 should go high approximately 1/2 second after power-on, and go low on power-down.
- 2) Look for illegal logic levels around computer busses and control lines.
- 3) Is clock getting to U5, pin 6?

Buttons are bouncing:

- 1) Carefully remove cap of bad button from front panel by gently prying it off with screwdriver. Important: spring will pop out!
- 2) Clean contacts3) Replace cap



SERVICE NOTES FOR LM-1 DRUM COMPUTER VERSIONS 2 AND 3

(Units with master volume in top center of front panel)

The LM-1 is a microcomputer-based device using the Zilog Z80 Microprocessor. It consists of 6 circuit boards:

LM1-A/keyboard--contains key switches, discrete leds, and tempo pot.

LM1-B/display board--contains two dual-digit numeric led displays.

LM1-C2/mixer board--contains 13 slide pots, master volume, summing and output amps.

LM1-D2/CPU board--contains microprocessor, program roms, rams, and all input/output driver circuitry.

LM1-E2/voice board--contains all drum voice generators.

LM1-F/input-output board--one half of board holds jacks and switches which connect to CPU board. The other half holds all audio output jacks and connects to voice board.

- All circuitry is contained in two schematics:
- #1) Includes LM1-D2, LM1-A, LM1-B, and half of LM1-F.
- #2) Includes LM1-E2, LM1-C2, and other half of LM1-F.

Circuit description of schematic #1:

Looking at left side of drawing, U5 is the microprocessor, a Zilog Z80 (or equivalent) U11 and U12 buffer the address buss lines A0-A11. U13 and U49 split the Z80's bi-directional data buss into an 8-bit buffered input buss, and an 8-bit buffered output buss. U53 is a dual 2 to 4 line decoder which is used to decode the memory and 1/0 enable lines from the Z80's A10, A11, A12, A13 and memra lines. The computer's control program is contained in roms 030, 031, and 059. M1-M8 are 4K X 1 CMOS rams, and M9-M16 are sockets for 8 more (memory expansion). U55, U56, and U57 conprise the power fail, memory protect, and reset circuits, which work as follows: The input to the +5V regulator appears at the power connector, labled "+10V". In the event of a failure, the $+10\,\mathrm{V}$ signal will start to fall before the $+5\,\mathrm{V}$ supply fails giving an early warning. The power-fail signal is produced by pin 13 of U57--When it goes high, the chip enable and write signals to the rams are disabled, protecting volatile memory during power down. At this point, the rams are kept active by the nickel cadmium batteries (totaling approximately +3.6V) which are trickle charged by the 7805--the CMOS +5V The reset signal to the Z80 is also forced low, causing supply. a CPU re-initialization when power is re-applied. The power-fail trimpot (connected to pin 5 of U56) sets the threshold at which the power-fail signal sets.

The system clock is a rat-race type, oscillating at 3.57 MHZ, divided in half and squared by latch IC6.

Tempo oscillator, foot switch, record/safe switch (Version 2

only), tape storage PLL output, and internal clock out select switch (Version 2 only), are sensed by input port U44.

Keyboard status is provided by input port U37, discrete LED's on keyboard are driven by 2 octal latches, U10 and U50, buffer U36 and two 3 to 8 line decoders, U51 and U52.

Dual-digit displays are driven by octal latches U8 and U9, and 7-segment decoders U32, U33, U34, and U35. Enable lines for aforementioned input port (U44) and output latches (U8, U9, U10, an U50) as well as "play drum" enable signals are provided by U40 and U41 (3 to 8 line decoders).

U45 and U47 are identical single chip phase locked loops (XR 2211) used to decode frequency shift keying (FSK) signals from the tape sync and tape storage inputs, respectively. Both signals shift between 1.3KHZ and 2.1KHZ). Both FSK signals are generated by a single chip FSK oscillator with sine wave output, U43 (XR 2206).

Another XR 2206 (U42) is used as the tempo oscillator. Its square wave output is sampled by input port U44.

<u>Setting</u> of the trimpots (these should not require adjustment in the field):

- 1) Power Fail Threshold(located near M9)--this requires a variable voltage AC power source. Plug the LM-1 into the variable voltage source and set to the normal 120V (or 240V). Now, while typing numbers on the front panel (to set if the computer is still responding), slowly decrease the voltage. The computer should shut off at approximately 90VAC (180VAC)--The displays will no longer echo the numbers typed. Now, slowly increase the voltage. The computer should re-initialize at approximately 100VAC (200VAC). This 10VAC of hysteresis is provided by the 62K resistor around U56. If your LM-1 does not shut off and on at these voltages, carefully adjust trimpot near U56.
- 2) FSK oscillator freq: IC43 (XR 2206) provides the sine wave output for tape storage and tape sync FSK (frequency shift keying) data. The lower frequency should be 1.3KHZ. This can be adjusted by the trimpot next to IC43 which is closer to the rear of the LM-1. Pushing load on the front panel will cause the oscillator to produce its higher frequency, which is 2.1.KHZ. This can be adjusted with the other of the two trimpots.
- 3) Phase Locked Loop frequency: U45 and U47 are two identical circuits using the single chip phase locked loop FSK decoder, the XR 2211, to decode the tape sync and tape storage signals, respectively. The freq of its free running VCO should be exactly between the FSK frequencies, or 1.7 KHZ. To check this, short pin 2 to pin 10 and measure the freq at pin 3. It can be adjusted with the trimpot nearby.

Operation of drum generators:

There are eight drum generators, one click generator, and one beep generator on the voice board LM1-C2. The click generator is simply a "556" dual one-shot (one for the loud click and one for the soft)--U72. The beep is simply an inverter--the

beep is software-generated. The eight drum generators are:

- 1) Clave/Cowbell
- 2) Toms/Congas
- 3) Claps
- 4) Hi-Hat
- 5) Cabasa
- 6) Tambourine
- 7) Snare (identical to tambourine)
- 8) Bass

The generators are basically the same with subtle differences tailored to each particular drum. The most straightforward is the claps generator:

A "play claps" strobe is generated on the CPU board and arrives at pin 3 of U11, the clock input of a 74LS74 dual latch. On its rising edge, the data input (pin 2) is sampled which is connected to data output buss bit 0. If a "1" is sampled, pin 6 goes lo (Q not output). This starts the count of the 74LS393's (dual 4-bit ripple counters) at a rate determined by U10 (a 556 timer, used as an oscillator, controlled by the rear panel "claps pitch" pot). The counters are connected for a 13-bit count (12 bits to address all 4096 words contained in two 2716 2048-word proms; and a 13th bit which is inverted by U9 and resets latch U11 to disable counters. The outputs of the proms are sequentially passed to the multiplying D/A converter U58 (AM6070DC), whose output is the product of the digital input and the current input, pin 11. The differential current outputs, pins 16 and 17, are converted to a voltage by U69 (4558 opamp), and the output is routed to the output connector via a DC blocking capacitor.

Clave/Cowbell--these generators use only one 2716 prom each (2048 word, 11 bits each), so are able to share a 74LS393.

Tambourine--in this generator, the latch, U15, also samples data out buss bit 1. The corresponding output, pin 9, sums into the current input of the D/A converter, U60, to provide a selection of loud or soft play volume.

Snare-- Identical to tambourine.

Cabasa--same as tambourine except only one 2716 prom is used.

Bass-this generator is similar to the tambourine or snare circuits, with the addition of a 24 DB/OCT low pass filter, U75 (CEM 3320). The cutoff freq. is controlled by the offset trimpot, and an envelope generator (U18 generates a pulse, which quckly charges .047 MFD CCAP, which slowly discharges through 2.2 MEG RESISTOR). This allows the high freq. components of the drum's strike to pass, and reduces noise present in the decay of the recording.

Tom/Conga--this generator can play a tom-tom at one of two pitches, or a conga at one of two pitches. The operation is very similar to the bass generator, except:

- Data output buss bit 2 is also sampled by U2, pin 2 to select whether the tom recording or the conga recording play.
 - A dual 4-way CMOS Analog Multiplexer is used to select:
 - A) One of 4 outputs
 - B) One of 4 pitch pots

Hi-Hat--in this generator, the counters (U25 and U27) are always counting, so digital words are always being received at the D/A converter (U59). To produce output, U70 (TLO82) pin 1 must produce an output positive voltage to feed the current input of the D/A, pin 11. The envelope produced by U12, U13, and U70 can be small or large (loud or soft hihat) depending on the state of U13 pin 5, the true output of a 74LS74 dual latch. The input of this latch is connected to data output buss bit 1, providing volume information. In addition, the envelope can be long short decay (open or closed hihat), depending on the state of U13 pin 8. The data input to this half of the latch pin 12 is connected to the data output buss bit 2, which provides open/closed Hihat informtion. If A O is sampled at pin 12. U13, a "1" will appear at pin 8 (Q not output), turning transistor "on" and discharging envelope capacitor quickly through 1 meg rear panel hihat decay pot. If state at pin 12 were "1", capacitor would be discharged slowly through 2.2 meg resistor (open hihat).

Getting to P.C. boards for repair:

CPU Board--all that is necessary is to remove top by removing 6 black screws, then removing CPU board (if necessary) by removing 5 retaining screws. Important: If power connector is removed from CPU board, all memory will be lost! Also, if power connector is removed and reconnected to CPU board, power switch may have to be turned on and off up to 4 times before LM-1 will initialize properly.

Front Panel Boards -- remove top, then remove wood sides with 8 screws, then remove front panel with 6 screws and nuts located at bottom front. NOTE: Nuts are loose inside.

Voice Board--remove top, unscrew CPU board, place insulating sheet over power supply, and fold CPU board over and rest it on insulating sheet. If voice board needs to be removed., remove 5 male/female hex spacers, unplug all connectors from voice board, pull forward, and lift out.

Possible Problems and Solutions:

Memory Loss:

- Check voltage at batteries. It should read approximately 3.6V. If lower, turn the LM-1 on for a day or two to charge them back up.
- 2) Check battery drain from rams--with power off, drain from battery should be no more than 200 microamperes. If it is, find the high current ram and replace it.
- 3) Check power fail circuitry:

- A) U5 pin 26 should be high with power on, low with power off.
- B) Rams pins 10 and 8 should show battery voltage with power off.
- 4) Test for memory errors:
 - A) Turn on LM-1
 - B) Press "chain on/off"
 - C) While holding "record" press "last entry," then release
- D) If "drumbeat #" display blinks, it is blinking the number of the bad ram (M1-M16). Otherwise no hard errors were found.

Tape Won't Load:

- 1) Read "Tape Storage" section of manual thoroughly.
- 2) Check PLL Frequency (described earlier) U47.
- 3) Make sure signal is getting to pin 2, U47

LM-1 Won't Initialize:

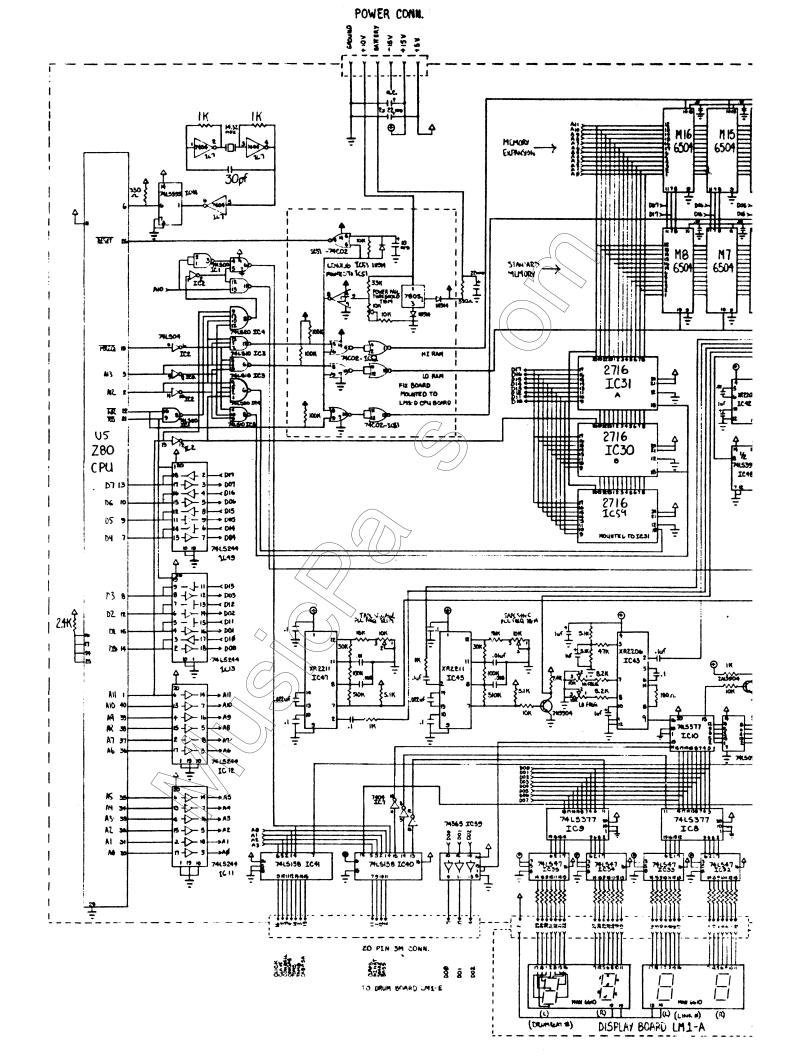
Proper power initialization is indicated by all LED's turning off except "drumbeat #" display, which should show "00". If it doesn't (after powering on and off 3 or 4 times):

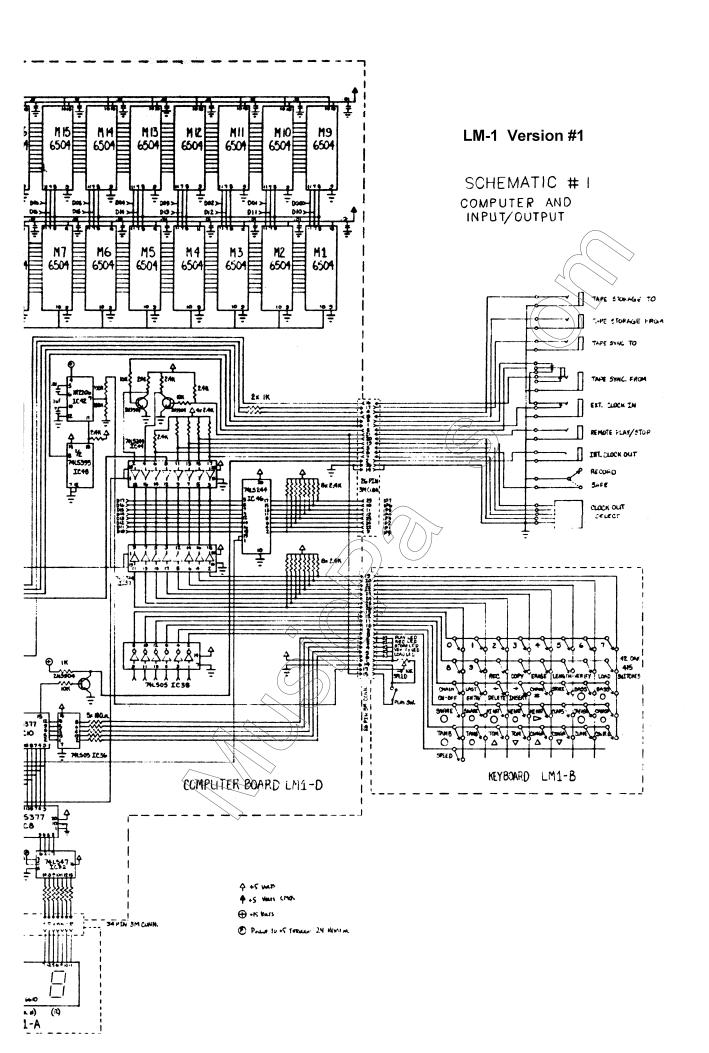
- 1) Check reset signal: Pin 26, U5 should go high approximately 1/2 second after power-on, and go low on power-down.
- 3) Is clock getting to U5, Pin 6?

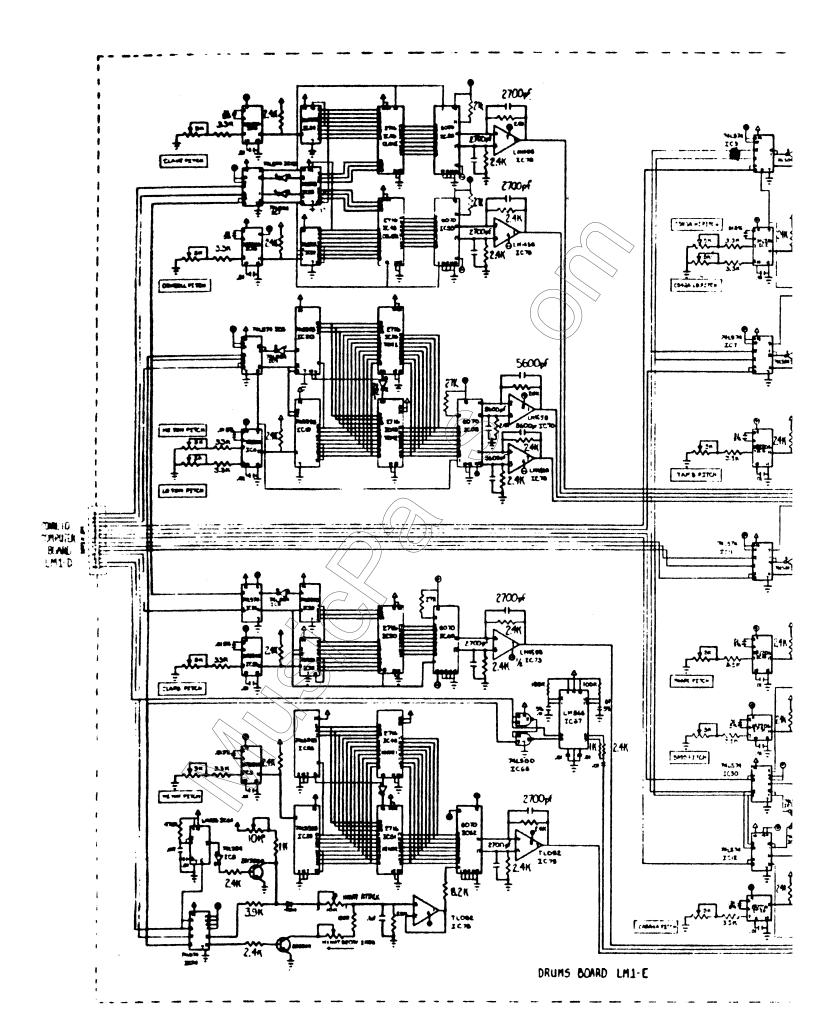
Buttons are Bouncing:

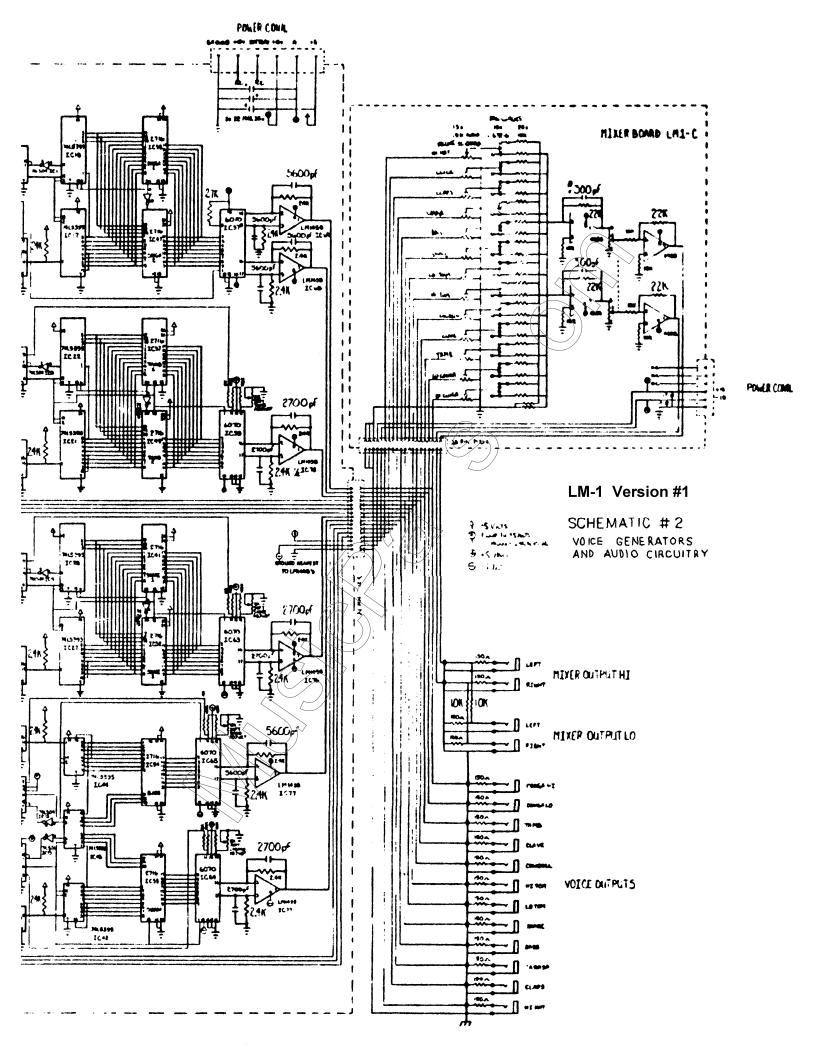
- 1) Carefully remove cap of bad button from front panel by gently prying it off with screwdriver. Important: Spring will pop out!
- 2) Clean contacts
- 3) Replace cap

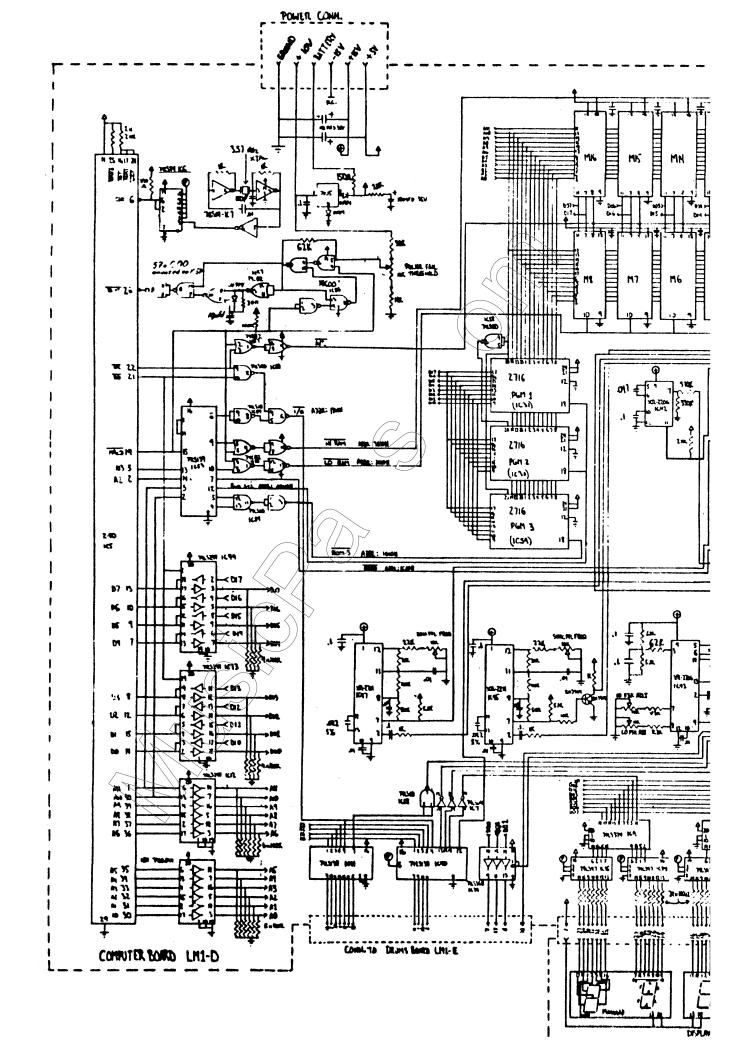
NOTE: Some units used a different type of button which when pressed, a "click-stop" is felt (Shadow collapsable diaphragm switch). These switches cannot be cleaned, so must be replaced.

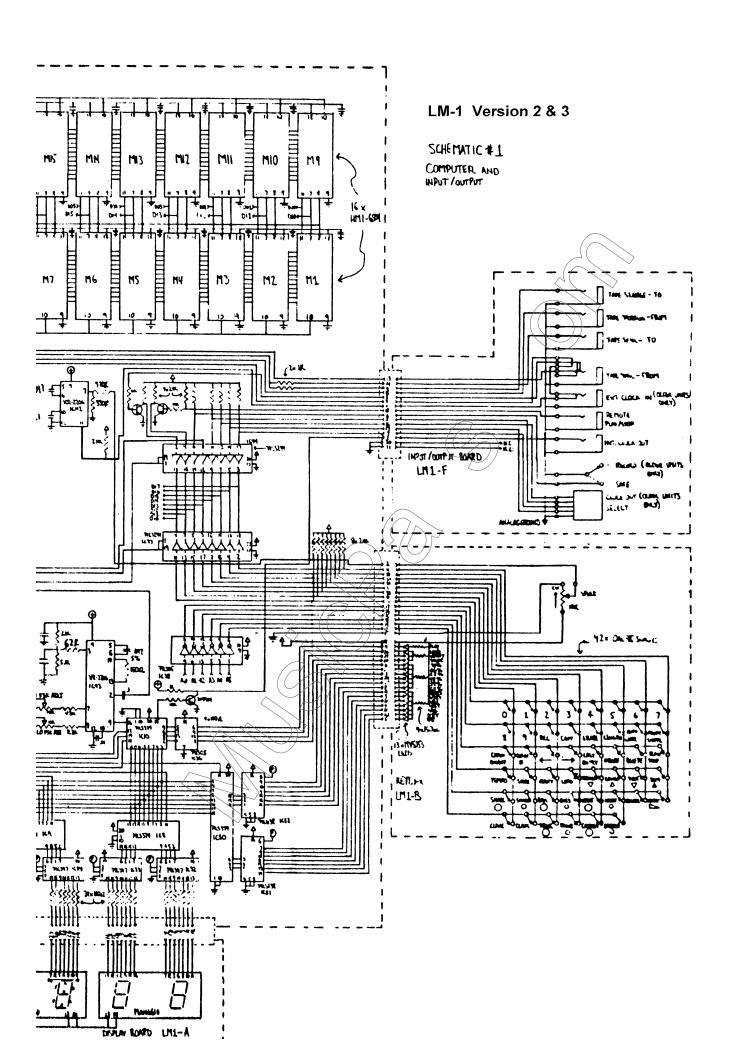


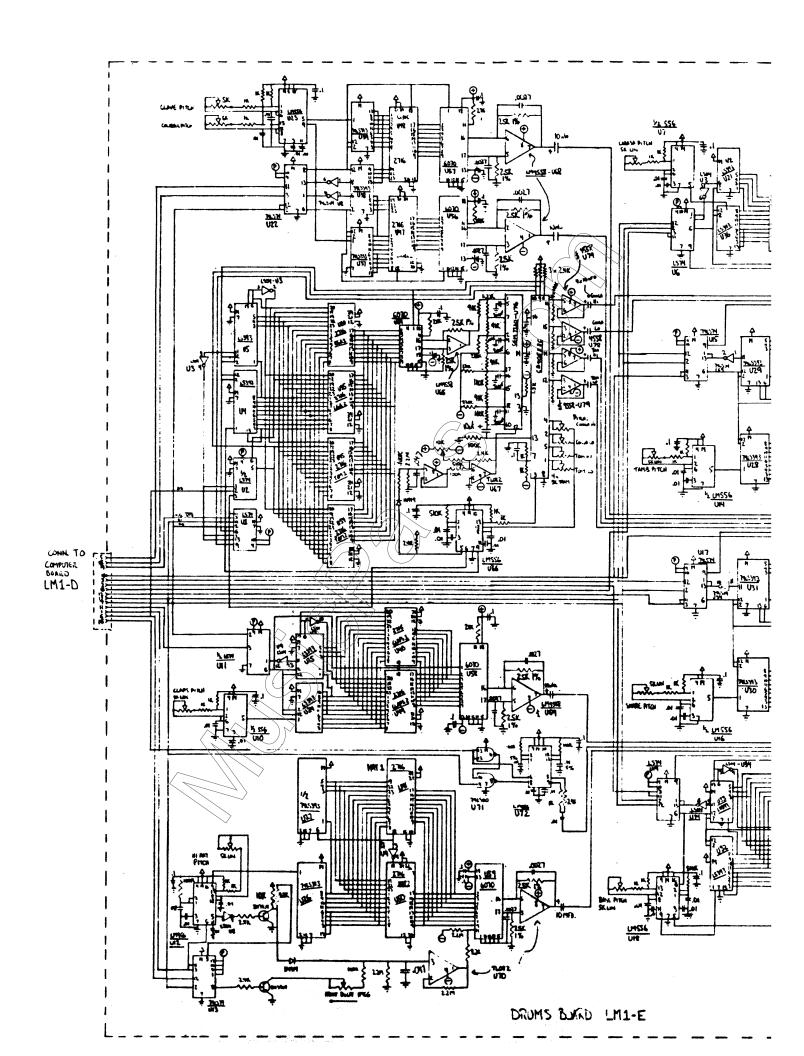


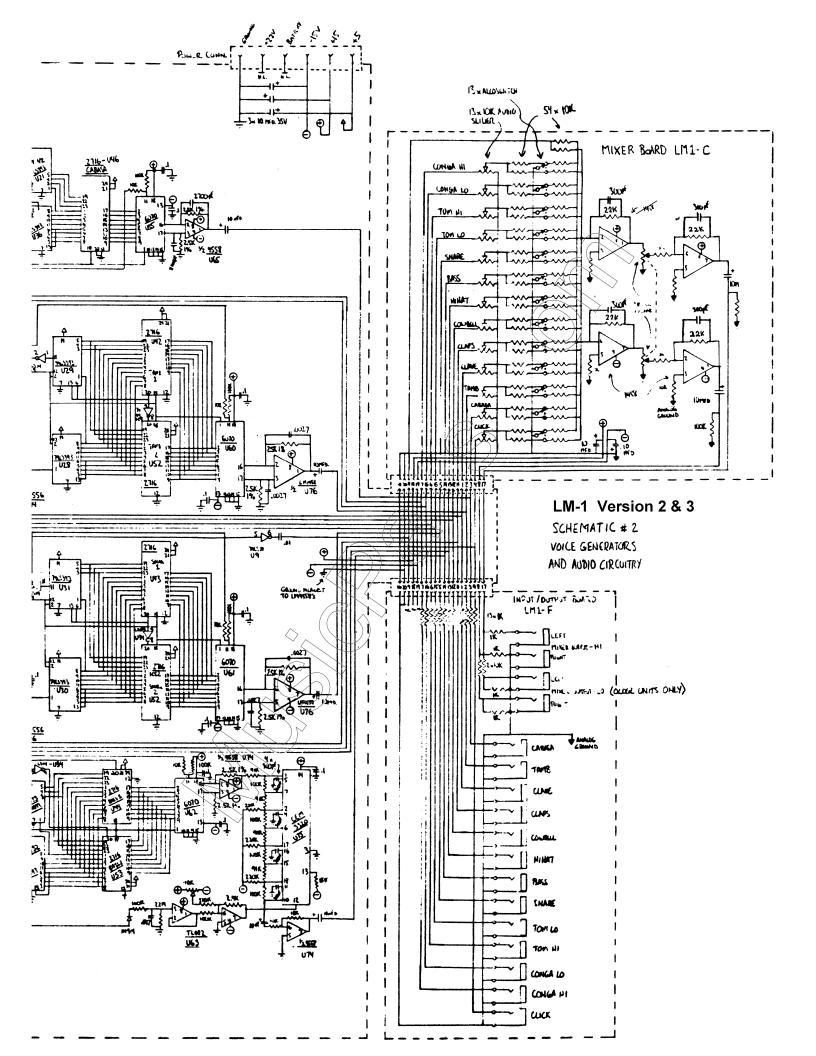












PART NUMBER	DESCRIPTION	PRICE
	CPU TO DRM CABLE ASSEMBLY	7.500
	5VSR TO DRM CABLE ASSEMBLY	6.600
	COMPLETELY ASSEMBLED CPU BOARD	
	COMPLETELY ASSEMBLED DRM BOARD	
	COMPLETELY ASSEMBLED MXR BOARD	
	5VSR BOARD COMPLETELY ASSEMBLD	31.020
	CMOS QUAD NAND GATE	0.440
	CMOS QUAD NOR GATE	0.440
	CMOS QUAD OR GATE	0.440
	CMOS 1 TO 8 HUX/DEMUX (RCA)	0.900
CD4053BC	CMOS TRIPLE SPDT ANALOG SWITCH	0.900
	TTL HEX BUS DRIVER	0.620
	QUAD NAND GATE	0.360
	HEX INVERTER	0.360
	HEX OPEN COLLECTOR INVERTOR 8 INPUT NAND GATE	0.360
	QUAD 2 INPUT OR GATE	0.360
	BCD TO 7 SEGMENT DECODER	0.940
741.574	DUAL D TYPE LATCH	0.400
74LS138	3 TO 8 LINE DECODER	0.600
74LS139	DUAL 3 TO 8 LINE DECODER	0.600
74LS151	1 OF 8 DATA SELECTOR/MPX	0.640
	OCTAL BUFFER/LINE DRIVER/RECVR	1.120
74LS365	HEX BUS DRIVER	0.540
74LS374	OCTAL D LATCH	1.120
74LS393	DUAL 4 BIT BINARY COUNTER	0.950
74LS627	DUAL VCO (T.I. ONLY)	2.880
	Z80 CPU	6.500
	2K X 8 CMOS RAM	18.700
	CURTIS FILTER I.C.	5.780 5.440
CEM330U	CURTIS VCA T.C.	0.860
	QUAD CMOS OF AMP	2.900
	COMPANDING D/A CONVERTOR	13.500
LH1605CK	+5 VOLT SWITCHING REGULATOR	17.100
LM340T-50	5 VOLT REGULATOR	1.440
	+15 VOLT REGULATOR	0.900
	DUAL OP AMP	0.500
	-15 VOLT REGULATOR	1.700
LM340T-5.0	5V REGULATOR FOR 5VSR BOARD	0.000
LM385BH1.2	VOLTAGE REFERENCE DIODE	12.000
	PRECISION REFERENCE DIODE IC	4.000
	LOW NOISE OF AMP (T.I. ONLY)	1.180
10M 5%	STACKPAC CF-07 10M 5% RESISTOR	0.030
.215 OHM 1%	RWR80SR215FR (RESISTOR)	1.260 0.050
1K 5%	RF14J1K (1/4W RESISTOR C+F)	0.050
1M 1%	5043ED1M000FT (1/4W RESISTOR) RF14J1M (1/4W RESISTOR)	0.050
1M 57 10v Ed	RF14J1M (1/4W RESISTOR)	0.050
101 26	WE LAGIOW/ IN AM WEDTOTOMY	0.000

	DDTCC
PART NUMBER DESCRIPTION	PRICE
	0.050
	0.050
	0.050 0.050
A A	0.050
3.6K5% RF14J3.6K	0.050
	0.050
	0.050 0.050
	0.050 0.050
75K 5% RF14J75K (1/4W RESISTOR)	0.050
	0.050
	0.050 0.050
	0.050
	0.050
	0.050
	0.050
	0.050 0.100
	0.050
5.1K 5% RF14J5.1K	0.050
	0.050 0.050
	0.050
8.2K 5% RF14J8.2K(1/4W(RESISTOR)	0.050
19.1K 1% 5043ED19K100FT (1/24W RESISTOR)	0.050
	0.100 0.100
	0.050
220 OHM 5% RF14J220 (1/4W RESISTOR C+F)	0.050
330 OHM 5% RF14J330 (1/4)W RESISTOR C+F)	0.050
	0.050
	0.100 5.760
1MFD 1.0/10/63MKS-4 WIMA CAPACITOR	0.570
.1MFD .1/10/160 MKS-4 (WIMA CAP)	0.196
	0.148
	0.282 0.140
.22MFD .22/10/100MKS (WIMA CAPACITOR)	0.248
	0.156
2700PF .0027/10/630 FKS (WIMA CAP)	0.136
	0.172 1.360
	0.112
.05MFD-DISC .05 /50-Z (PIHER CERAMIC DISC)	0.068
1000MFD-25V 1000/25PC (PANA RAD LYTIC CAP)	5.524
1000MFD-35V 1000/35PC (PANA RAD LYTIC CAP)	0.804

PART NUMBER	DESCRIPTION	PRICE
	1000/50PC (PANA RAD LYTIC CAP)	
	RF14J3.3K (1/4 W RESISTOR)	0.026
	TOLTEC#30060 INDUCTOR	2.720
	115 VOLT VARISTOR	1.640
	NPN TRANSISTOR GENERAL PURPOSE NPN TRANSISTOR	0.380 0.150
	PNP TRANSISTOR	0.150
	RECTIFIER DIODE FOR +/-15V	0.098
	GENERAL PURPOSE DIODE	0.128
	RECTIFIER DIODE FOR +5V	0.226
_	7.55V ZENER DIODE	0.130
	GERMANIUM DIODE	0.300
	DISCRETE RED LED	(())0.324
MAN4710	7 SEGMENT LED DISPLAY	2.500
	CTS 4 MHZ XTAL #MPO4A	1.920
	4114R-001-103 (BOURNS RES NET)	0.650
	4116R-001-153 (BOURNS RES NET)	0.650
	4114R-001-181 (BOURNS RES NET)	0.650
	4114R-002-104 (BOURNS RES NET)	0.650
	4116R-001-562 (BOURNS RES NET)	0.650
	16 PIN AUGAT SOCKET 24 PIN AUGAT SOCKET	0.294
	40 PIN AUGAT SOCKET	0.350 0.540
	24 PIN ARIES ZIF SOCKET	9.000
	OAK WOOD SIDES, OILED	24.000
	SV20N-1PC-CH15-BK(10/K10K SLDR)	0.786
	V16LPVN-MQ42-B100K (100K RTRY)	0.900
	V16LPVN-MQ29-8100K	0.716
POT-MASTER	V16LG3PVN-MQ42-A/10K (DUAL10K)	1.610
	SV45NP-CH15-AK5 (5KAUD SLIDER)	0.810
•	HORZ MNT 10K BECKMAN TRIM POT	1.040
•	VERT MNT 10K BECKMAN TRIM POT	1.040
RN-111PC		0.740
	J102Q06JJQ02J11 C+K PWR SWITCH	1.280
13001XXX	ECG BUTTON SWITCH	0.940
05020012	SWITCHCRAFT DPDT 110/220V SWCH LARGE CRIMP PIN	0.858
	6-PIN CRIMP CONNECTOR	0.050 0.220
	10-PIN CRIMP CONNECTOR	0.440
	20-PIN CRIMP CONNECTOR	0.750
	6-PIN FRICTION LOCK WAFER	0.330
	10-PIN FRICTION LOCK WAFER	0.560
	POLARIZING KEY	0.080
	16-PIN RIBBON CABLE CONNECTOR	6.240
40-012-100	40-PIN RIBBON CABLE CONNECTOR	14.260
7637-BLACK	C+K BLACK SWITCH CAP (BOOTIE)	0.060
	#6 KEPS NUT	0.050
#8KEPS	#8 KEPS NUT	0.050
6/32X3/8BLK	6/32 X 3/8 BLK PHILLIPS SCREW	0.050

PART NUMBER	DESCRIPTION	PRICE
	6/32 X 3/8 WZ-LK HEX SCREW	0.080
8/22V2/8W2LK	6/32 X 5/8 WZ-LK HEX SCREW 8/32 X 3/8 WZ-LK HEX SCREW	0.070 0.1 40
ATTHE LETT	SMITH 1/4 X 6/32 THRDED SPCR	0.100
8425 SMITH	SMITH 3/4 X 6/32 THRDED SPCR	2.000
HP1-T03-33CB	HEAT SINK FOR 5 VOLT SUPPLY	3.020
6063	THERMALLOY SM. HEAT SINK	0.260
	KEYSTONE BATTERY HLDR CLIP	0.080
	KEYSTONE AA SIZE BATTERY HLDR	1.620
	AA SIZE NICD BATTERIES	2.400
	BELDEN AC POWER CORD POWER TRANSFORMER	4.500 23.760
	LINNDRUM USER MANUAL	3.000
LGPHILSCREW		0.000
.012MFD	.012/10/160 MONO CAPACITOR	0.166
	BUSSMAN 1/2 AMP FUSE	1.012
	BUSSMAN FUSEHOLDER CLIPS	0.108
3MKUBBEKFEET	RUBBER FEET FOR UNIT BOTTOM SMALL BLACK PLASTIC KNOBS	0.080 0.396
G-100-WD	LARGE BLACK PLASTIC KNOBS	0.658
	LM-1 FSK VCO	5.620
	LM-1 FSK DECODER	6.580
LM-XTAL	LM-1 3.579 MHZ CRYSTAL	3.700
	LM-1 MASTER VOLUME POT	10.000
	LM-1 HIHAT DECAY POT - 1M AUD	3.000
	LM-1 PITCH OR TEMPO POT - 5K	3.000
	TAPE SYNC FROM JACK ON LM-1 LM-1 PAN SWITCH	2.580 3.640
	LM-1 PAN SWITCH LM-1 VOLUME SLIDER (CTS)	0.960
	LM-1 DUAL DIGIT DISPLAY	6.000
AM2716DC		0.000
CD4052BC	LM-1 CMOS MULTIPLEXOR	1.100
HM3-6504C-9	LM-1 4K X 1 CMOS RAM LOW POWER	7.700
LM1.1	LM-1 SOFTWARE VERS. 1	36.000
	LM-1 SOFTWARE VERS. 2 LM-1 SOFTWARE VERS. 3	36.000 36.000
	LM-1 STANDARD CABASA	24.000
	LM-1 STANDARD CONGA	36.000
	LM-1 STANDARD CLAVE	24.000
LM-COW1	LM-1 STANDARD COWBELL	24.000
LM-HAT1	LM-1 STANDARD HIHAT	36.000
	LM-1 STANDARD TOM-TOM	36.000
	LM-1 VERS.1 STANDARD BASS	24.000 36.000
	LM-1 VERS.2 STANDARD BASS LM-1 VERS.1 STANDARD CLAPS	24.000
	LM-1 VERS.2 STANDARD CLAPS	36.000
	LM-1 RIMSHOT	24.000
LM-SNAR1	LM-1 STANDARD SNARE	36.000
LM-TAMB1	LM-1 STANDARD TAMBOURINE	36.000

PART NUMBER	DESCRIPTION	PRICE
	LM-1 KEYSWITCH (OAK #415) LM-1 BATTERY- C SIZE	1.080





