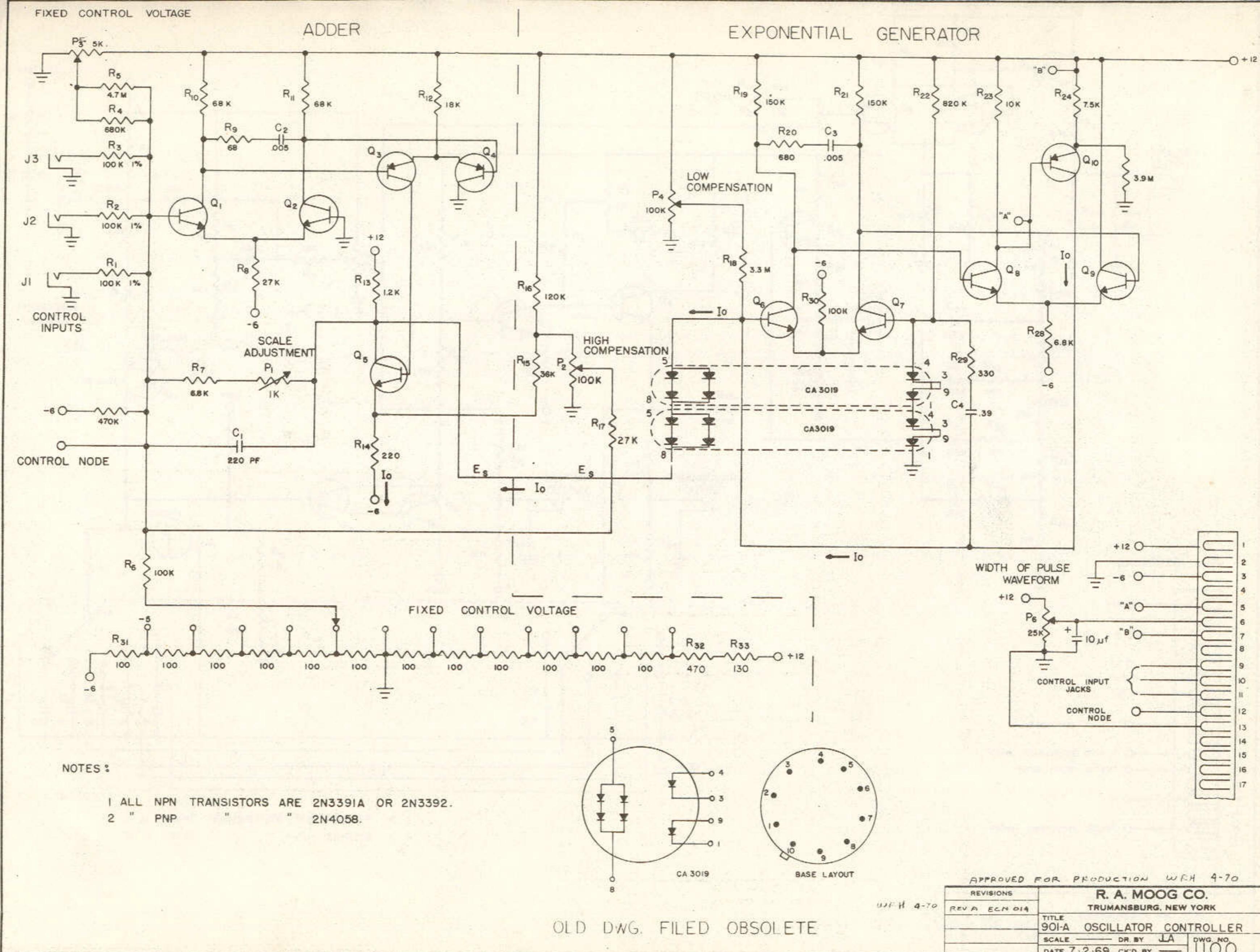


## **Moog Model 10**

### **Modules**

**901\***  
**901A**  
**901B(x2)**  
**902**  
**903A**  
**904A**  
**907**  
**910**  
**911(x2)**  
**951**  
**CP11\***

**(\*) Doesn't appear in schematics**



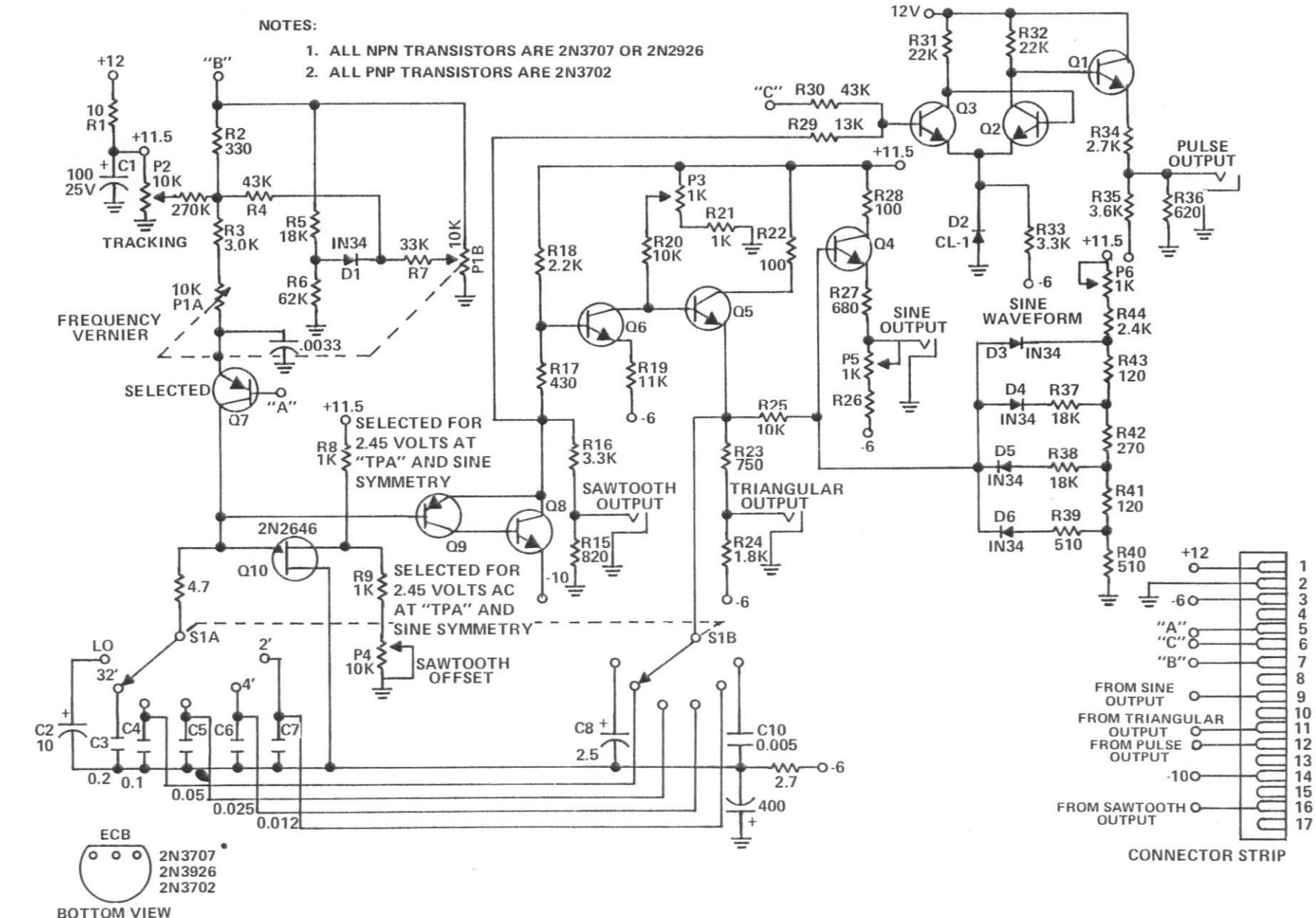


FIGURE 7 OSCILLATOR 901B

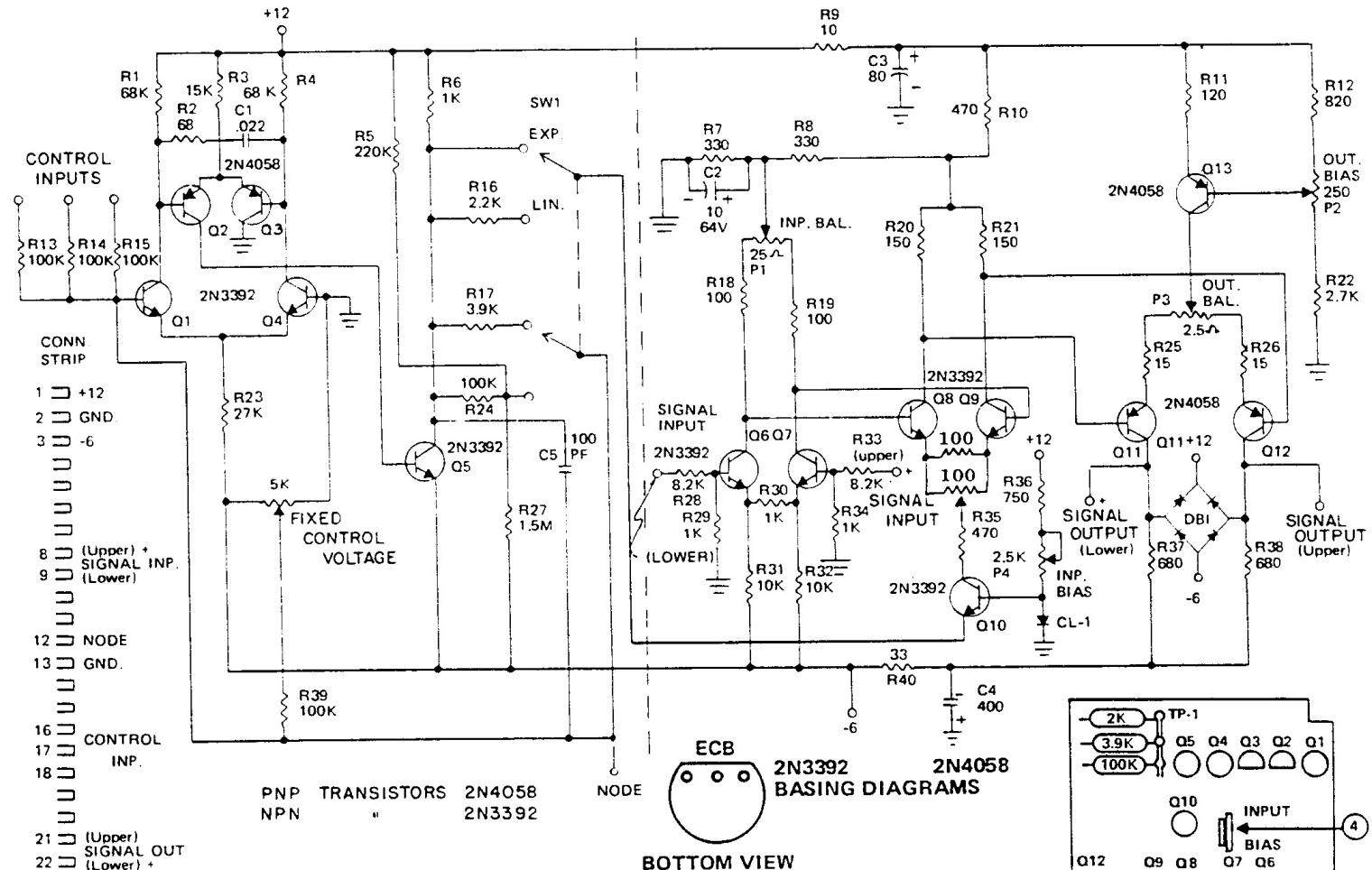
## 902 VOLTAGE CONTROLLED AMPLIFIER TEST PROCEDURE

1. Connect dc voltmeter to TP-L (collector of Q5); low side to ground.
2. Turn FIXED CONTROL VOLTAGE pot to 6 and set CONTROL MODE switch to "EXP." DC voltage should read approximately zero.
3. Rotate FIXED CONTROL VOLTAGE pot to 0. DC voltage should read approximately +0.24V.
4. Set CONTROL MODE switch to LIN. DC voltage should read approximately +1.2V.
5. Rotate FIXED CONTROL VOLTAGE pot to 6. DC voltage should read approximately -4.8V.

### NOTE

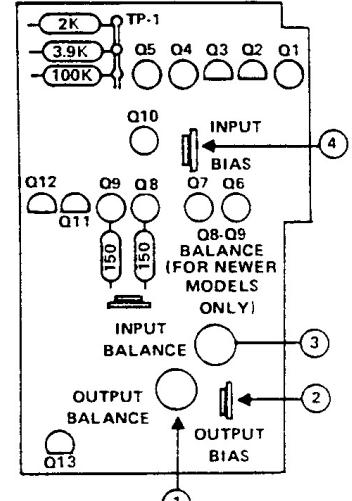
If the above voltages are observed, the adder section (Q1 thru Q5) is operating properly.

6. With FIXED CONTROL VOLTAGE in 6 and dc voltmeter connected between one of the SIGNAL OUTPUTS jacks and ground, adjust OUTPUT BIAS trimpot for zero volts.
7. Connect dc voltmeter across positive terminals of SIGNAL OUTPUTS jacks. Connect jumper between collectors of Q8 and Q9 and adjust OUTPUT BALANCE trimpot for 0 VDC.
8. Remove jumper across collectors of Q8 and Q9 and connect across collectors of Q6 and Q7. Adjust Q8 and Q9 BALANCE trimpot for 0 VDC.
9. Remove jumper and adjust INPUT BALANCE trimpot for 0 VDC.
10. Turn FIXED CONTROL VOLTAGE pot and ascertain that there is no large offset. If necessary, repeat steps 7, 8 and 9.
11. Turn FIXED CONTROL VOLTAGE pot to 6. Apply 0db 1kHz sine wave to one of the SIGNAL INPUTS. Signal output should be approximately +5db to +7db.
12. Note the output level. Set the CONTROL MODE switch to "EXP." Adjust INPUT BIAS to obtain a level equal to that noted in the "LIN" position.



13. Slowly turn FIXED CONTROL VOLTAGE pot from 6 to 0 and check for linear action in the LIN mode and exponential action in the EXP mode. At 0, signal output should be -60db maximum.
14. Turn FIXED CONTROL VOLTAGE pot to 6. With a dc bias, check each control input for proper voltage control. 0 volts should have no effect, -6 volts should cut the amplifier off completely.
15. With no signal input and FIXED CONTROL VOLTAGE set at 6, output noise should be -60db maximum.

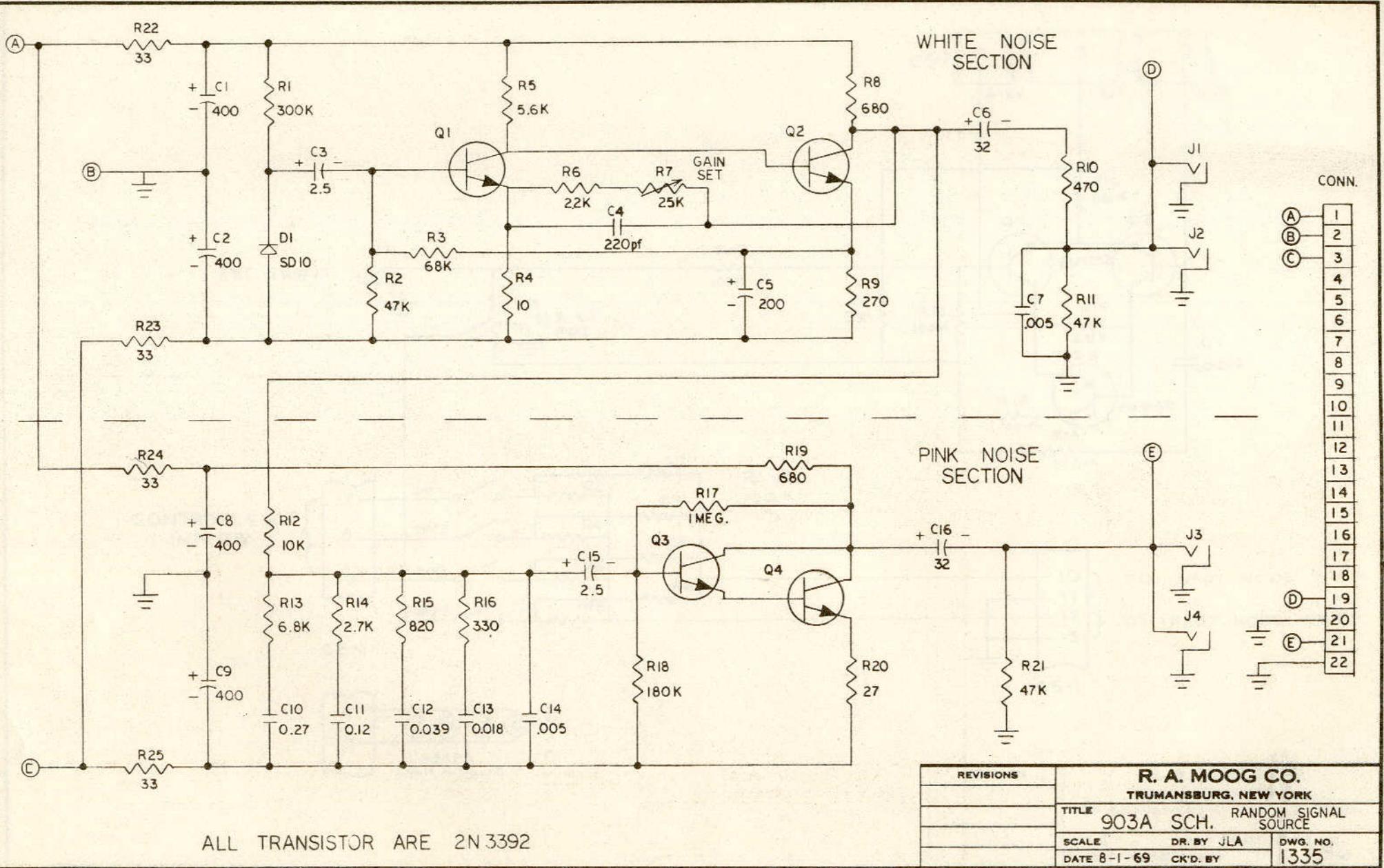
- ① Adjusts + output balance for exponential dc voltages with FIXED CONTROL VOLTAGE CONTROL fully counterclockwise.
- ② Adjusts zero output offset with FIXED CONTROL VOLTAGE control fully counterclockwise.
- ③ Adjusts zero output offset with FIXED CONTROL VOLTAGE control fully clockwise.
- ④ Adjusts amplitude level balance between linear and exponential mode with FIXED CONTROL VOLTAGE control full clockwise.

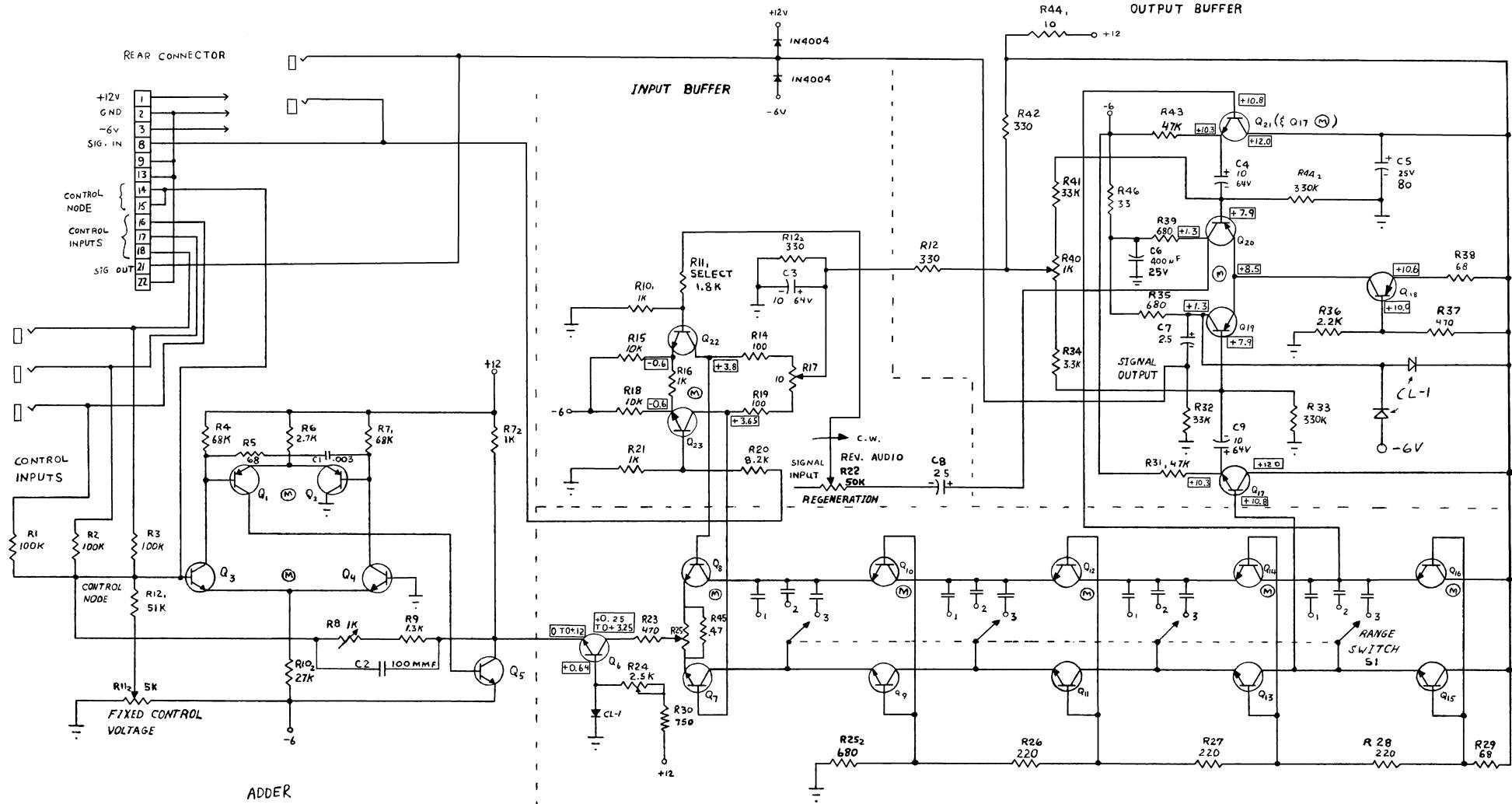


MOOG MUSIC INC.

SCHEMATIC, 902 VOLTAGE CONTROLLED AMPLIFIER  
993-041813

FIGURE 9 VOLTAGE CONTROLLED AMPLIFIER MODEL 902





NOTES:

1. ALL NPN TRANSISTORS: 2N 3392

2. ALL PNP TRANSISTORS: 2N 4058

3. (M)  $\Rightarrow$  MATCHED PAIR

4. RANGE CAPACITOR SIZES

- |   |               |
|---|---------------|
| 1 | $1.2 \mu F$   |
| 2 | $0.3 \mu F$   |
| 3 | $0.075 \mu F$ |

904-A VOLTAGE CONTROLLED  
LOW PASS FILTER

DRAWN BY P.Y. SCHEMATIC

APPR. BY DATE 7-25-67 DRAWING NUMBER SUPERCEDES NO. 1039 1149

REV. E	REV. D	REV. C	REV. B
7-25-67	7-25-67	11/10/70	ECN-003
WGS	JLA	WGS	ECN-003

R. A. MOOG CO.  
THUMANSBURG, N.Y.

5H

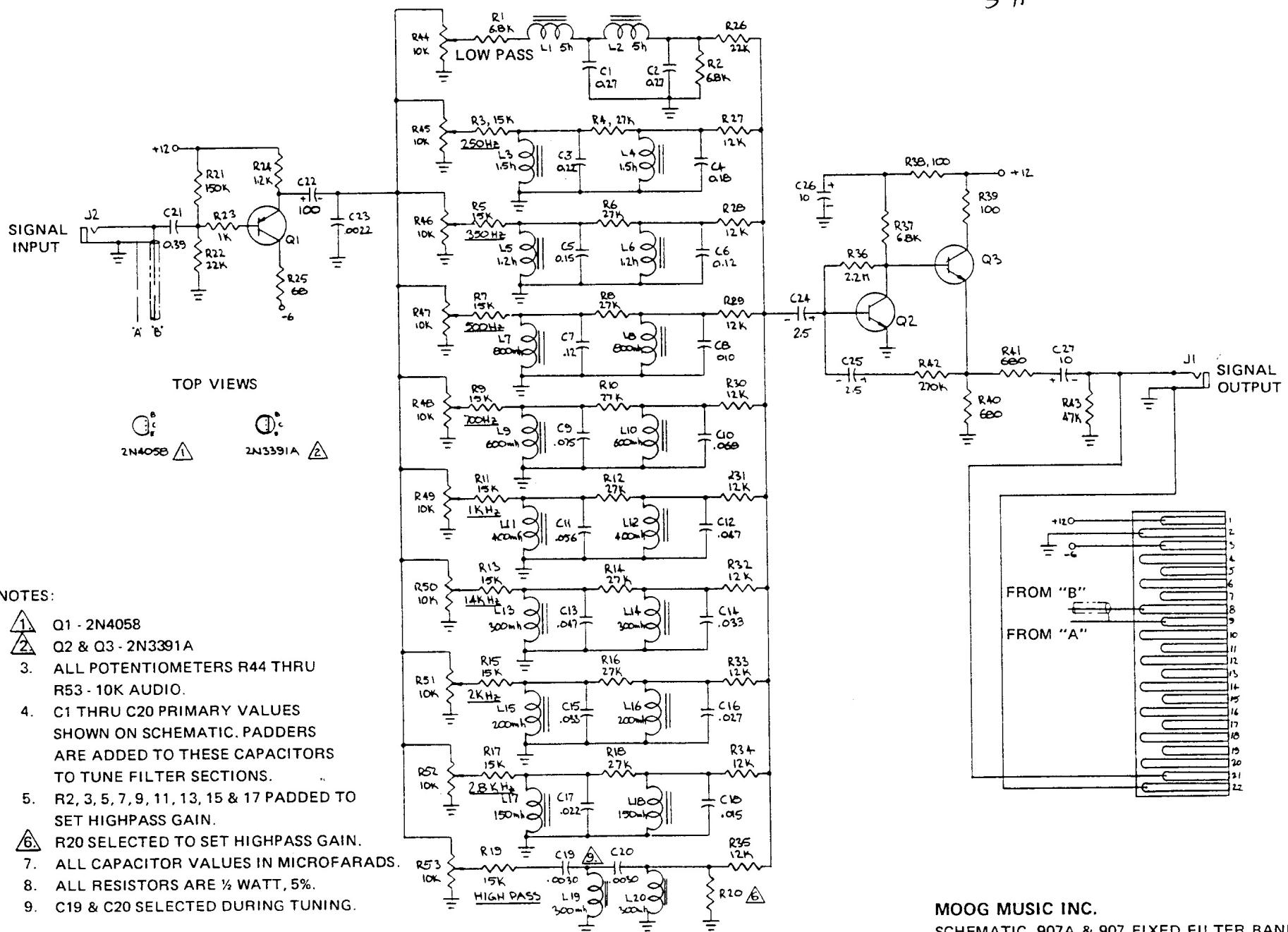


FIGURE 15 FIXED FILTER BANK MODELS 907 AND 907A

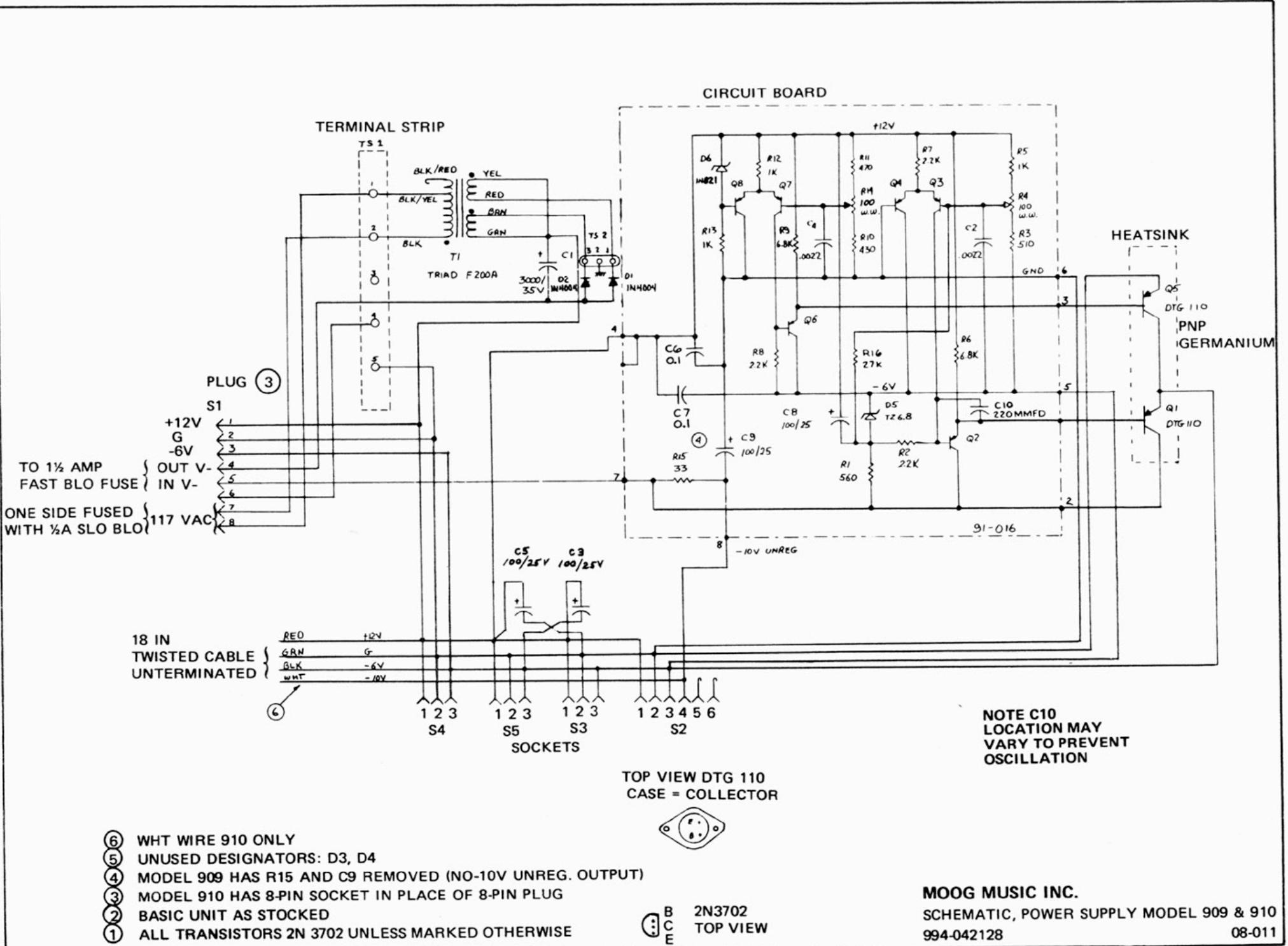
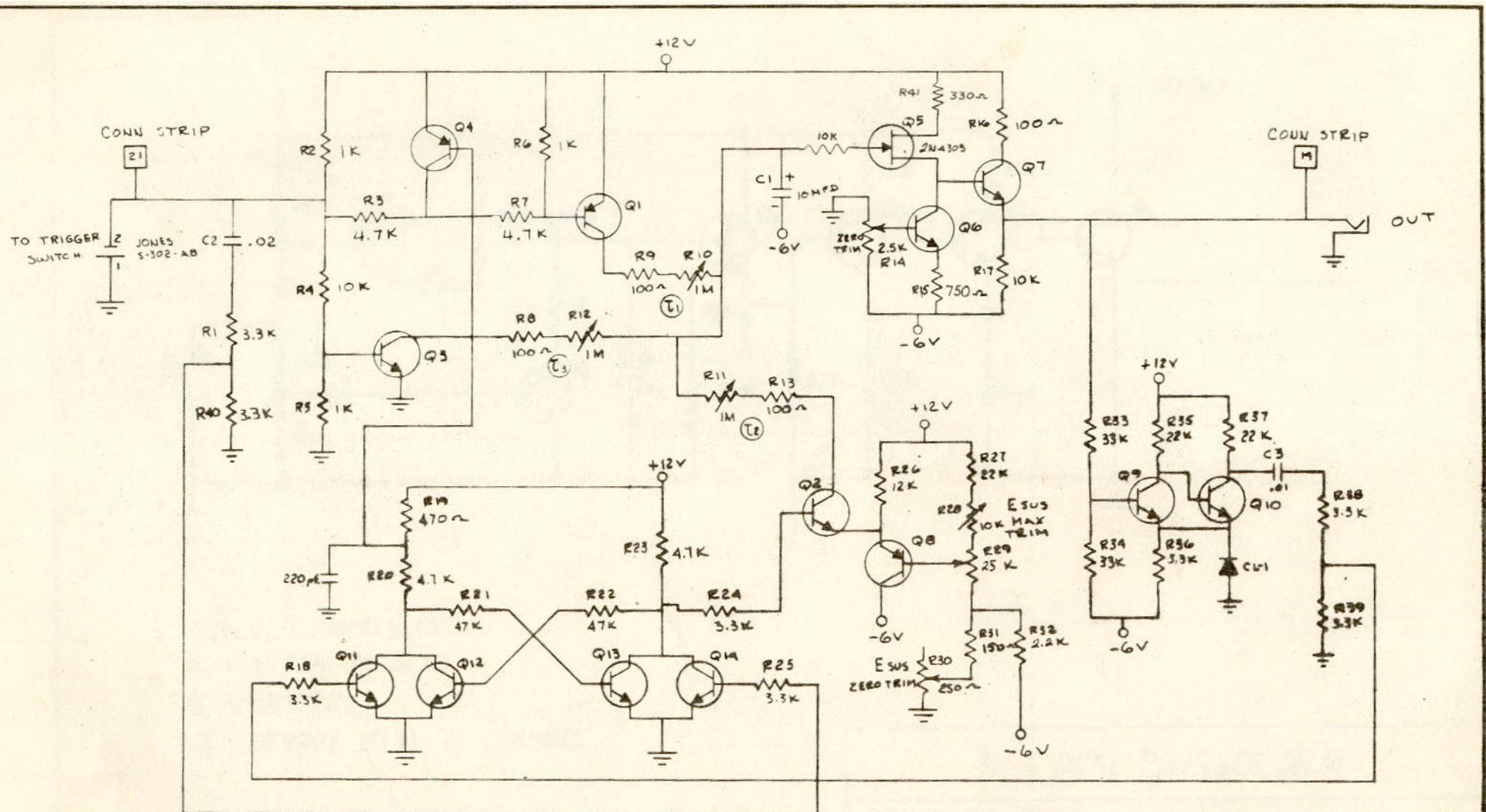


FIGURE 16 POWER SUPPLY MODELS 909 AND 910



NOTES:

- I. ALL PNP TRANSISTORS ARE 2N4058
- II. ALL NPN TRANSISTORS ARE 2N3392

REPLACES DWG 1103

W/R	C-ECN-004 1-12-69 21A	REVISIONS	
		A-B15 FROM 1.5K	
	To B20A		
	A - R41 FROM Q5		
	To E12V		
	B - ECN-002		
	DATE 8-14-68 CKD BY	1220	DWG. NO.

R. A. MOOG CO.  
TRUMANSBURG, NEW YORK

911 ENVELOPE GENERATOR

