

Moog Model 12 Modules

902

903A

904A

907

910

911(x2)

921

921A

921B(x2)

952

CP11*

(*) Doesn't appear in schematics

902 VOLTAGE CONTROLLED AMPLIFIER TEST PROCEDURE

1. Connect dc voltmeter to TP-L (collector of Q5); low side to ground.
2. Turn FIXED CONTROL VOLTAGE pot to 6 and set CONTROL MODE switch to "EXP." DC voltage should read approximately zero.
3. Rotate FIXED CONTROL VOLTAGE pot to 0. DC voltage should read approximately +0.24V.
4. Set CONTROL MODE switch to LIN. DC voltage should read approximately +1.2V.
5. Rotate FIXED CONTROL VOLTAGE pot to 6. DC voltage should read approximately -4.8V.

NOTE

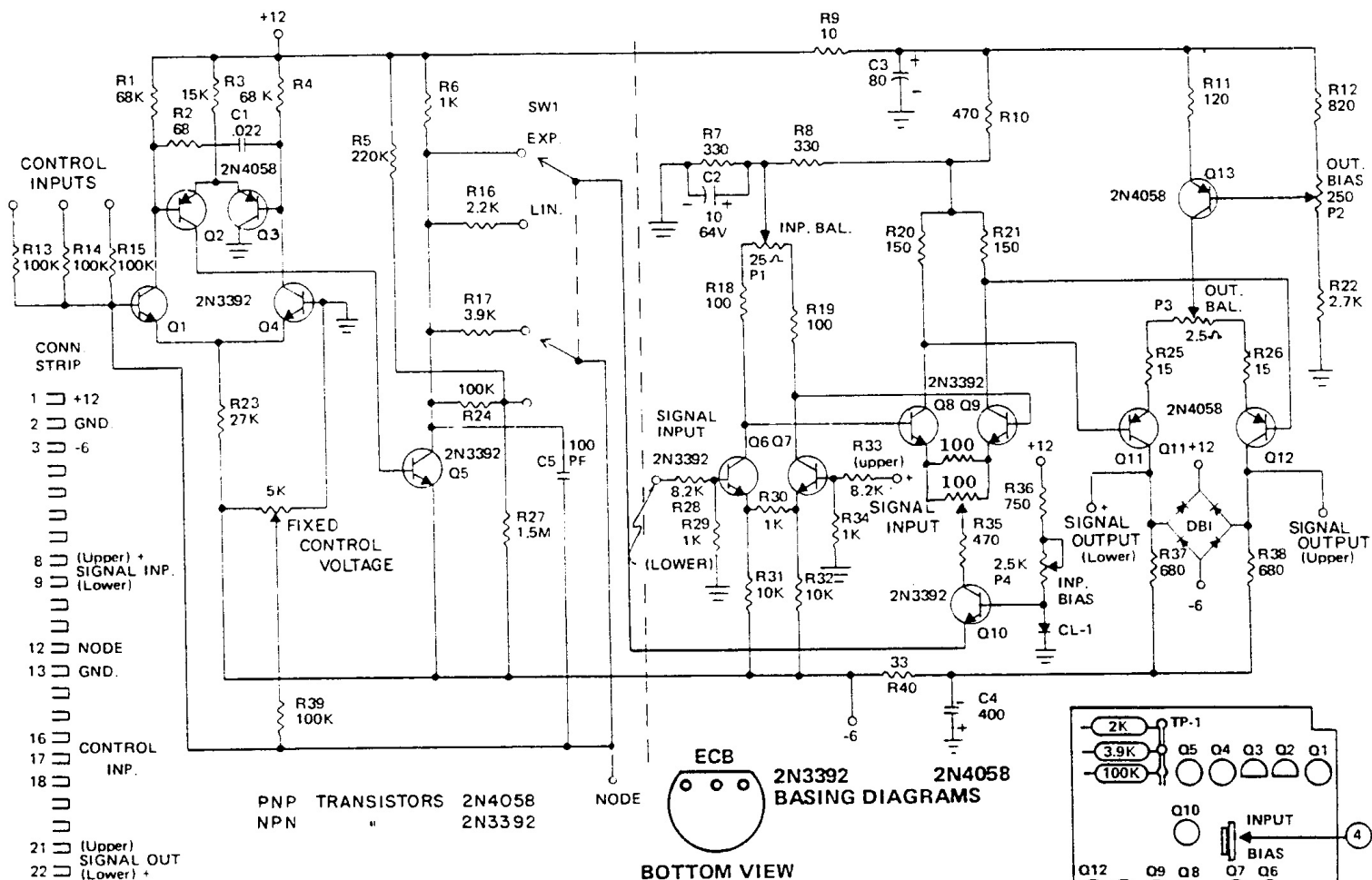
If the above voltages are observed, the adder section (Q1 thru Q5) is operating properly.

6. With FIXED CONTROL VOLTAGE in 6 and dc voltmeter connected between one of the SIGNAL OUTPUTS jacks and ground, adjust OUTPUT BIAS trimpot for zero volts.
7. Connect dc voltmeter across positive terminals of SIGNAL OUTPUTS jacks. Connect jumper between collectors of Q8 and Q9 and adjust OUTPUT BALANCE trimpot for 0 VDC.

8. Remove jumper across collectors of Q8 and Q9 and connect across collectors of Q6 and Q7. Adjust Q8 and Q9 BALANCE trimpot for 0 VDC.
9. Remove jumper and adjust INPUT BALANCE trimpot for 0 VDC.
10. Turn FIXED CONTROL VOLTAGE pot and ascertain that there is no large offset. If necessary, repeat steps 7, 8 and 9.

11. Turn FIXED CONTROL VOLTAGE pot to 6. Apply 0db 1kHz sine wave to one of the SIGNAL INPUTS. Signal output should be approximately +5db to +7db.

12. Note the output level. Set the CONTROL MODE switch to "EXP." Adjust INPUT BIAS to obtain a level equal to that noted in the "LIN" position.



13. Slowly turn FIXED CONTROL VOLTAGE pot from 6 to 0 and check for linear action in the LIN mode and exponential action in the EXP mode. At 0, signal output should be -60db maximum.
14. Turn FIXED CONTROL VOLTAGE pot to 6. With a dc bias, check each control input for proper voltage control. 0 volts should have no effect, -6 volts should cut the amplifier off completely.
15. With no signal input and FIXED CONTROL VOLTAGE set at 6, output noise should be -60db maximum.

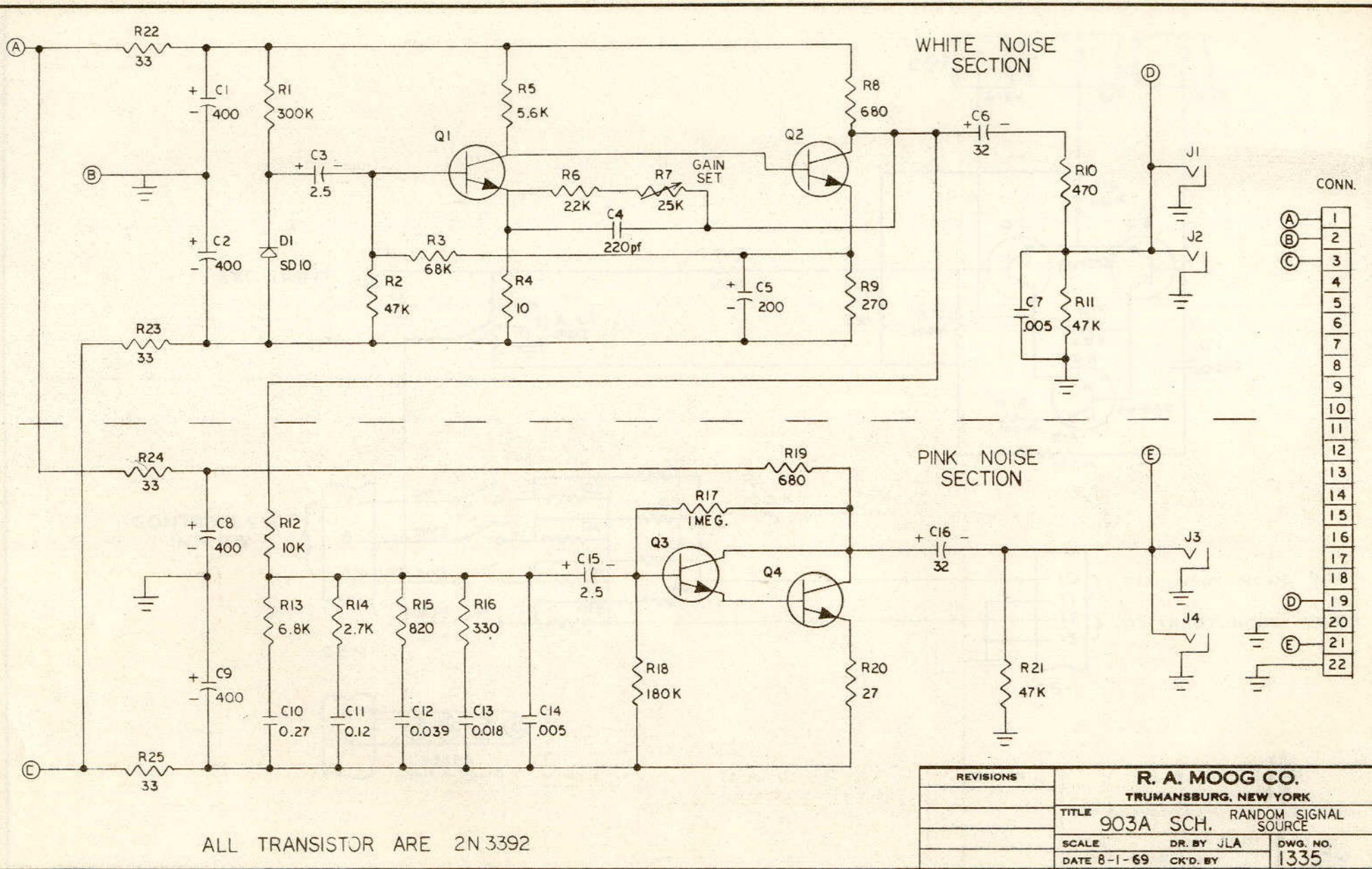
- 1 Adjusts + output balance for exponential dc voltages with FIXED CONTROL VOLTAGE control fully counterclockwise.
- 2 Adjusts zero output offset with FIXED CONTROL VOLTAGE control fully counterclockwise.
- 3 Adjusts zero output offset with FIXED CONTROL VOLTAGE control fully clockwise.
- 4 Adjusts amplitude level balance between linear and exponential mode with FIXED CONTROL VOLTAGE control full clockwise.

MOOG MUSIC INC.

SCHMATIC, 902 VOLTAGE CONTROLLED AMPLIFIER
993-041813

1068

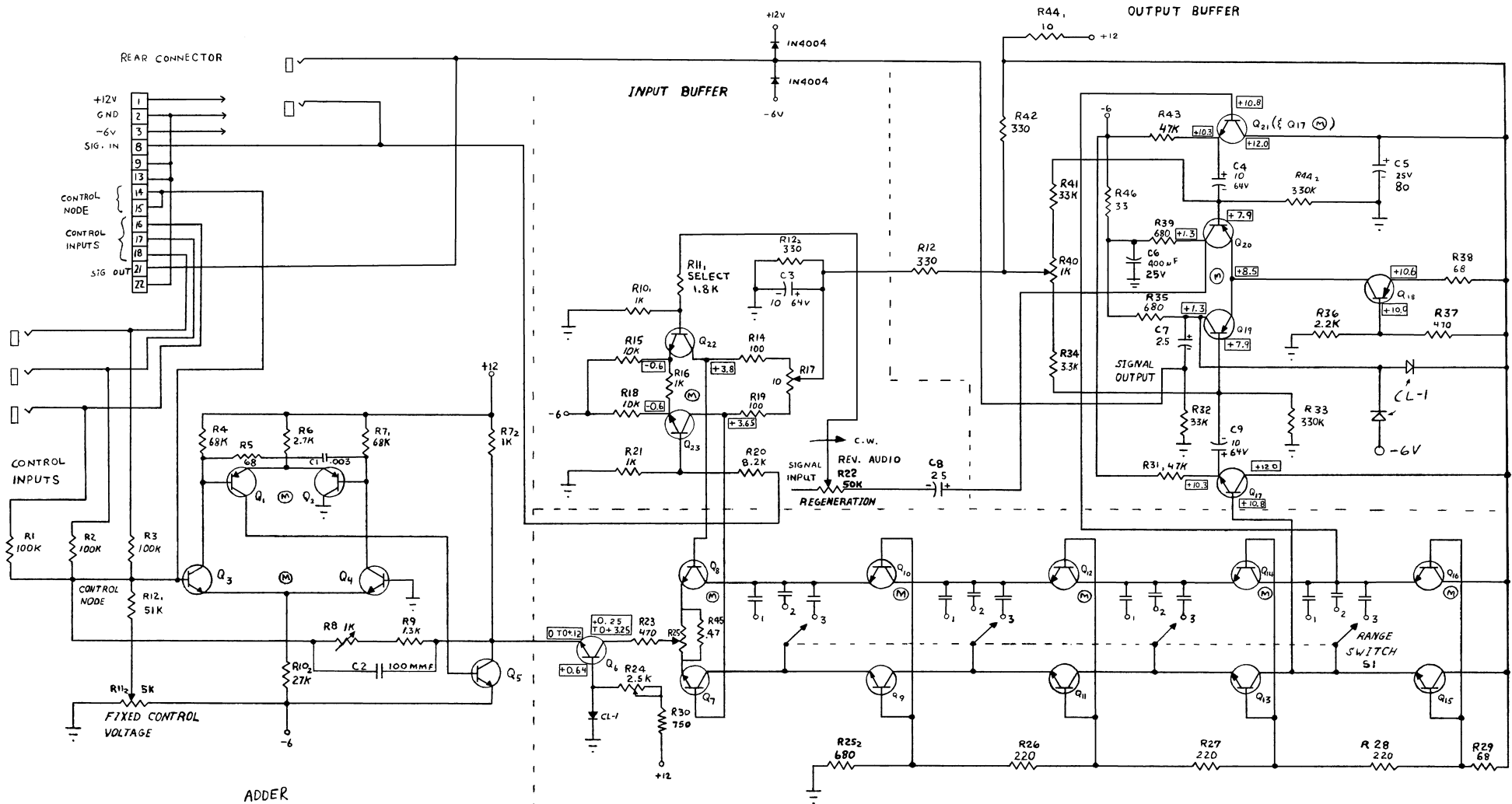
FIGURE 9 VOLTAGE CONTROLLED AMPLIFIER MODEL 902



REVISIONS		R. A. MOOG CO.	
		TRUMANSBURG, NEW YORK	
		TITLE 903A SCH. RANDOM SIGNAL SOURCE	
		SCALE	DR. BY JLA
		DATE 8-1-69	CK'D. BY
		DWG. NO. 1335	

CONN.

A	1
B	2
C	3
	4
	5
	6
	7
	8
	9
	10
	11
	12
	13
	14
	15
	16
	17
	18
D	19
E	20
	21
	22



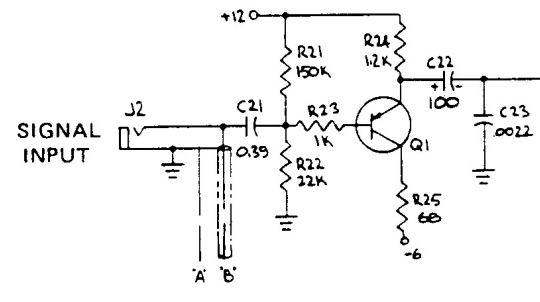
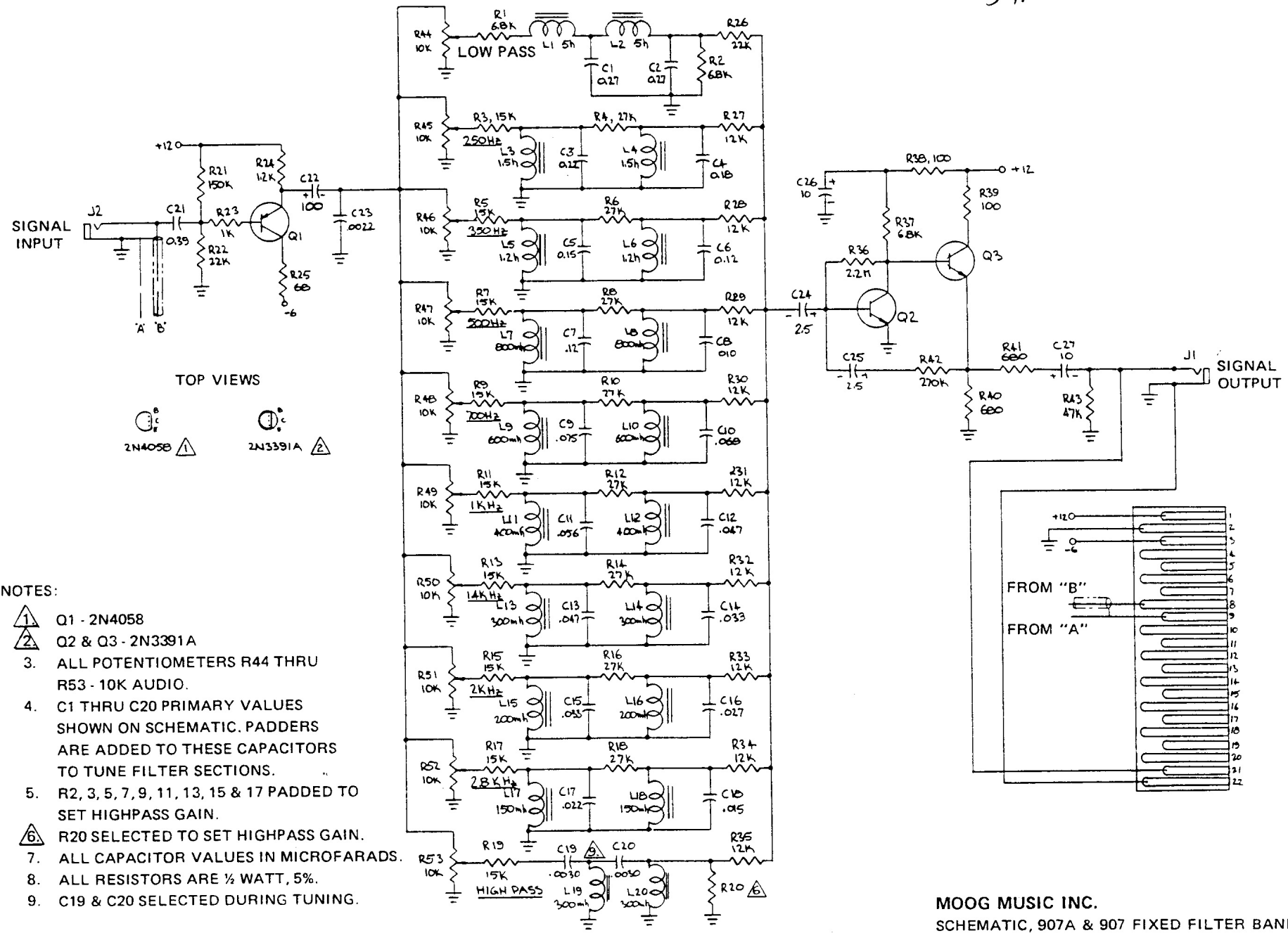
- NOTES:
1. ALL NPN TRANSISTORS: 2N 3392
 2. ALL PNP TRANSISTORS: 2N 4058
 3. (M) ⇒ MATCHED PAIR
 4. RANGE CAPACITOR SIZES

1	1.2 μ F
2	0.3 μ F
3	0.075 μ F

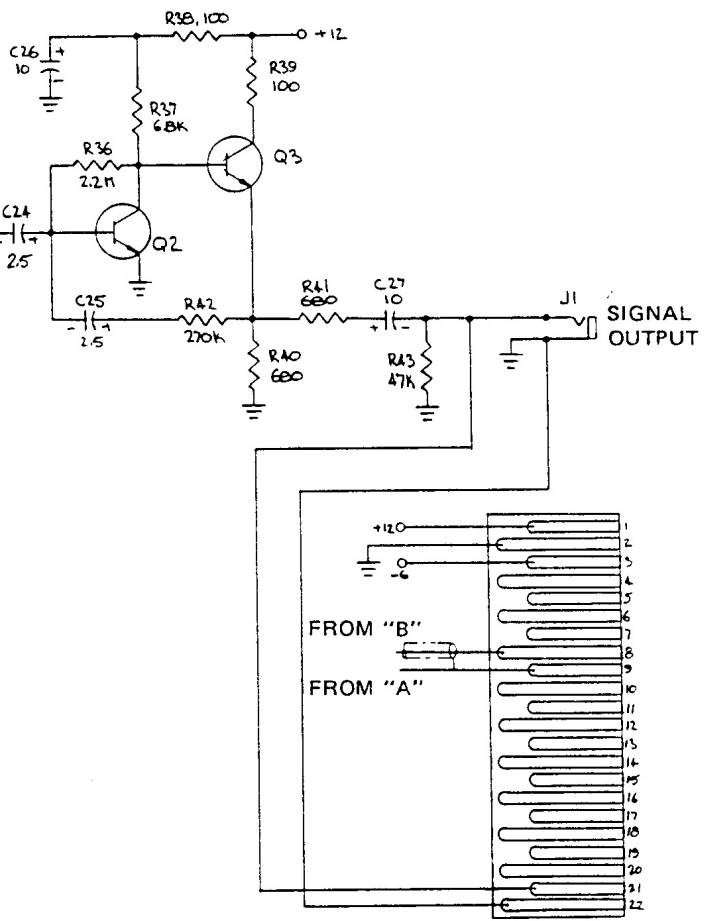
904-A VOLTAGE CONTROLLED LOW PASS FILTER	
DRAWN BY P.Y.	
APPR. BY	
DATE 7-25-67	DRAWING NUMBER SUPERCEDES NO. 1039
R. A. MOOG CO. TRAUMANSBURG, N. Y.	
REV. C 11/10/70 w/g.s.	REV. D
REV. A 9-8-69 - JLA	REV. B ~ECN-003

INDUCTORS 10mm x 5mm Body

SH

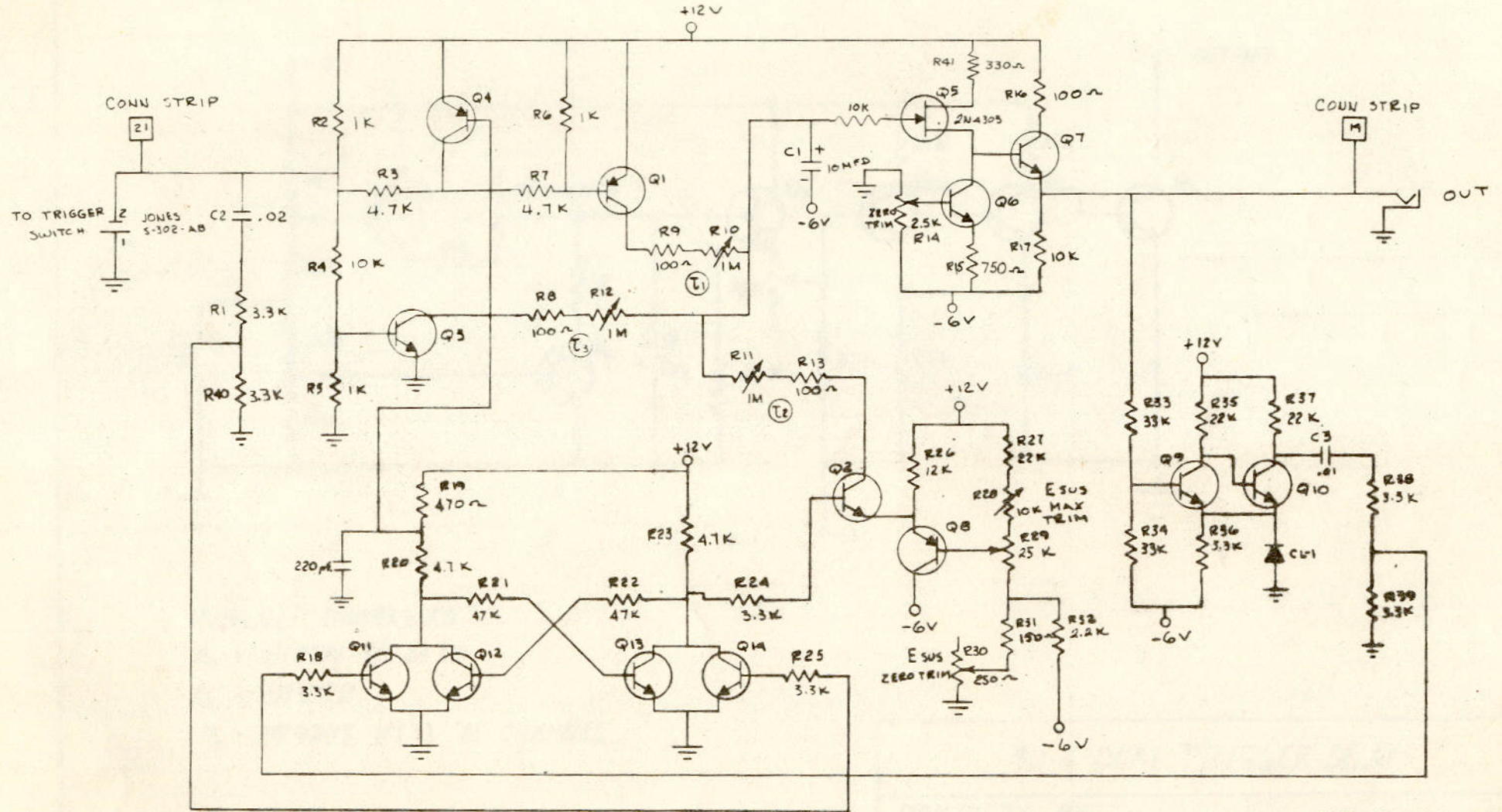


- NOTES:
- 1. Q1 - 2N4058
 - 2. Q2 & Q3 - 2N3391A
 - 3. ALL POTENTIOMETERS R44 THRU R53 - 10K AUDIO.
 - 4. C1 THRU C20 PRIMARY VALUES SHOWN ON SCHEMATIC. PADDERS ARE ADDED TO THESE CAPACITORS TO TUNE FILTER SECTIONS.
 - 5. R2, 3, 5, 7, 9, 11, 13, 15 & 17 PADDED TO SET HIGHPASS GAIN.
 - 6. R20 SELECTED TO SET HIGHPASS GAIN.
 - 7. ALL CAPACITOR VALUES IN MICROFARADS.
 - 8. ALL RESISTORS ARE 1/2 WATT, 5%.
 - 9. C19 & C20 SELECTED DURING TUNING.



MOOG MUSIC INC.
SCHEMATIC, 907A & 907 FIXED FILTER BANK
993-041838 08-028

FIGURE 15 FIXED FILTER BANK MODELS 907 AND 907A

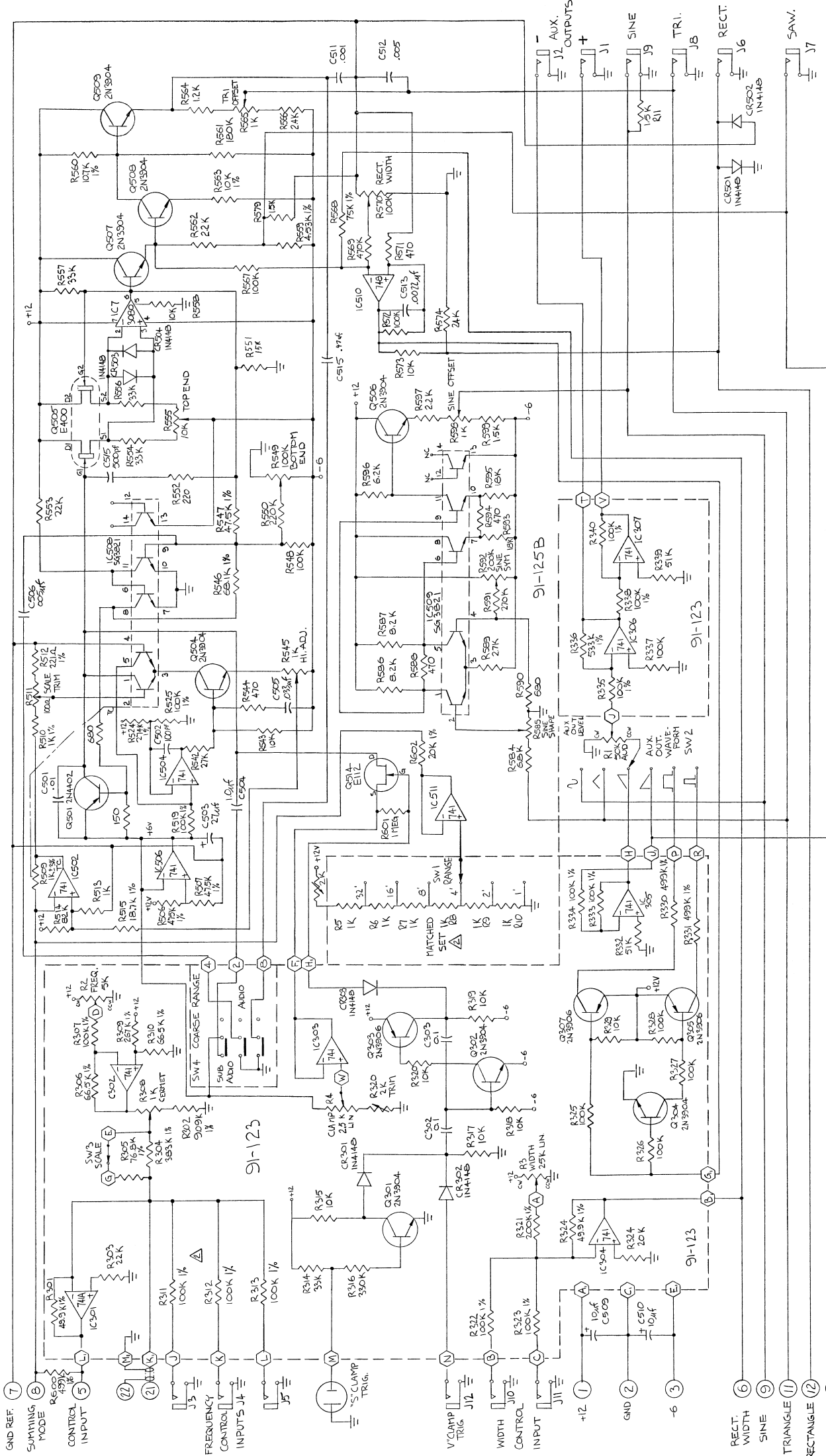


NOTES:

- I. ALL PNP TRANSISTORS ARE 2N4058
- II. ALL NPN TRANSISTORS ARE 2N3392

REPLACES DWG. 1103

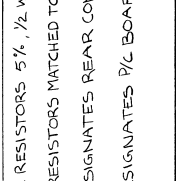
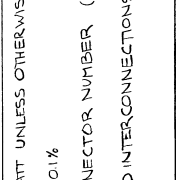
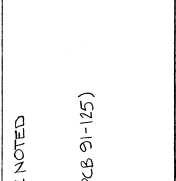
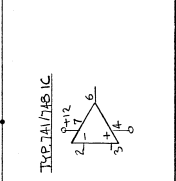
WAR C-ECN-004 1-12-69 318	REVISIONS	R. A. MOOG CO. TRUMANSBURG, NEW YORK	
	A-R15 FROM 1.5K	TITLE	911 ENVELOPE GENERATOR
	To R20	SCALE	DR. BY RER
	A-R41 FROM Q5 To R12V	DATE 8-14-68 CK'D. BY	DWG. NO.
B-ECN-002			



ITEM	PART NUMBER	DESCRIPTION	MATERIAL
1	Q501	741	IC501
2	Q502	2N3904	Q502
3	Q503	2N3904	Q503
4	Q504	2N3904	Q504
5	Q505	2N3904	Q505
6	Q506	2N3904	Q506
7	Q507	2N3904	Q507
8	Q508	2N3904	Q508
9	Q509	2N3904	Q509
10	IC501	741	IC501
11	IC502	741	IC502
12	IC503	741	IC503
13	IC504	741	IC504
14	IC505	741	IC505
15	IC506	741	IC506
16	IC507	741	IC507
17	IC508	741	IC508
18	IC509	741	IC509
19	IC510	741	IC510

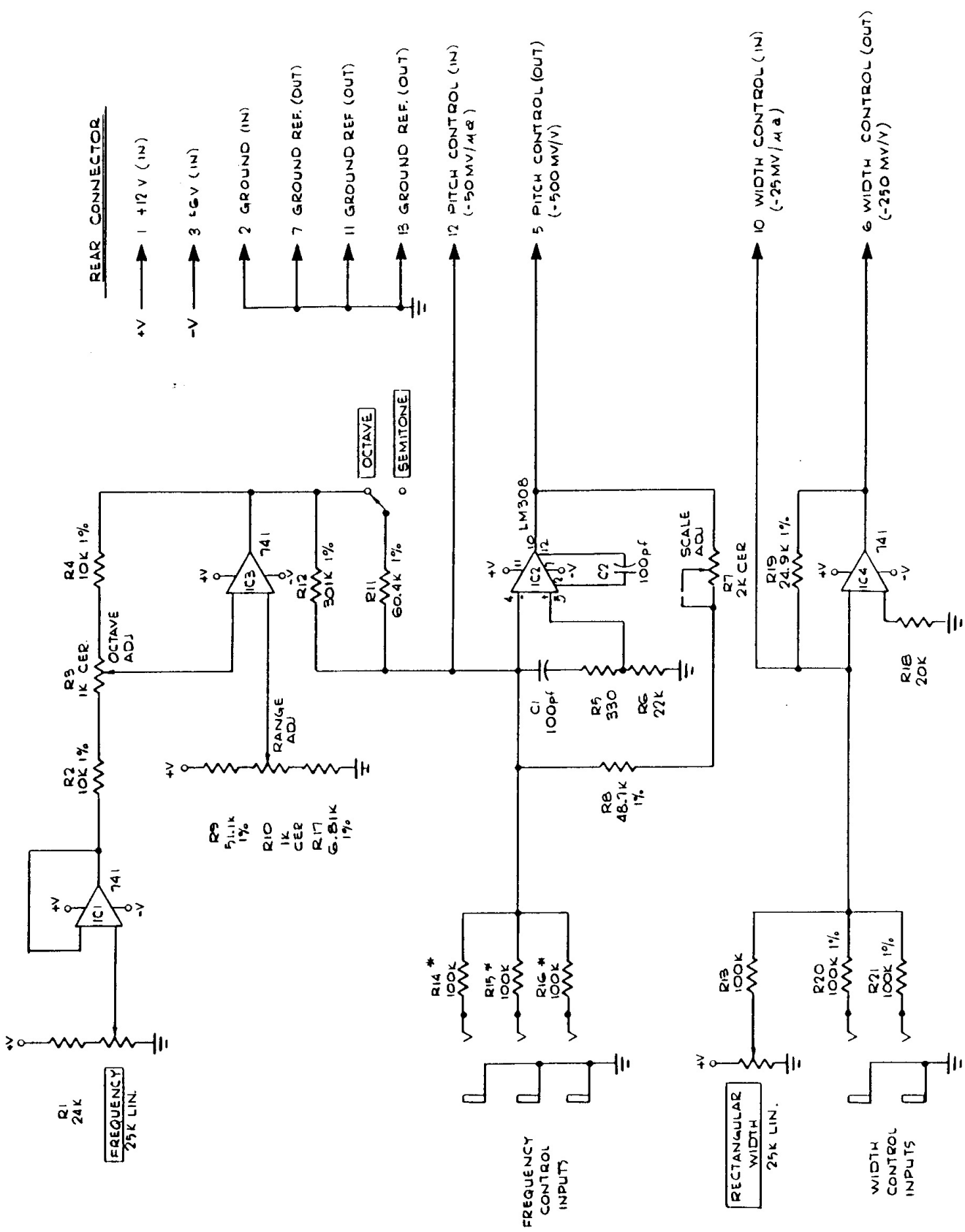
DRAWN BY JRB/7/74
 CHECK
 GRP ENGR
 REVIEW OC
 SUPERVISOR
 SIZE CODE IDENT
 NEXT ASSY MODEL NO. 921
 APPLICATION SCALE 1:1
 WT. 08-036
 SHEET 1 OF 1

COMPONENT DESIGNATORS
 NO PREFIX → FRONT PANEL
 3XX → PCB ASSY
 5XX, 6XX → 921-123 PCB ASSY



NOTES:

- △ ALL RESISTORS 5%, 1/2 WATT UNLESS OTHERWISE NOTED
- △ 1% RESISTORS MATCHED TO 0.1%
- ⊗ DESIGNATES REAR CONNECTOR NUMBER (PCB 91-125)
- ⊗ DESIGNATES P/C BOARD INTERCONNECTIONS



NOTES:

1. UNLESS OTHERWISE SPECIFIED ALL RESISTORS ARE ± 5%, 1/2 W.
2. ALL 1% RESISTORS ARE 1/4 W.
3. * MATCHED TO 0.1%

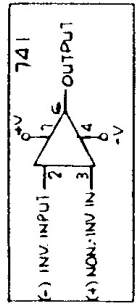
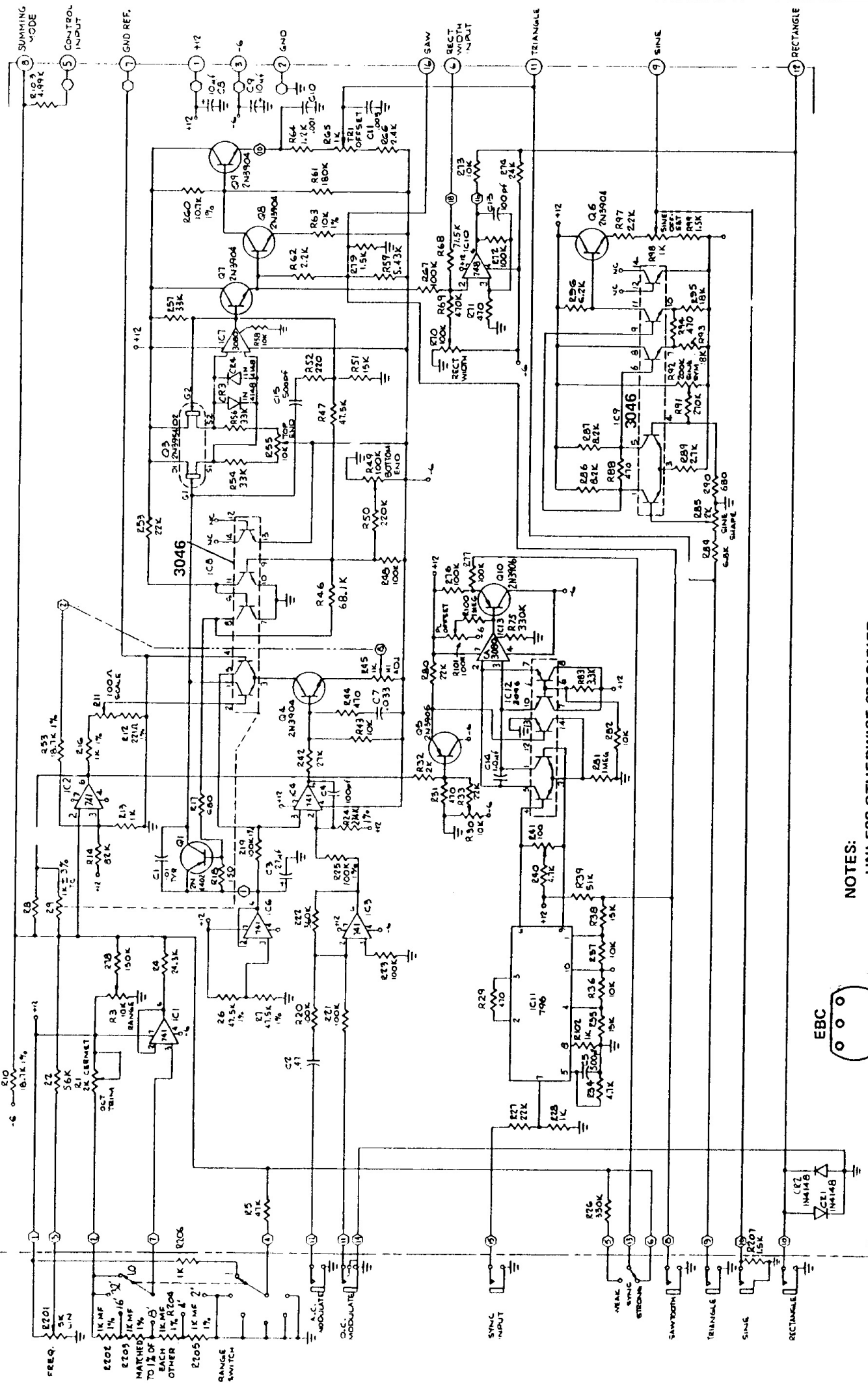
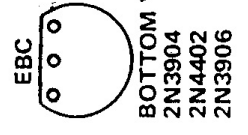


FIGURE 22. OSCILLATOR DRIVER MODEL 921A

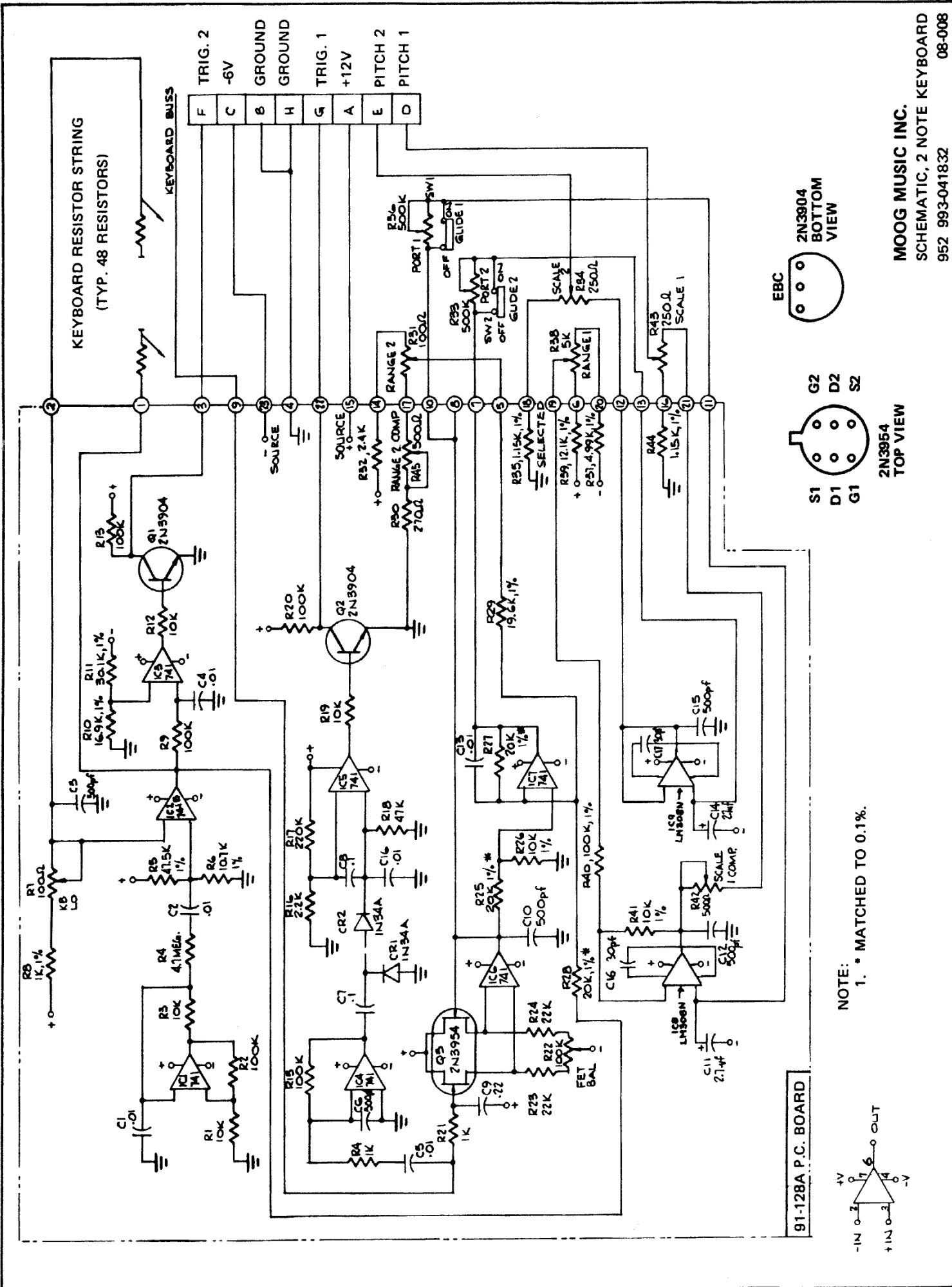


- NOTES:
1. ALL RESISTOR VALUES IN OHMS, 1/4 OR 1/2W
 2. ALL CAPACITOR VALUES IN MFD.
 3. ○ DESIGNATES PLUG PIN
 4. ○ DESIGNATES REAR CONNECTOR FINGER

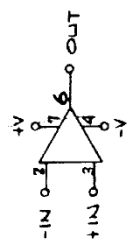


MOOG MUSIC INC.
 SCHEMATIC, OSCILLATOR 921B
 993-041875 08-013

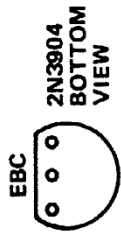
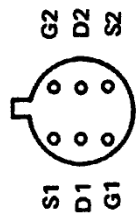
FIGURE 23. OSCILLATOR MODEL 921B



91-128A P.C. BOARD



NOTE:
1. * MATCHED TO 0.1%.



MOOG MUSIC INC.
SCHEMATIC, 2 NOTE KEYBOARD
952 993-041832 08-008

FIGURE 29 TWO NOTE KEYBOARD MODEL 952