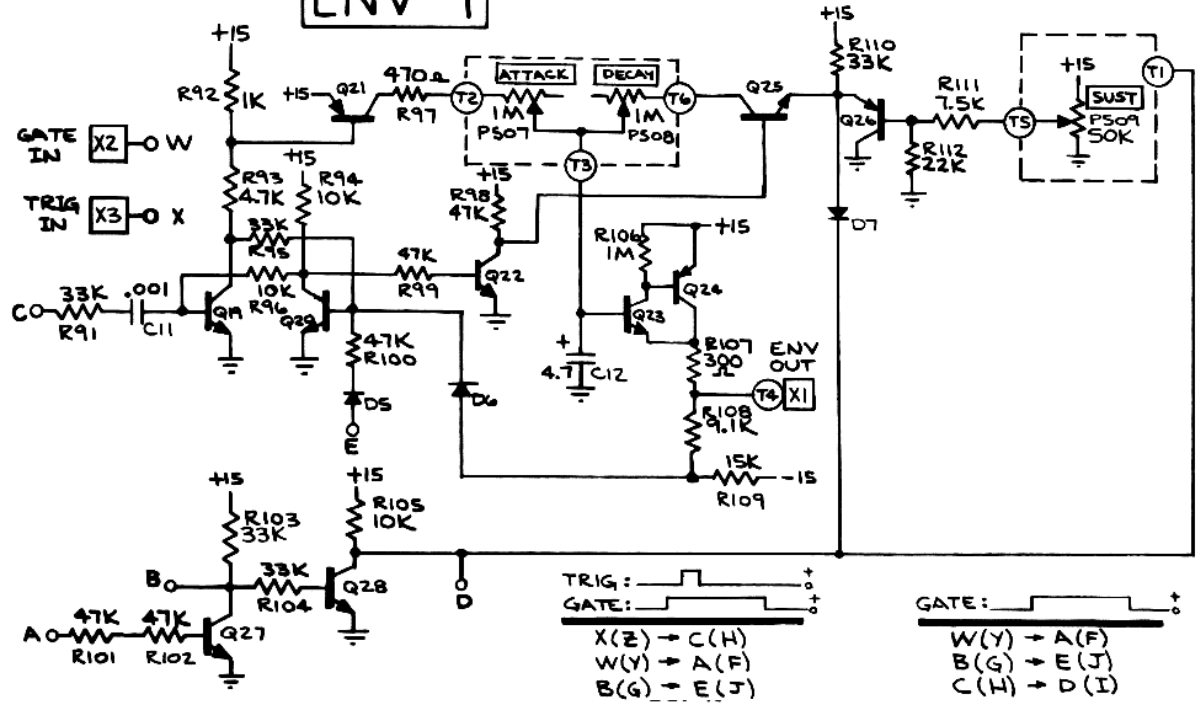


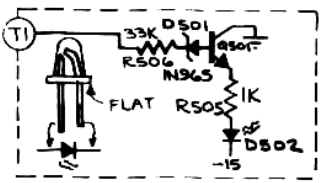
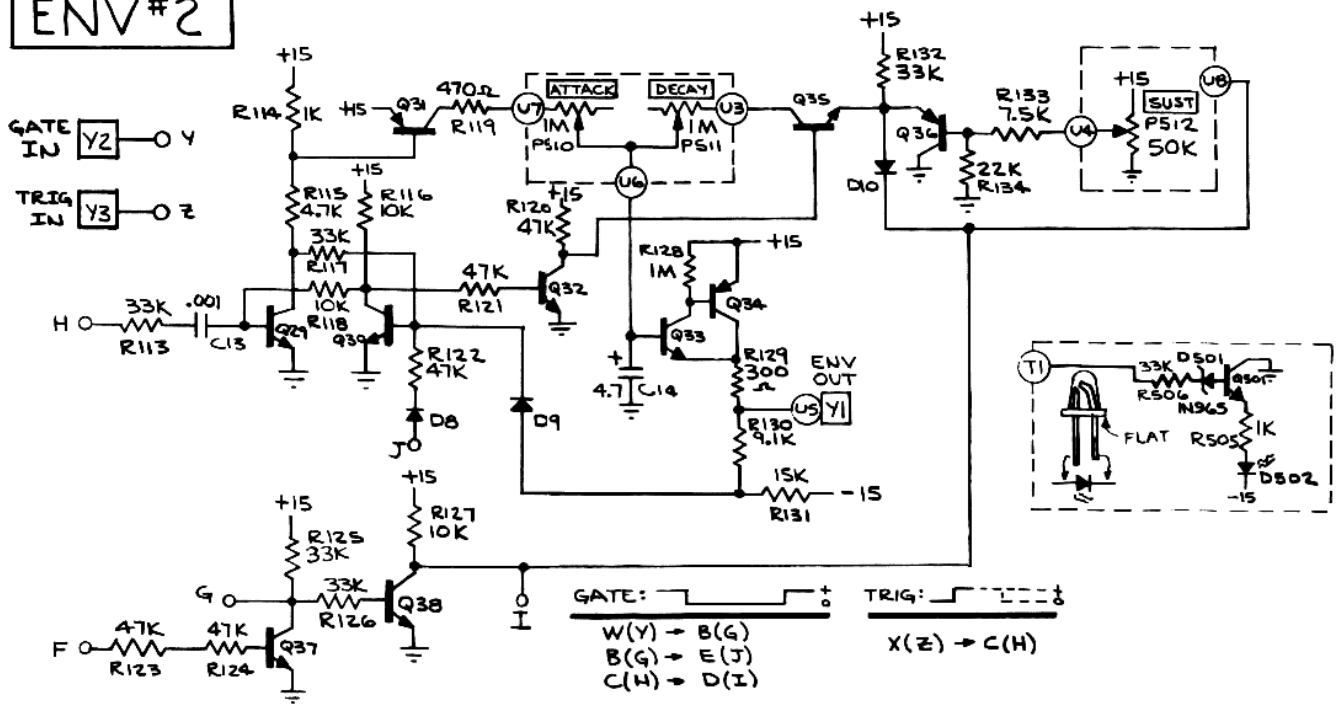
NOTES :

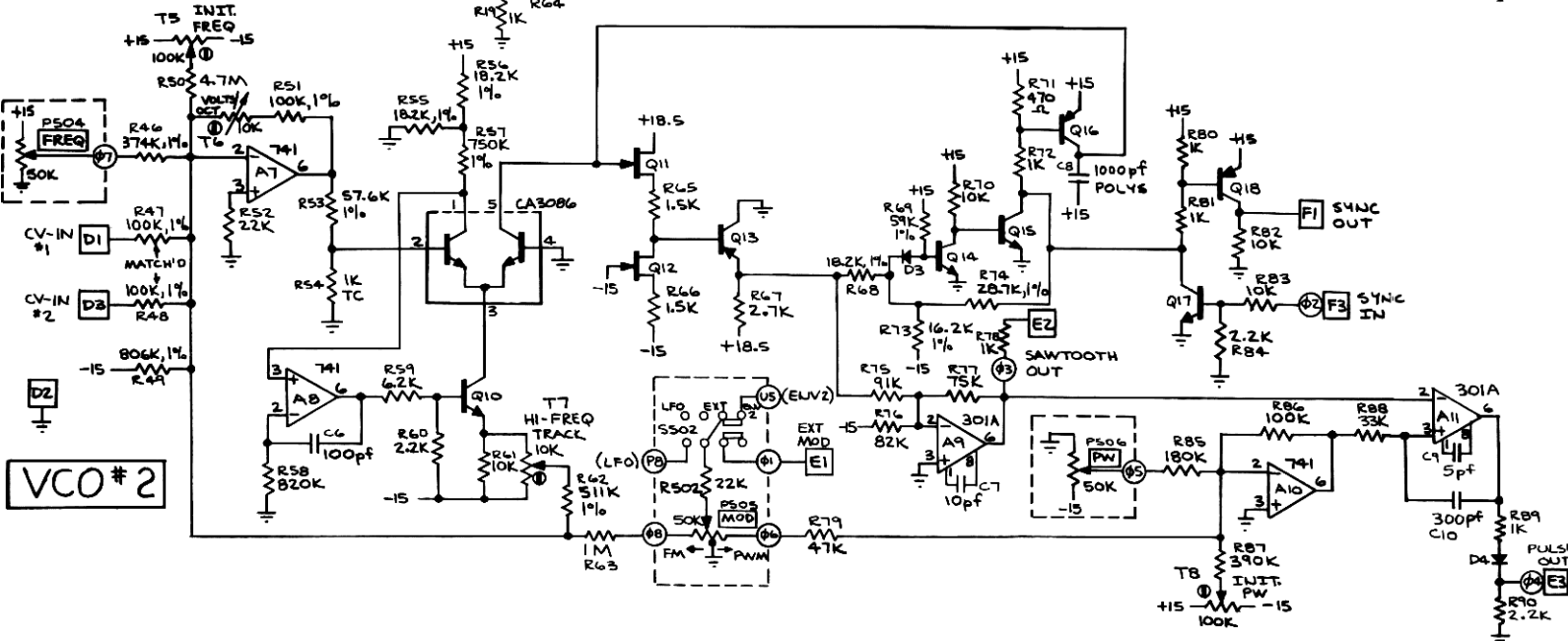
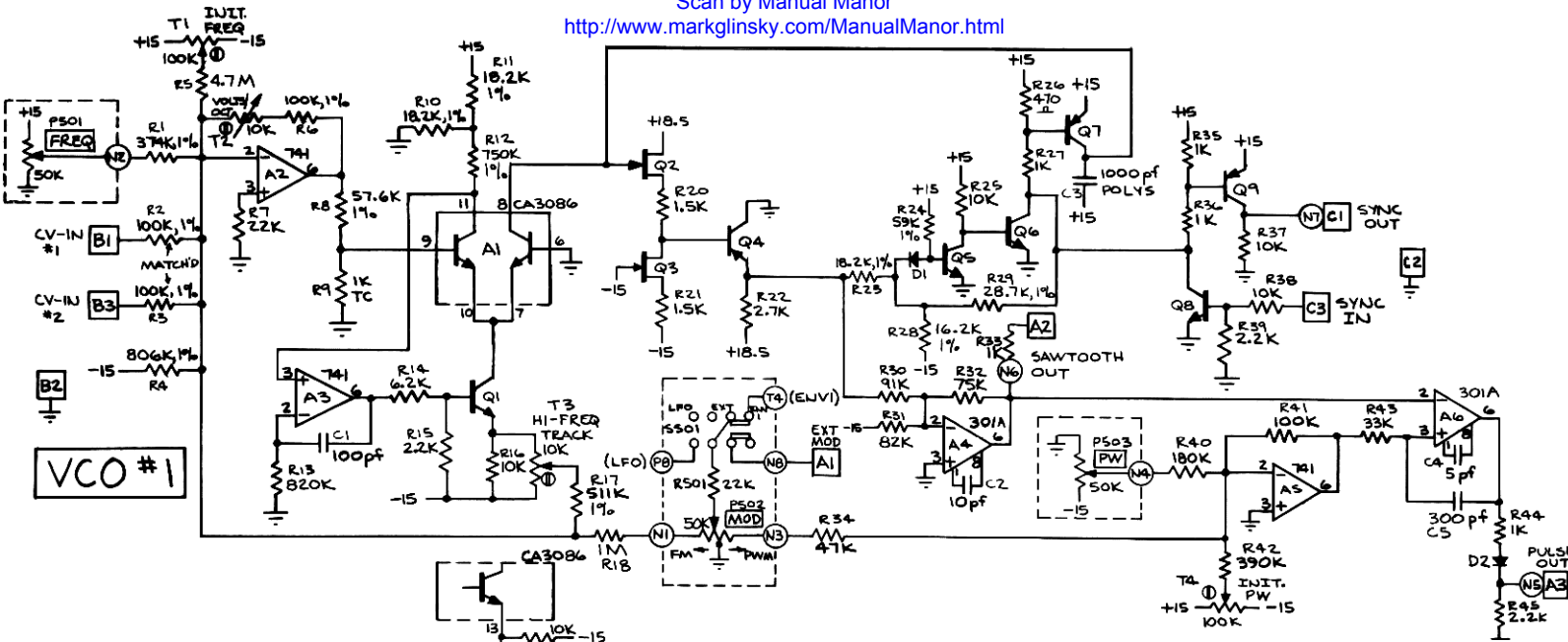
1. ALL "N" TRANSISTORS - 2N5172 (485003)
2. ALL "P" TRANSISTORS - 2N3905 (486002)
3. ALL "F" TRANSISTORS - 2N4302
4. ALL DIODES - 1N4148 (481001)
5. * - SELECTED 3080 (313201)
6. MOUNT ALL TRIMMERS OFF BOARD (1/16")

ENV #1

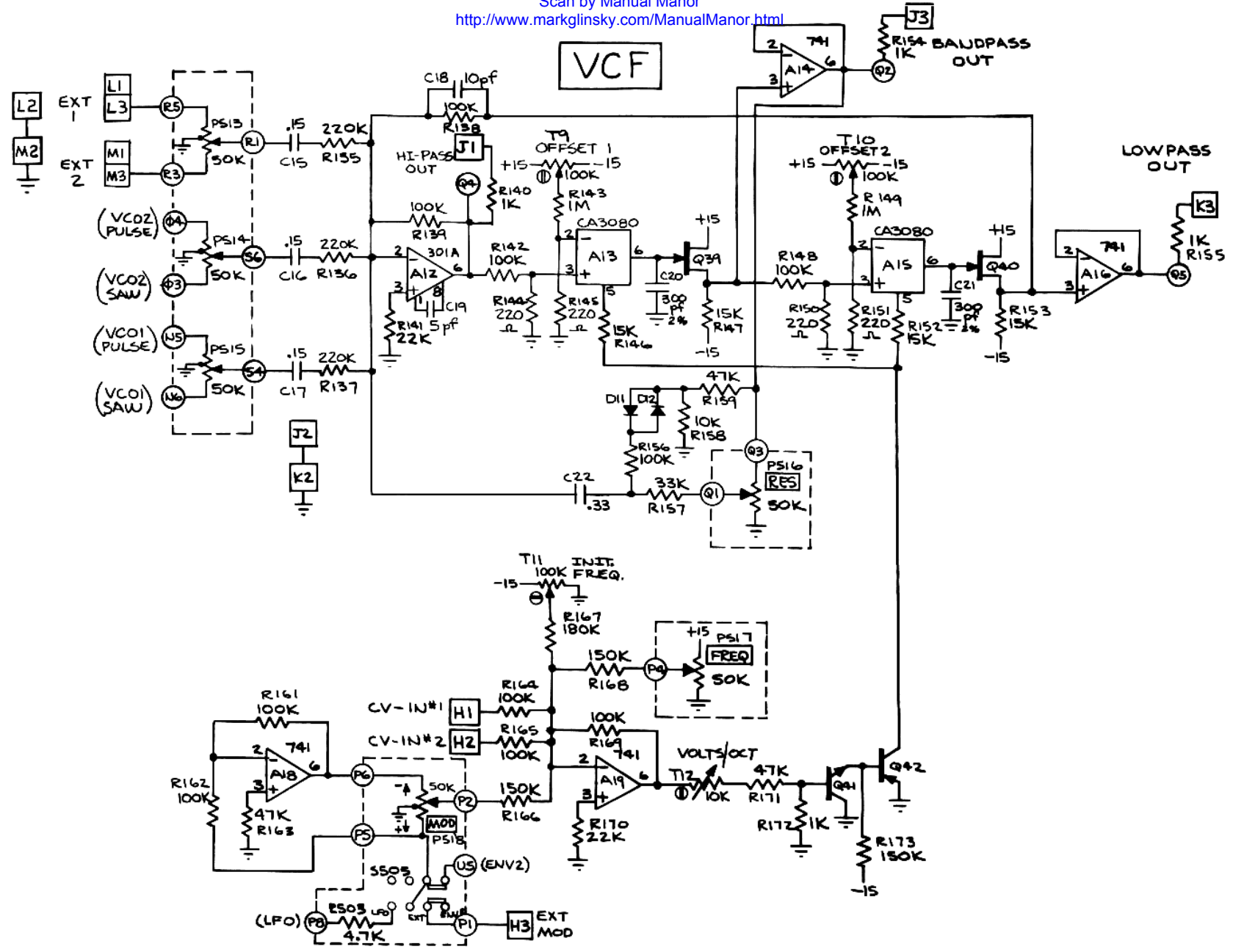


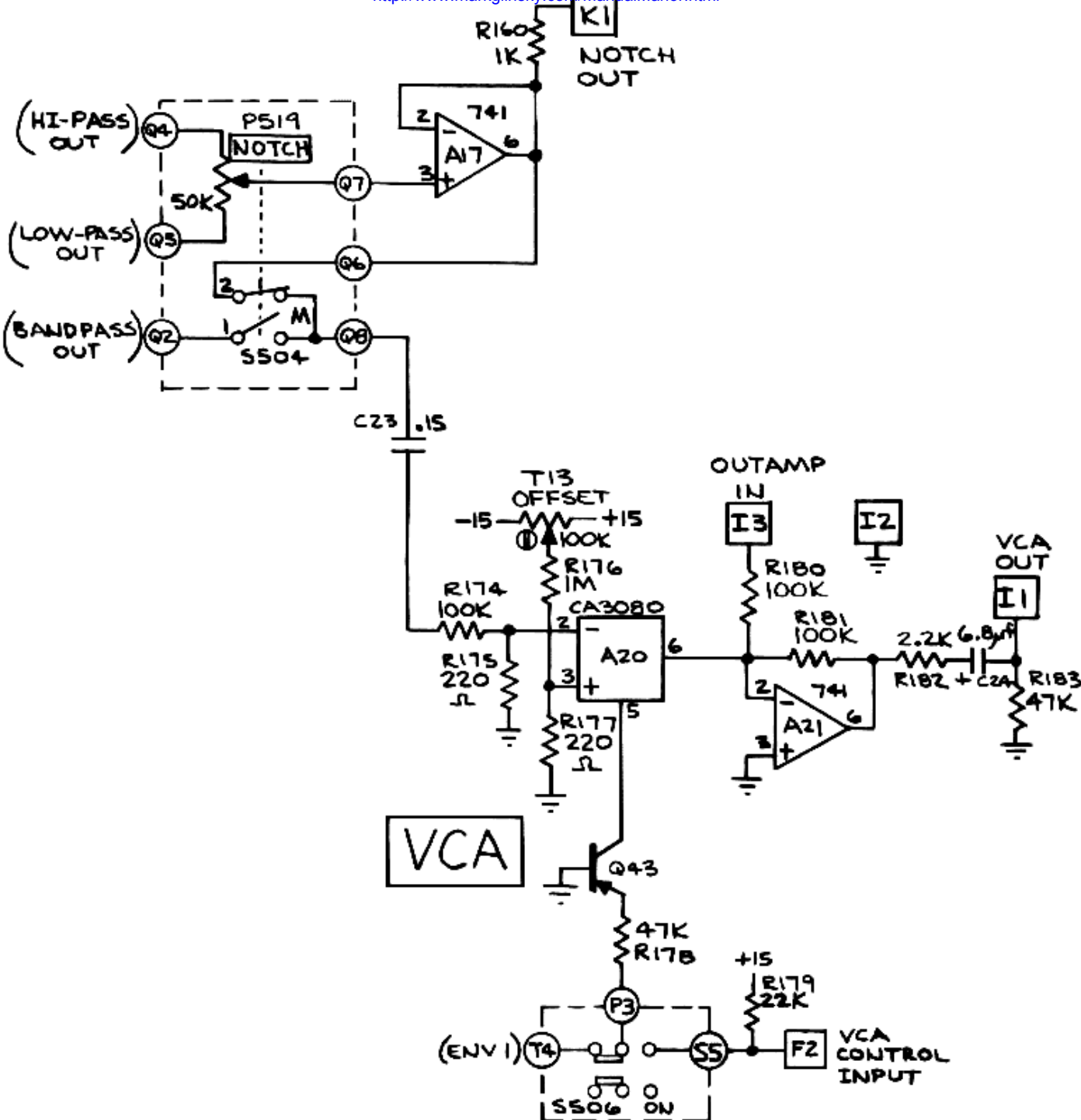
ENV #2





VCF



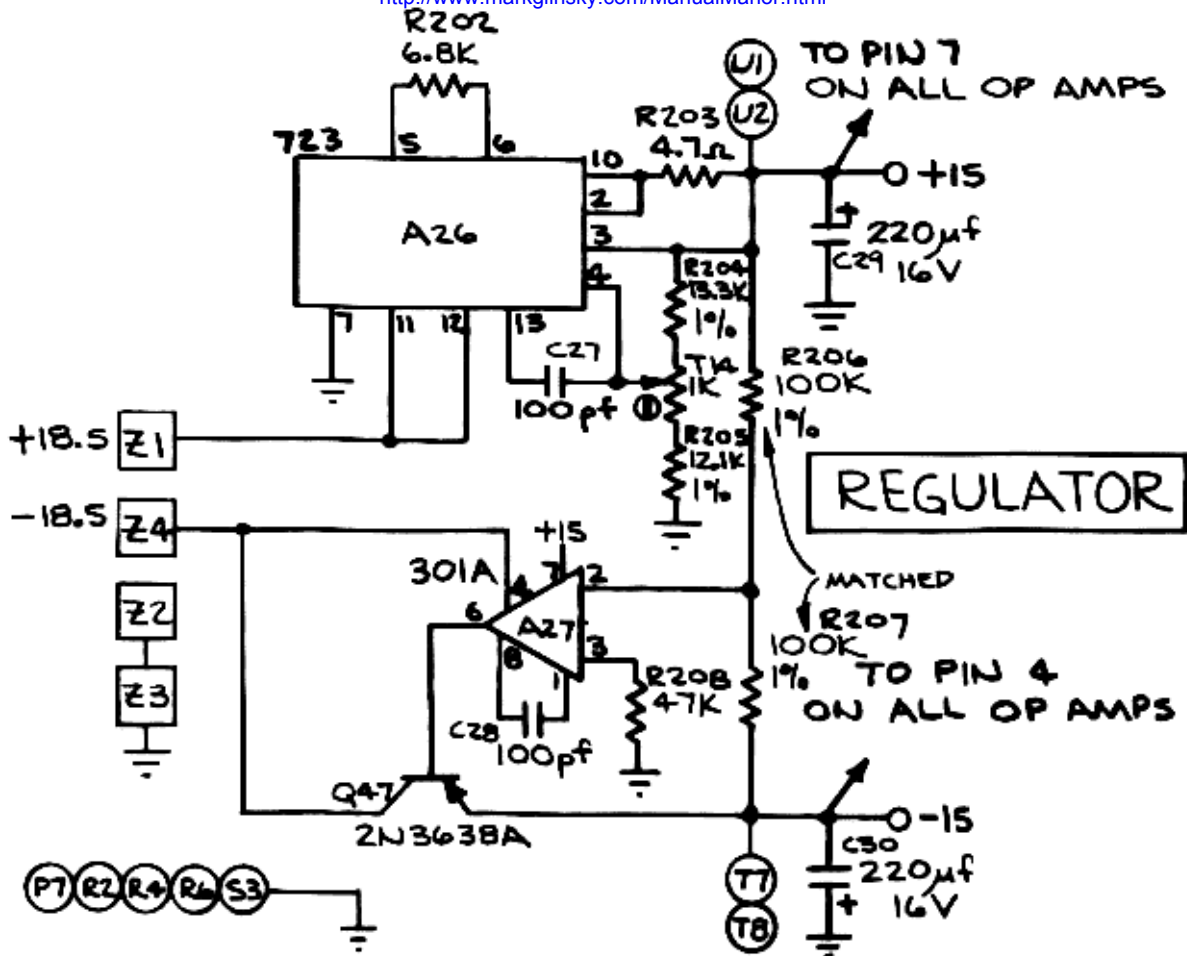


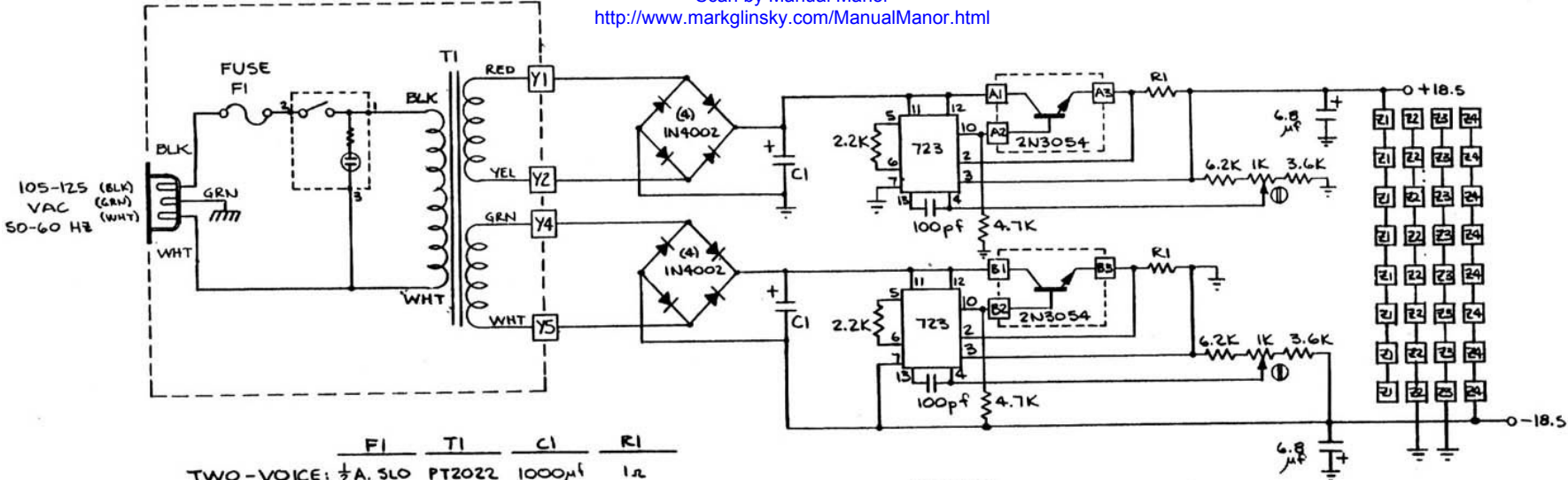
NOTES:

(UNLESS SPECIFIED OTHERWISE)

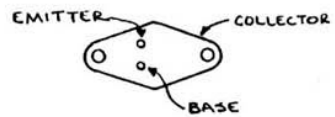
1. ALL RESISTORS ARE $\frac{1}{4}$ W, 5%
2. ALL CAPACITORS IN μ f.
3. ALL NPN TRANSISTORS - 2N5172
4. ALL PNP TRANSISTORS - 2N3905
5. ALL DIODES - 1N4148
6. ALL FET'S - 2N4302

<u>LAST</u>	<u>LAST (POT BD)</u>
R208	R506
T14	P520
C30	
D12	D502
Q47	Q501
A27	S506

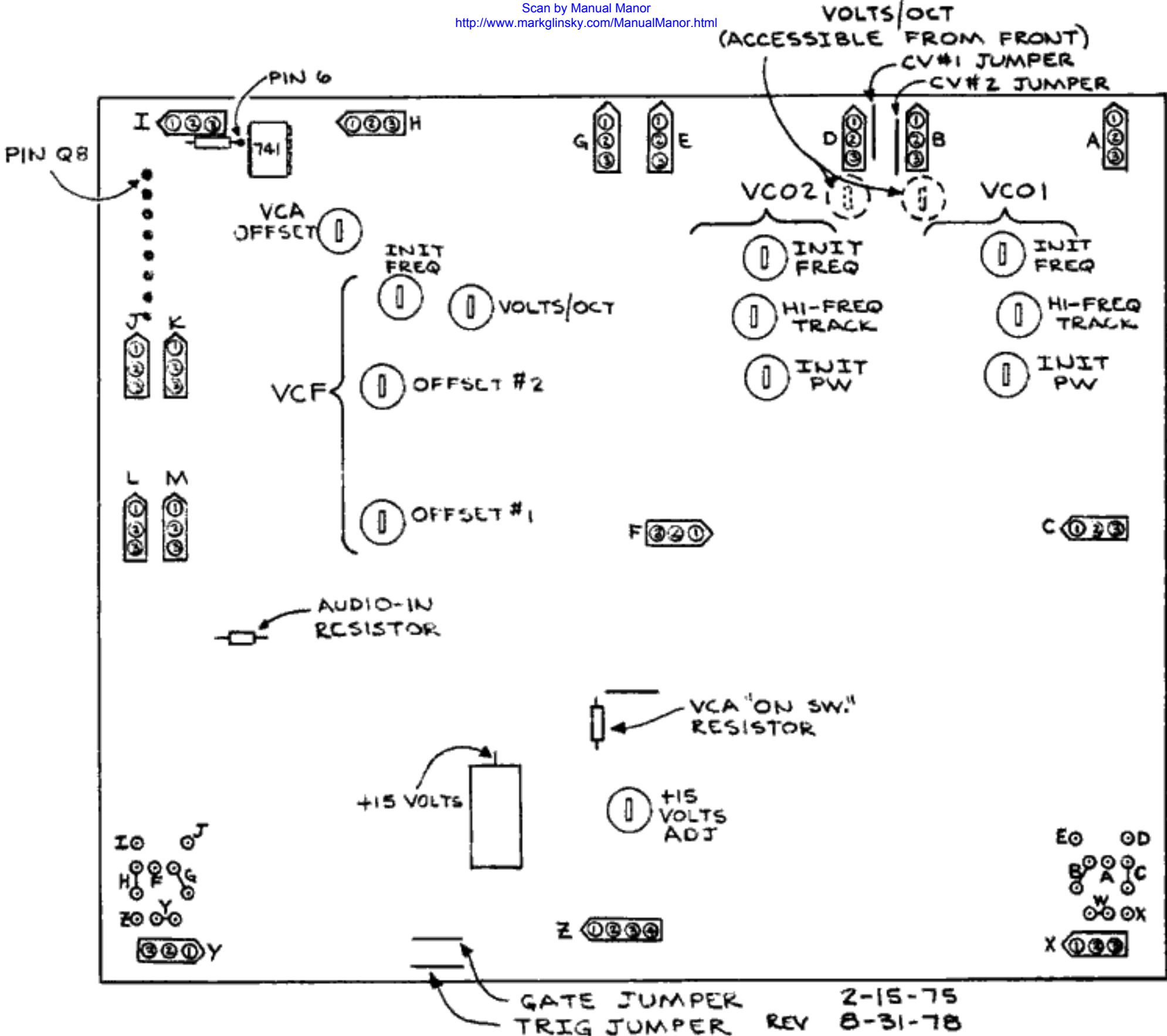




	F1	T1	C1	R1
TWO-VOICE:	1/2 A. SLO	PT2022	1000µf	1Ω
FOUR-VOICE:	1 A. SLO	PT2054	2000µf	.5Ω



OBERHEIM ELECTRONICS, INC
 - SCHEMATIC -
 SYNTHESIZER
 POWER SUPPLY
 NOV. 11, 1975
 REV 5-30-78



SEM-1A ELECTRONICS BOARD

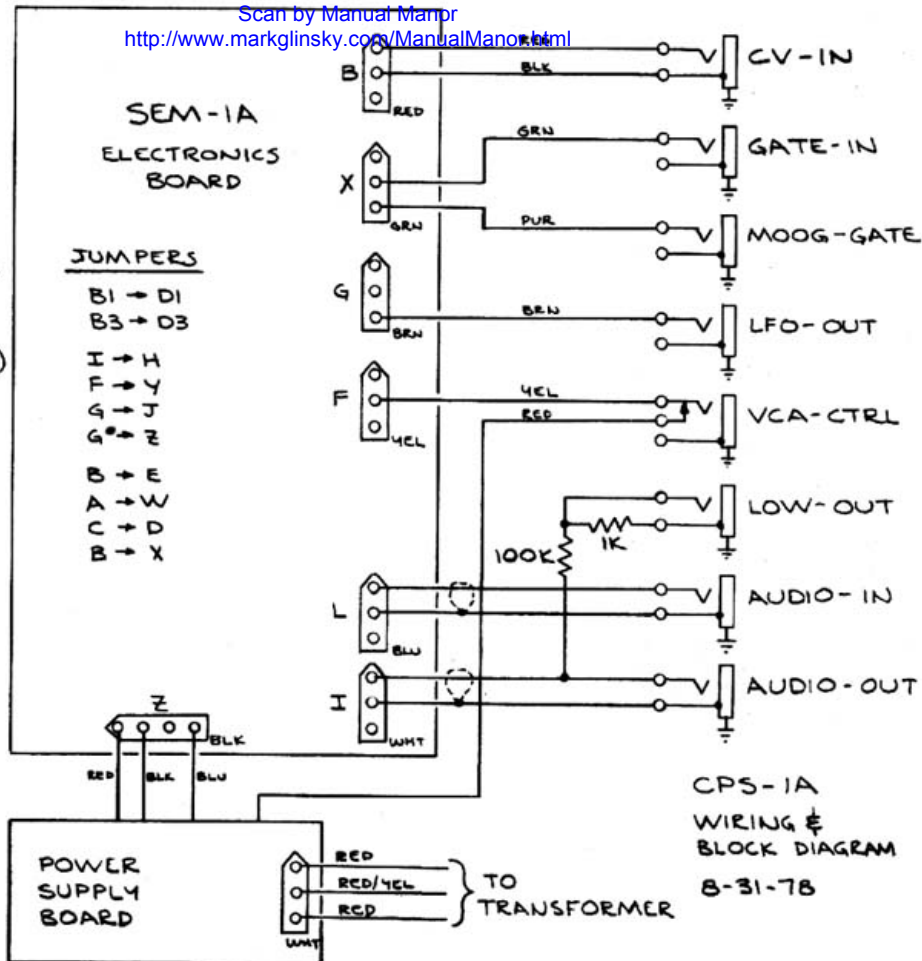
JUMPERS

B1 → D1
 B3 → D3
 I → H
 F → Y
 G → J
 G⁰ → Z
 B → E
 A → W
 C → D
 B → X

CHANGES
TO SEM-1A
ELECTRONICS
BOARD WHEN
USED IN CPS-1A

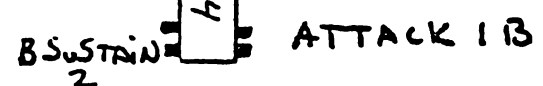
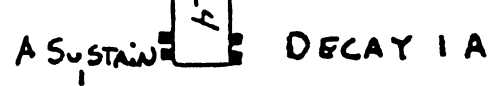
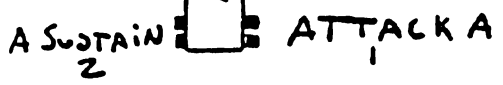
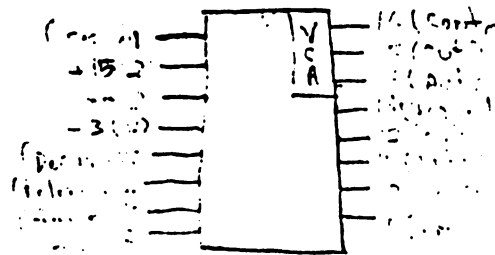
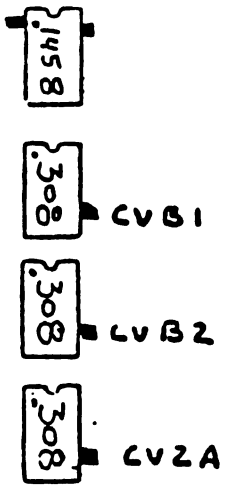
1) VCA "ON-SW"
RESISTOR (22K)
IS REMOVED.

2) AUDIO-IN
SUMMING
RESISTOR
(R155, 220K)
IS PARALLELED
WITH 47K.



CPS-1A
WIRING &
BLOCK DIAGRAM
B-31-78

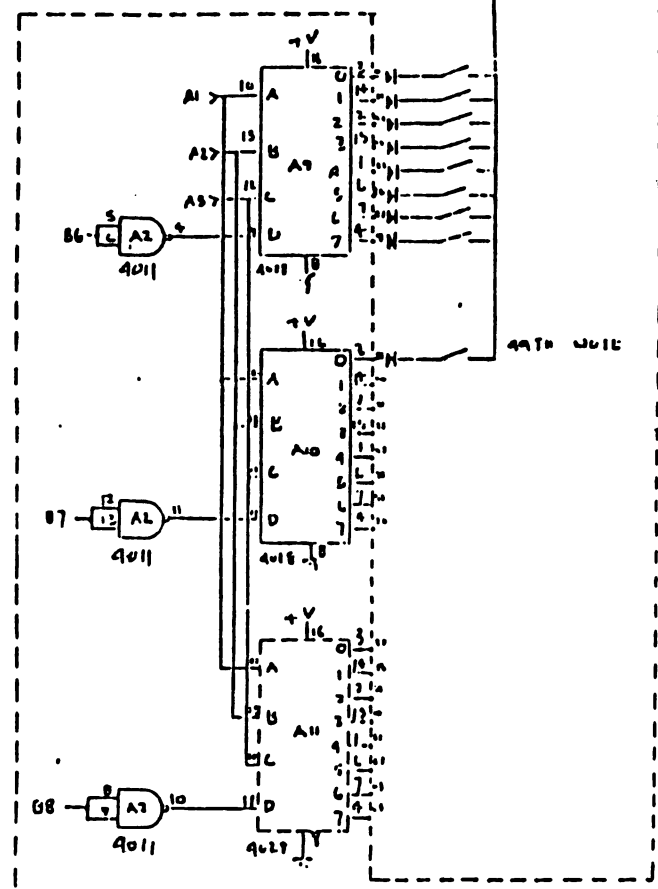
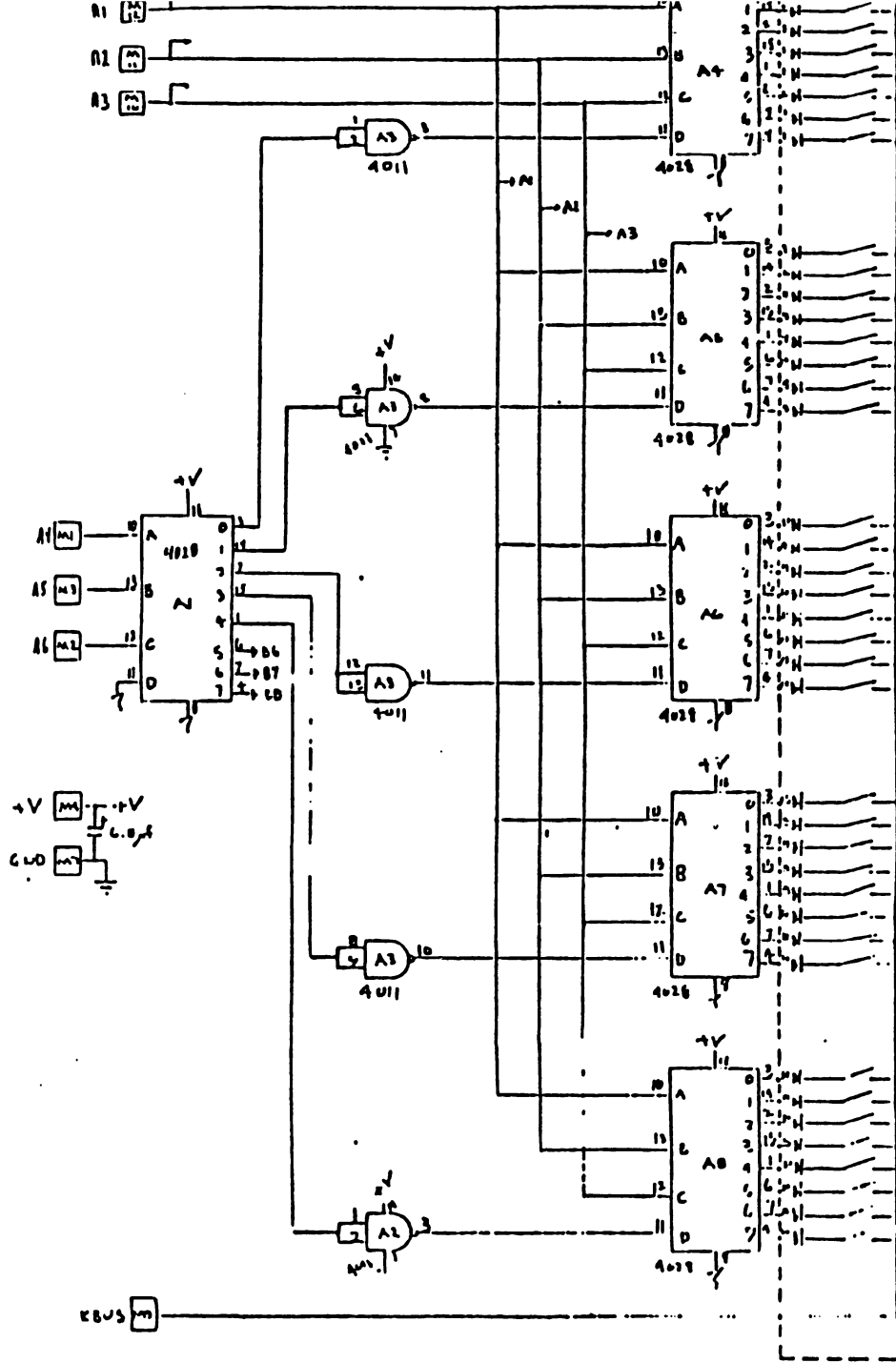
MC14028
 5-5-2



PSP SiH 12/163
 PIN OUT
 6-5-78 T.S.

LEN VCF
 SUSTAIN VCF
 SUSTAIN VCF
 SUSTAIN VCF

FV



OBERLINIA ELECTRONICS, INC.
 LOGIC DIAGRAM -
 POLY TECH NUMBER
 NOV. 11, 1975
 1126

FOUR VOICE INTER-CONNECT CABLE

(12 PIN MOLEX)

<u>FUNCTION</u>	<u>PIN</u>
CV #1	1
GATE #1	2
CV #2	3
GATE #2	4
CV #3	5
GATE #3	6
CV #4	7
GATE #4	8
GND	9
GND	10
VCO TUNE	11
VCF TUNE	12

① KB MODULE

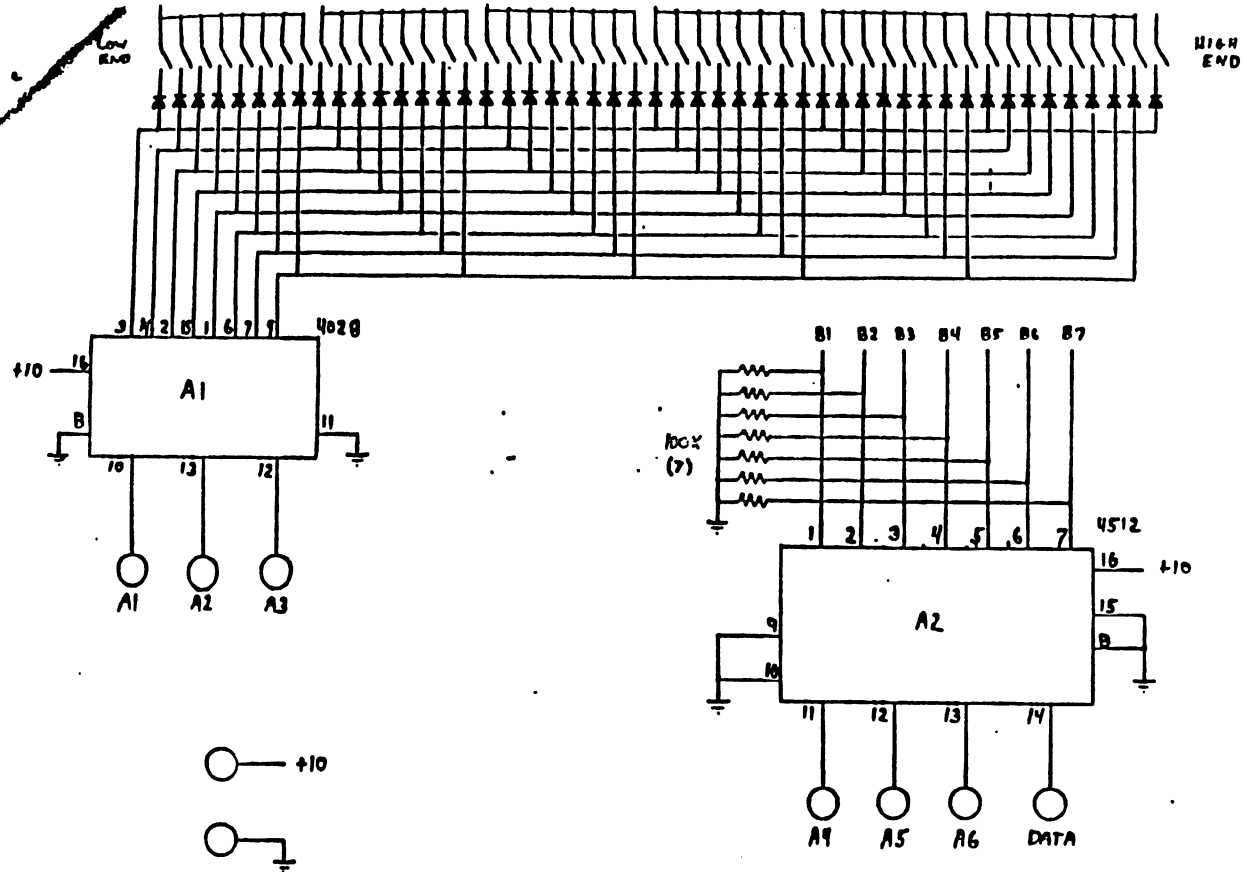
SET DAC REF TO 11.02 VOLTS
PIN 14 OF 4041 (V14-V15) [P00]
SET BY 723 ON OVERHEAD BOARD
SIDE MULTI-TURN

② SEM V/OCT WITH KB MODULE

IN MIDDLE TRANSPOSE -
HI NOTE INITIAL - KB CV ONLY

③ TRANSPOSE (KB MODULE) CAL.
MULTI TURN

④ PROGRAMMER MODULE V/SWA
VCO1:2 FREQ CONTROLS ONLY

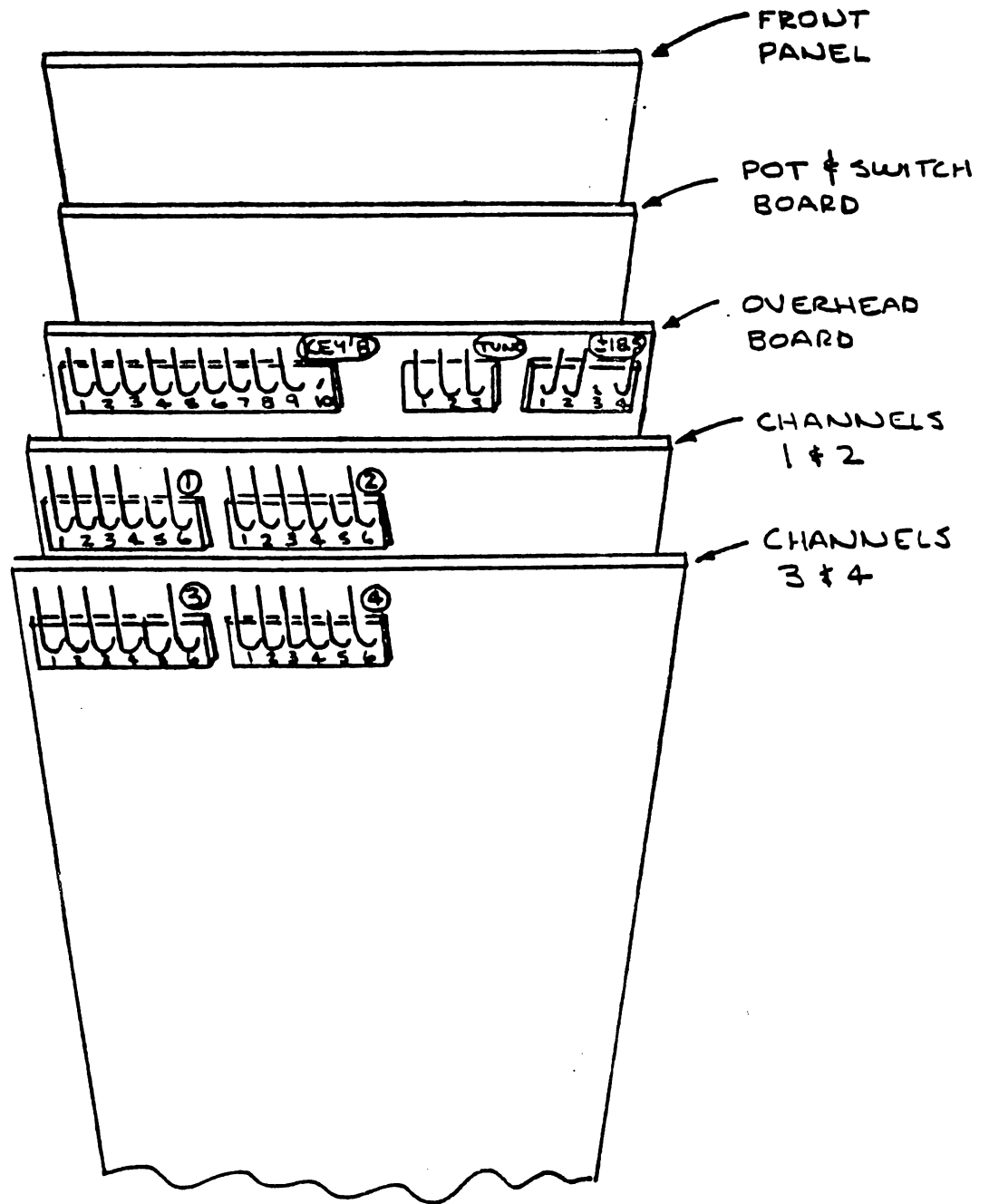


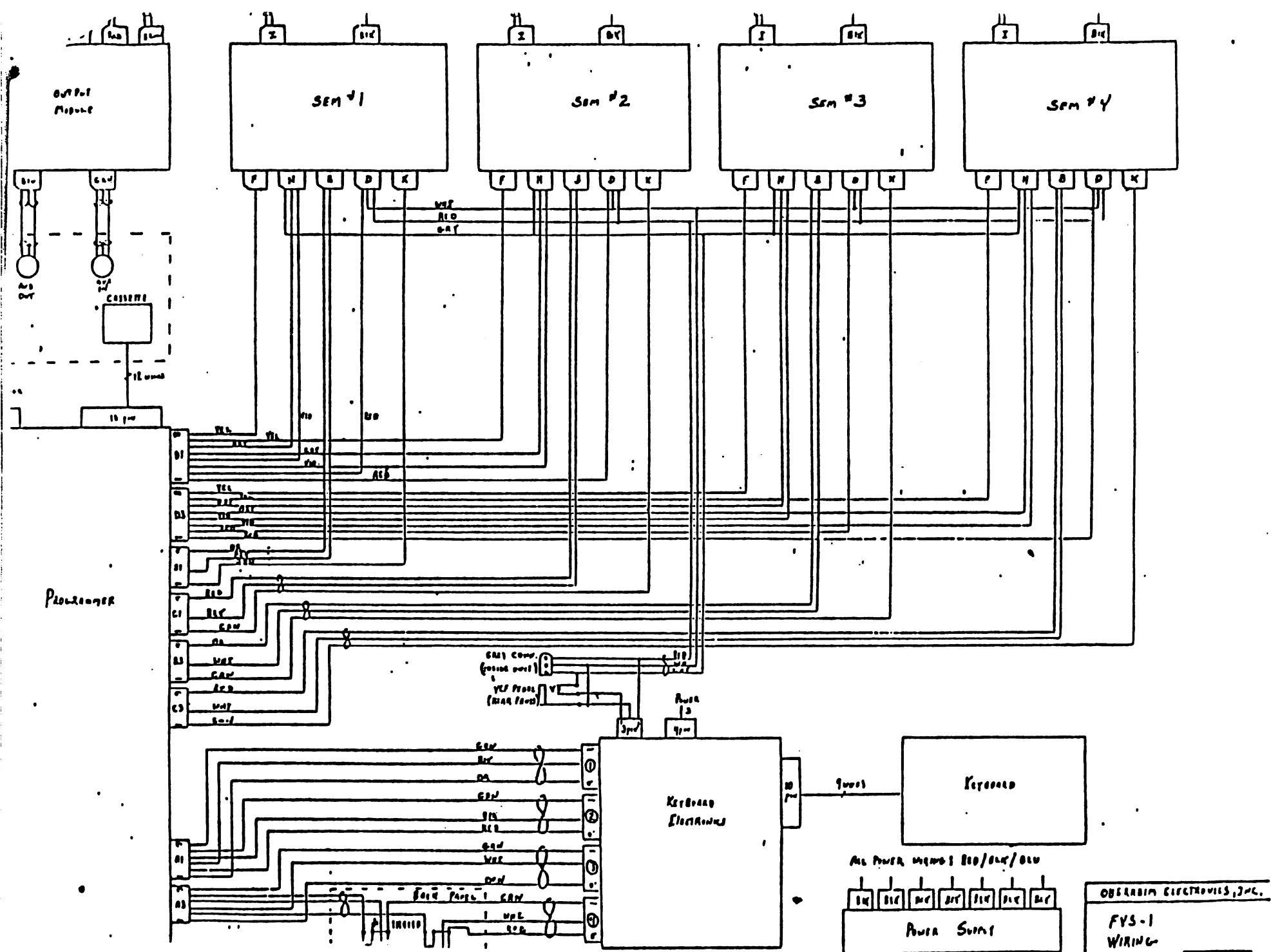
NOTES:

- 1) A1 AND A2 TO BE MOUNTED IN SOCKETS
- 2) ○ DENOTES SOLDERABLE PINS ON P.C BOARD
- 3) ALL RESISTORS 5% 1/4 OR 1/2 WATT

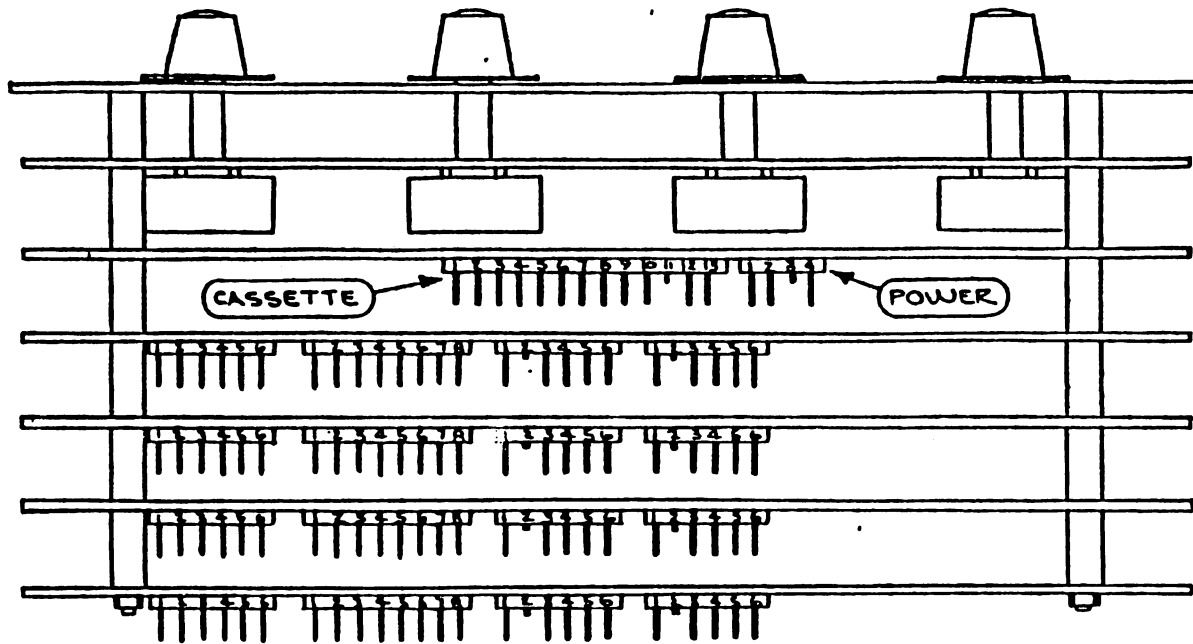
OBERHEIM ELECTRONICS, INC
 49 NOTE KEYBOARD
 WITH DECODER
 7/77

FOUR VOICE ELECTRONICS CONNECTIONS





TOP VIEW



FRONT PANEL

POT BOARD

OVERHEAD BOARD

CHANNEL BOARD #1 (CHANNELS 1 & 2)

CHANNEL BOARD #2 (CHANNELS 3 & 4)

CHANNEL BOARD #3 (CHANNELS 5 & 6)

CHANNEL BOARD #4 (CHANNELS 7 & 8)

"A"
CONN.

"D"
CONN.

"B"
CONN.

"C"
CONN.

A CONNECTOR

PIN	FUNCTION
1	KEY'B CV 'A' (CH'S 1,3,5,7)
2	KEY'B CV 'B' (CH'S 2,4,6,8)
3	GND
4	GND
5	KEY'B GATE 'B' (CH'S 2,4,6,8)
6	KEY'B GATE 'A' (CH'S 1,3,5,7)

D CONNECTOR

PIN	FUNCTION
1	VCO 2 CV 'B' (CH'S 2,4,6,8)
2	VCO 2 CV 'A' (CH'S 1,3,5,7)
3	VCF ENV 'B' (CH'S 2,4,6,8)
4	VCF ENV 'A' (CH'S 1,3,5,7)
5	VCF CV 'B' (CH'S 2,4,6,8)
6	VCF CV 'A' (CH'S 1,3,5,7)
7	VCA ENV 'B' (CH'S 2,4,6,8)
8	VCA ENV 'A' (CH'S 1,3,5,7)

B CONNECTOR

PIN	FUNCTION
1	GATE 'A' (CH'S 1,3,5,7)
2	KEY
3	GND
4	NOT USED
5	NOT USED
6	VCO 1 CV 'A' (CH'S 1,3,5,7)

C CONNECTOR

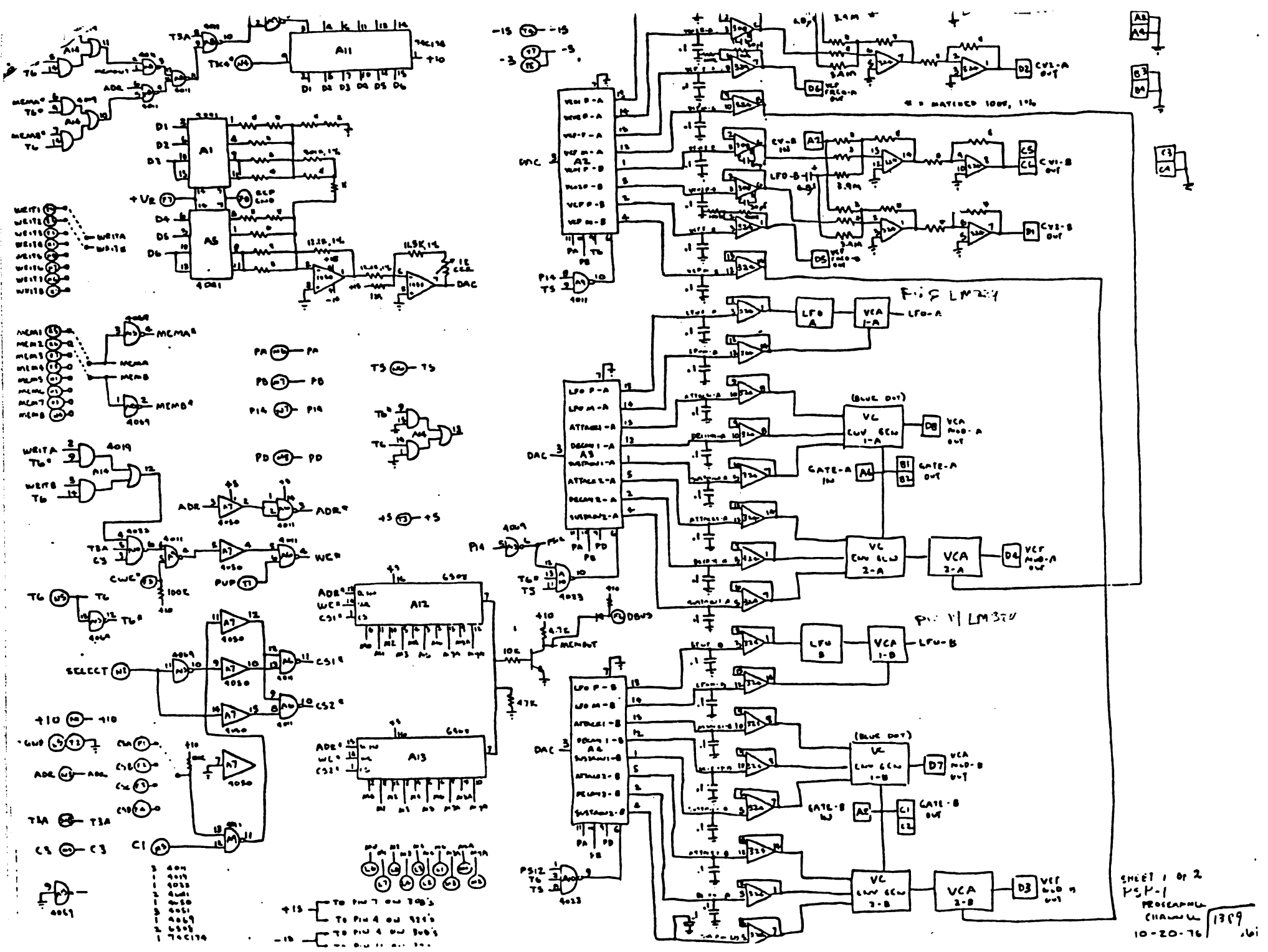
PIN	FUNCTION
1	GATE 'B' (CH'S 2,4,6,8)
2	KEY
3	GND
4	NOT USED
5	NOT USED
6	VCO 1 CV 'B' (CH'S 2,4,6,8)

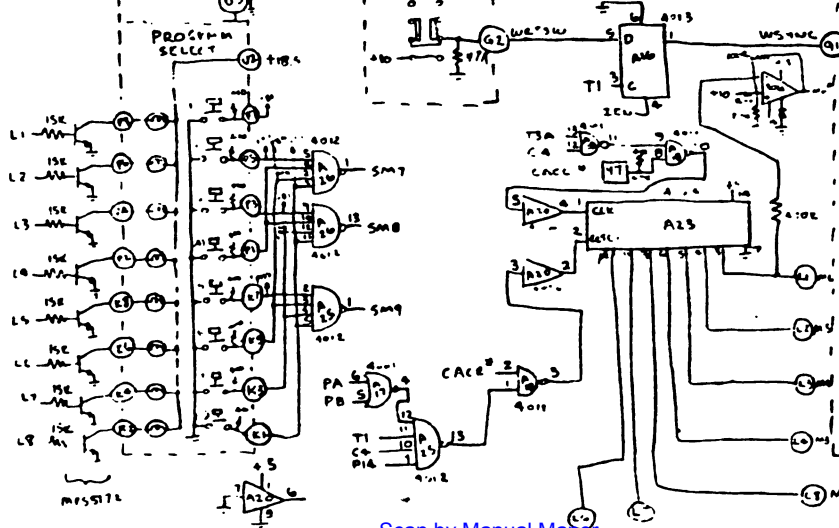
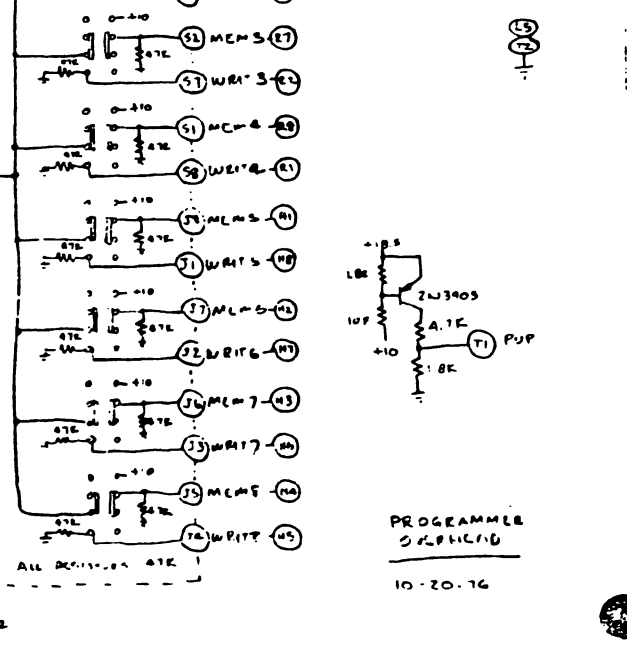
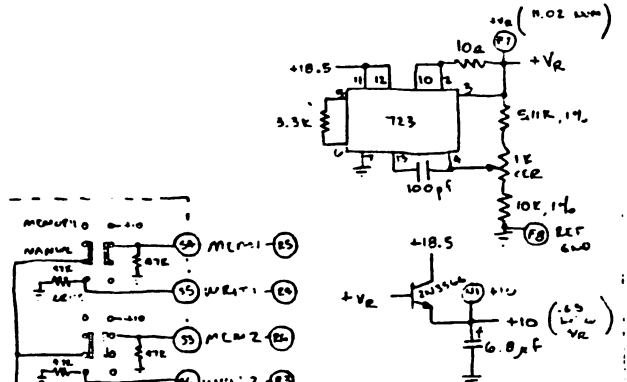
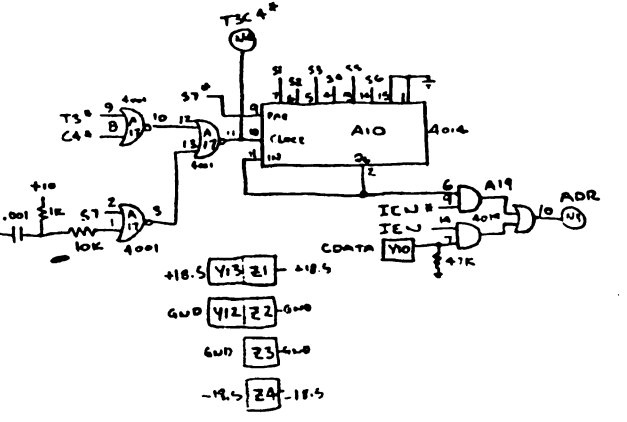
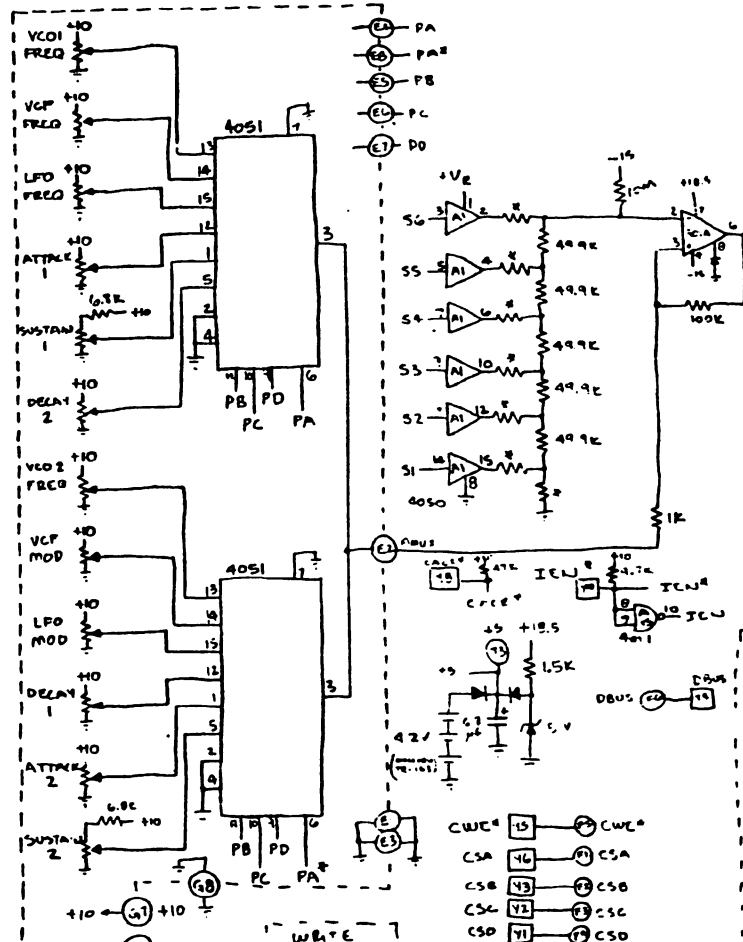
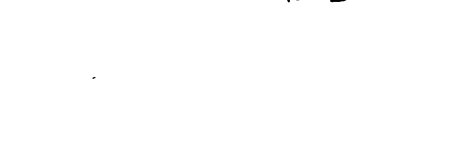
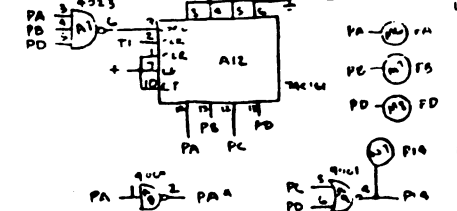
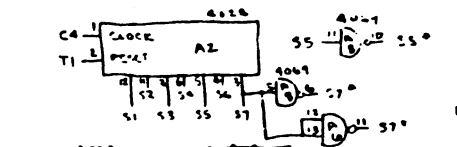
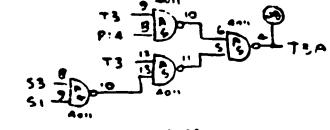
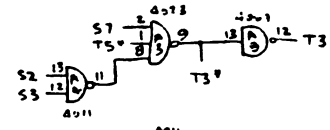
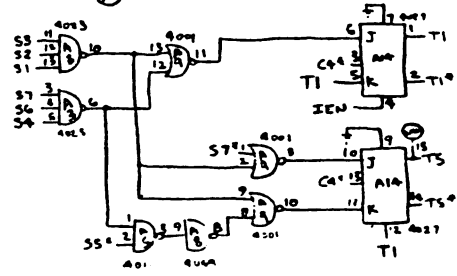
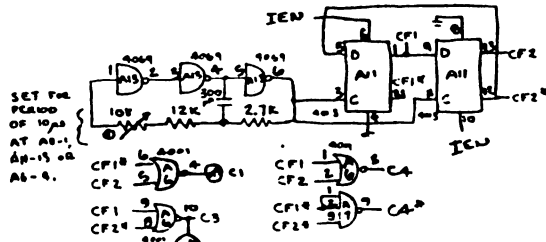
PIN	FUNCTION
1	+18.5 V
2	GND
3	KEY
4	-18.5 V

OBERHEIM ELECTRONICS, INC

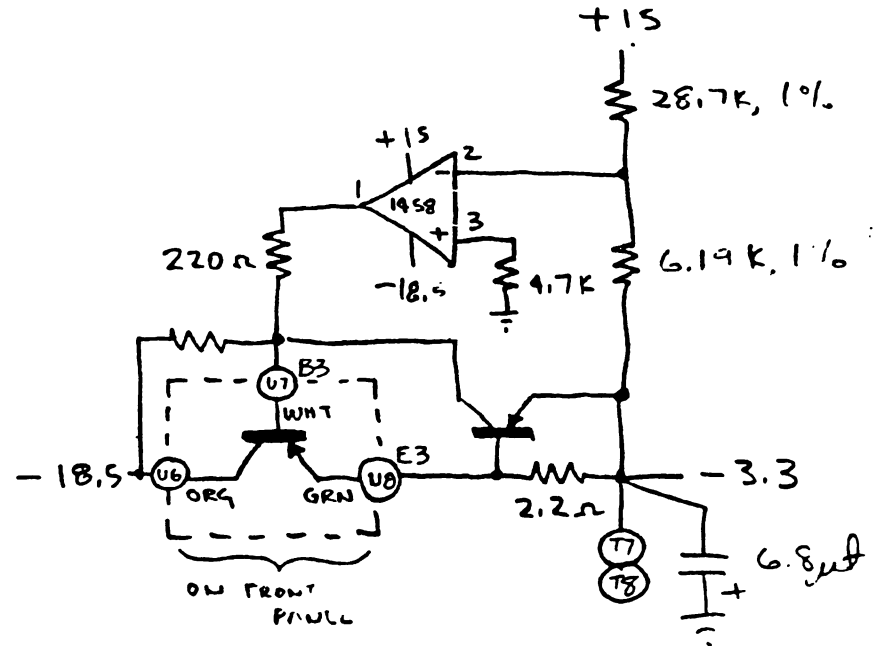
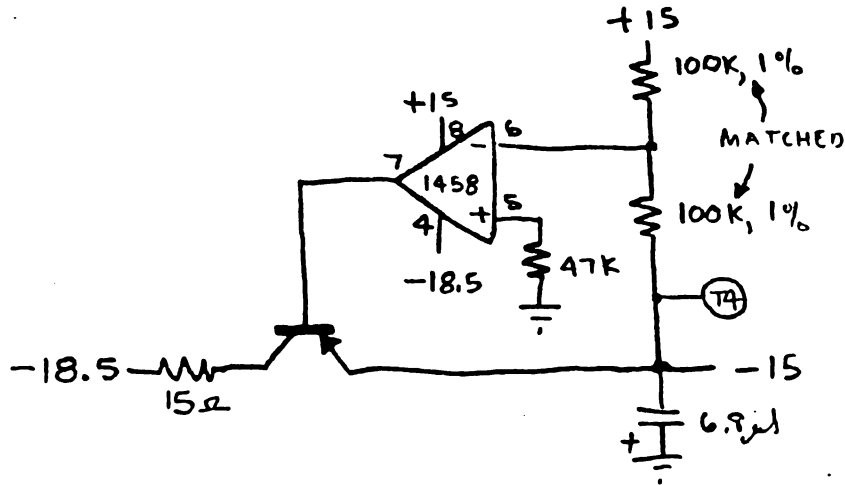
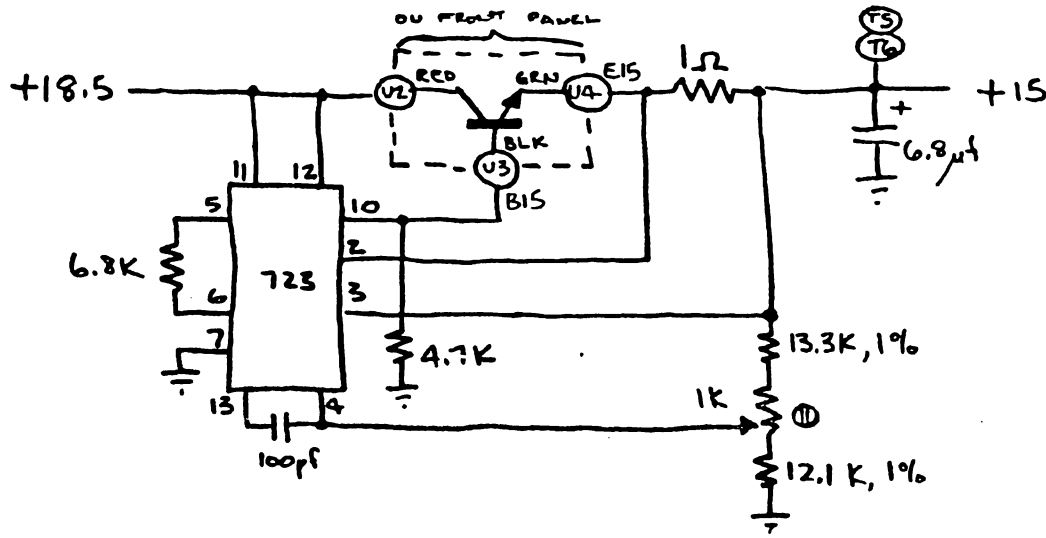
PROGRAMMER
INPUT-OUTPUT CONNECTOR
PLACEMENTS

DEC. 15, 1976



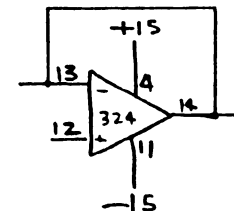
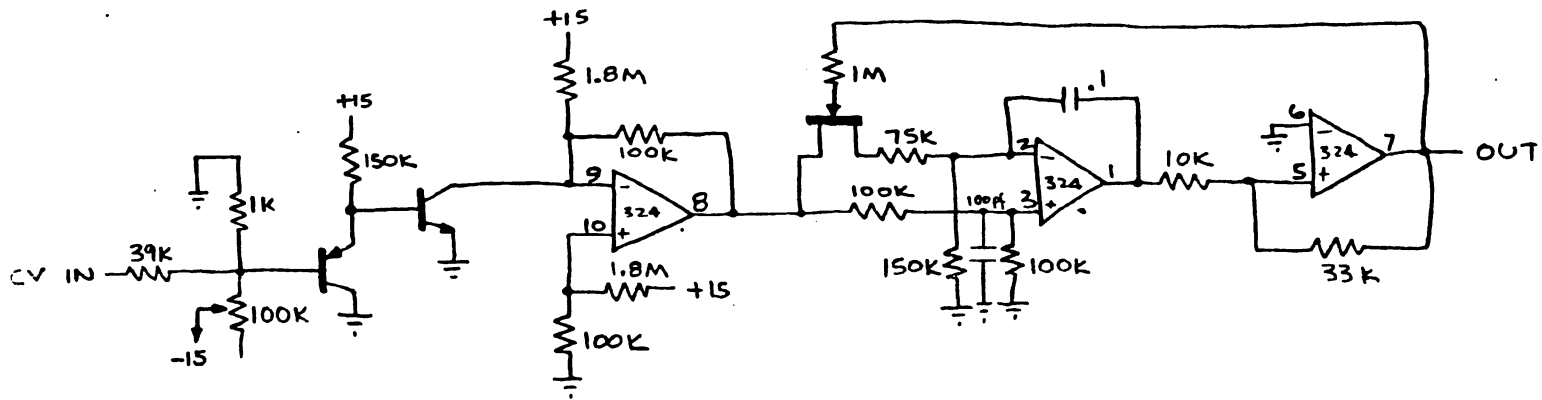


PROGRAMMER POWER SUPPLIES (ON OVERHEAD BD)



10-20-76

PROGRAMMER LFO

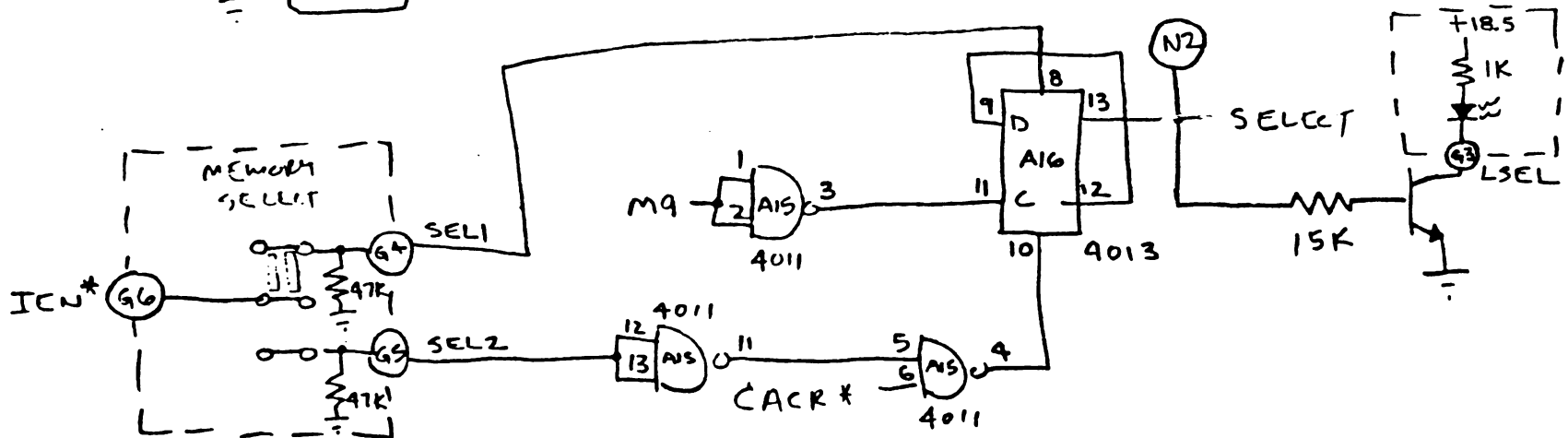
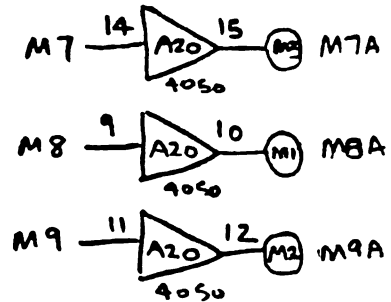
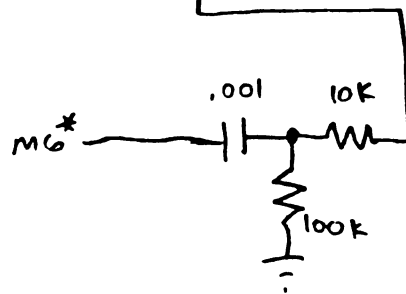
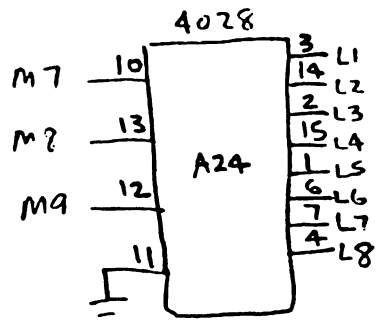
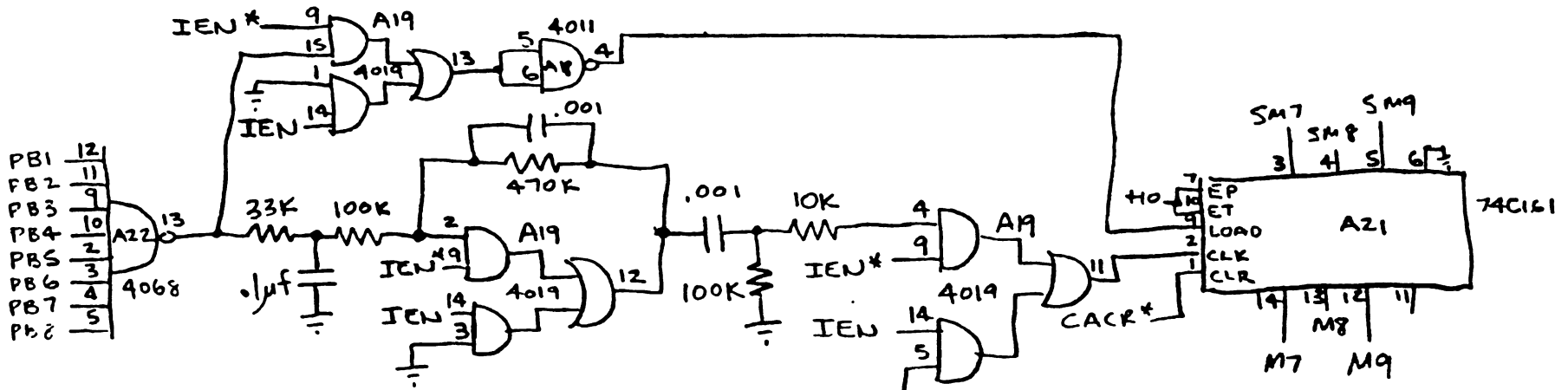


- NOTES:
 (UNLESS SPECIFIED OTHERWISE)
1. ALL RESISTORS ARE $\pm 5\%$
 2. ALL CAPACITORS IN μ
 3. ALL NPN TRANSISTORS - 2N5172
 4. ALL PNP TRANSISTORS - 2N3905
 5. ALL DIODES - 1N4148
 6. ALL FET'S - 2N4302

10-20-76

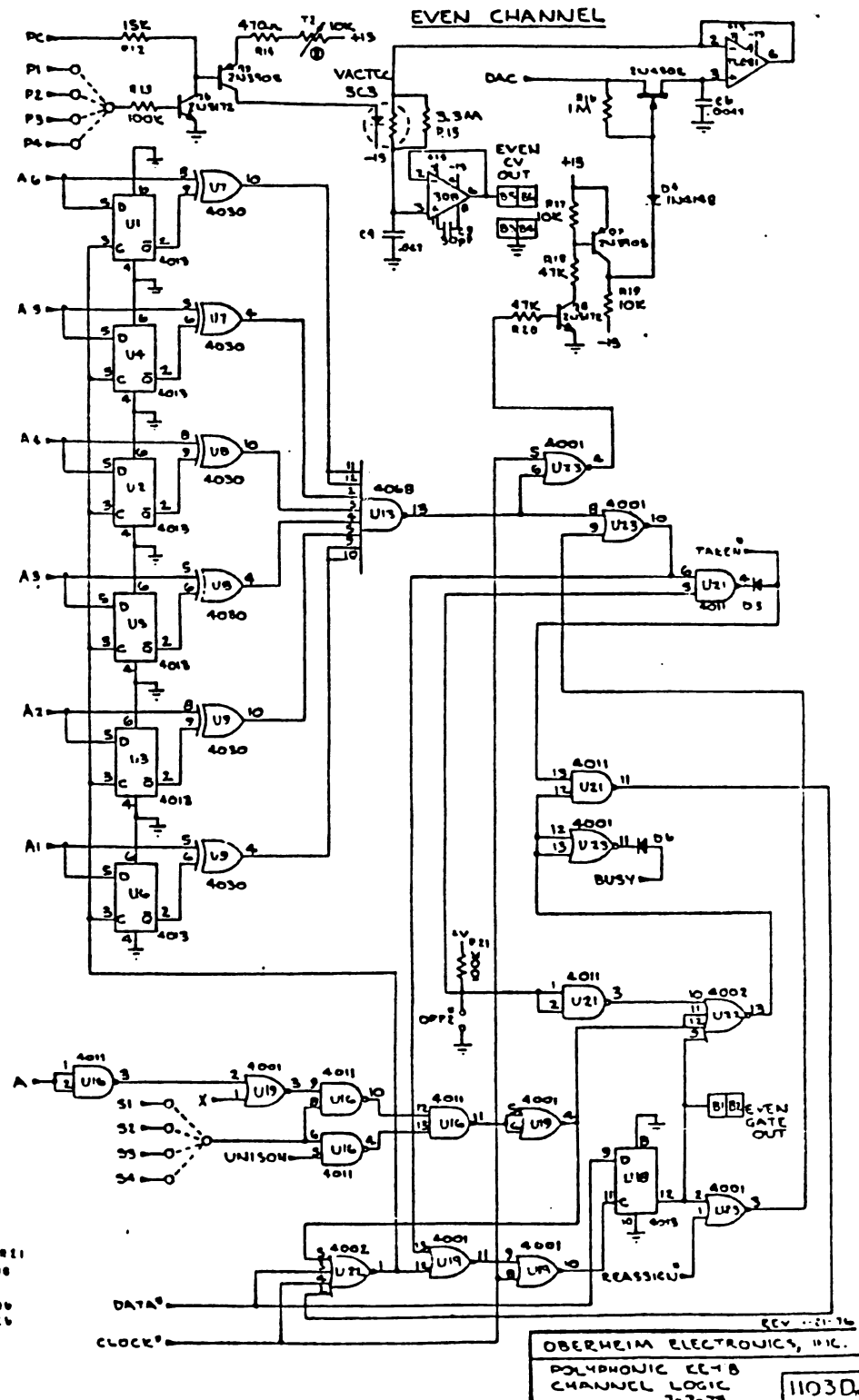
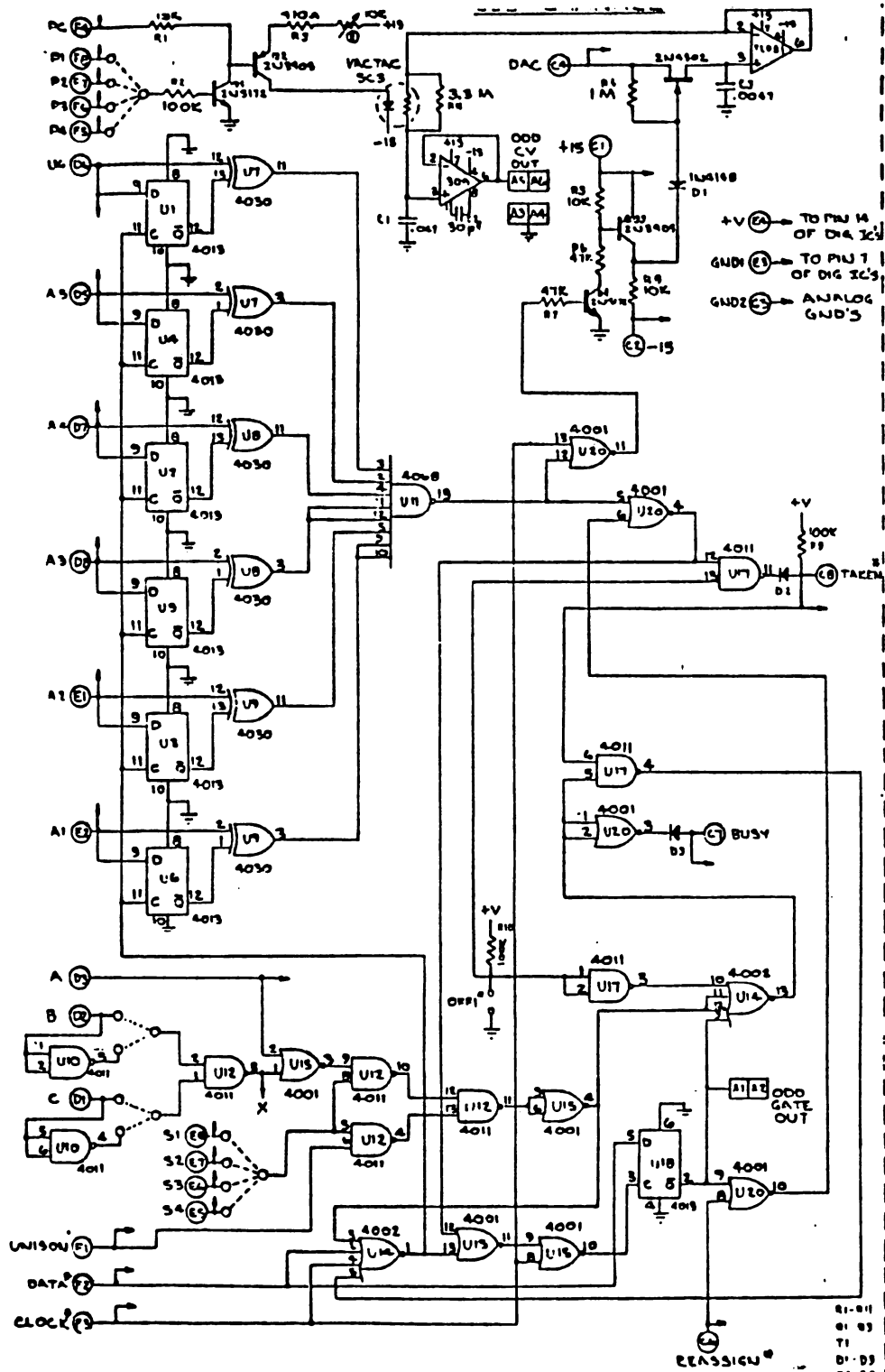
A

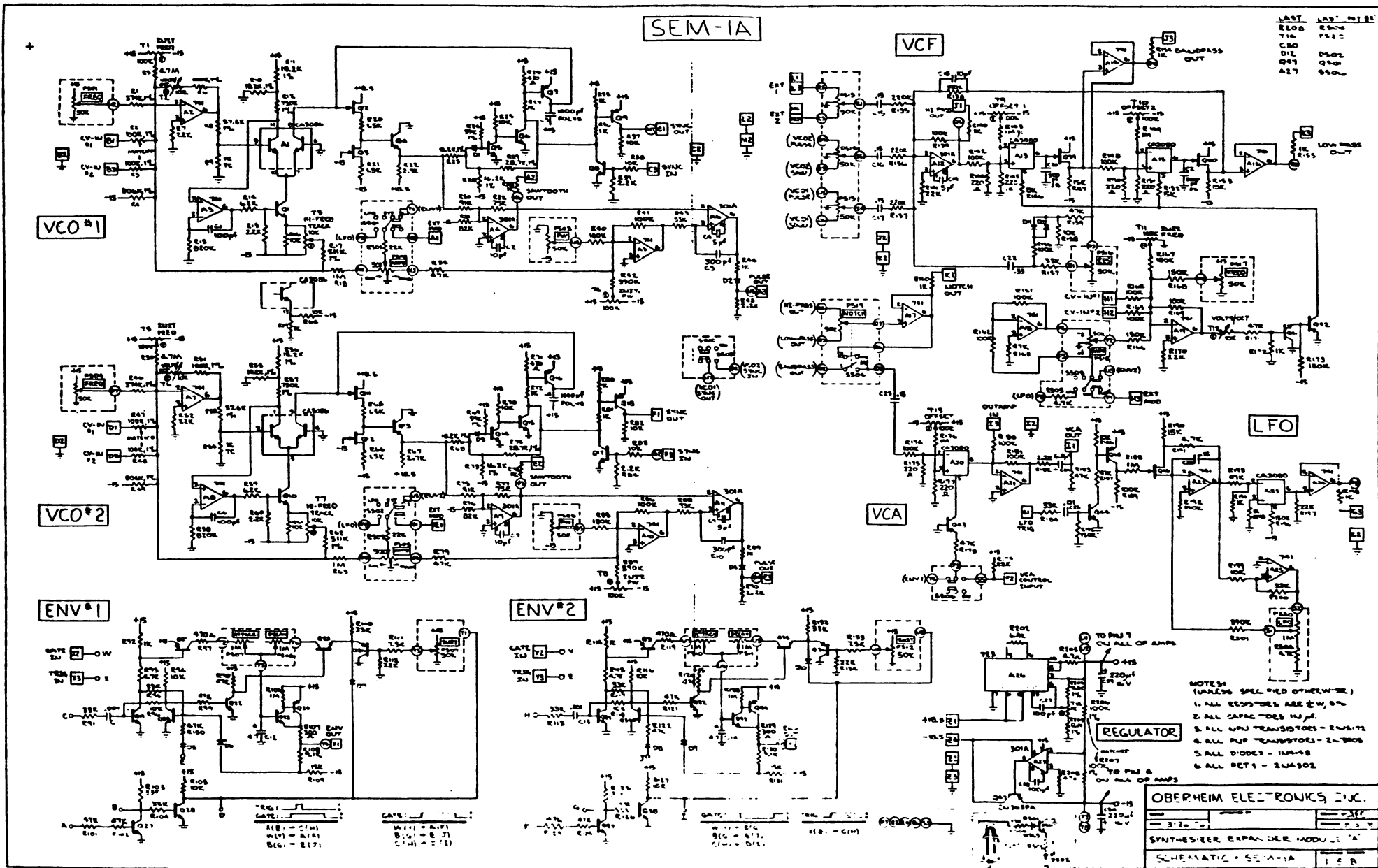
PSP-1 SHEET 2 of 2
 1389



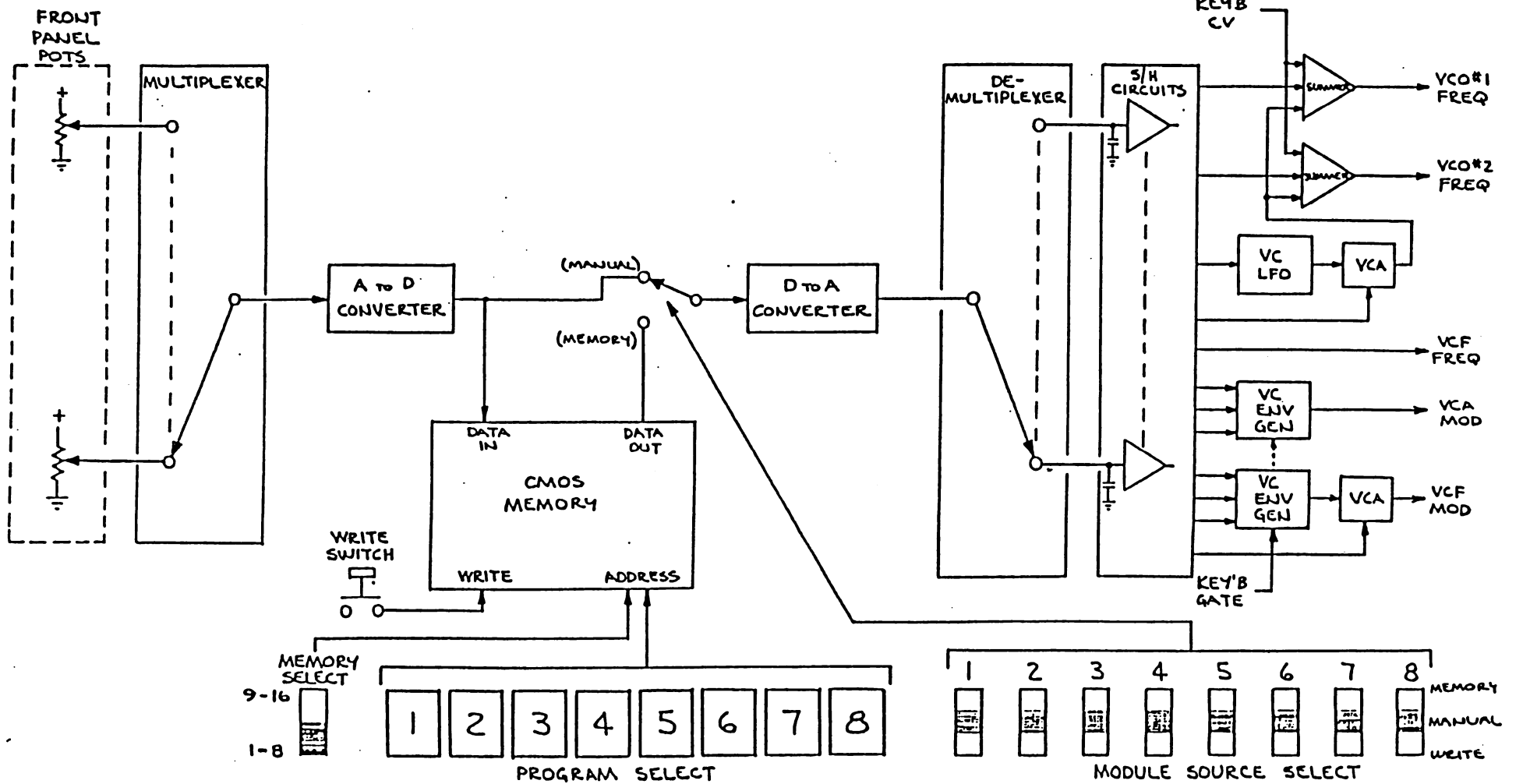
10-20-76

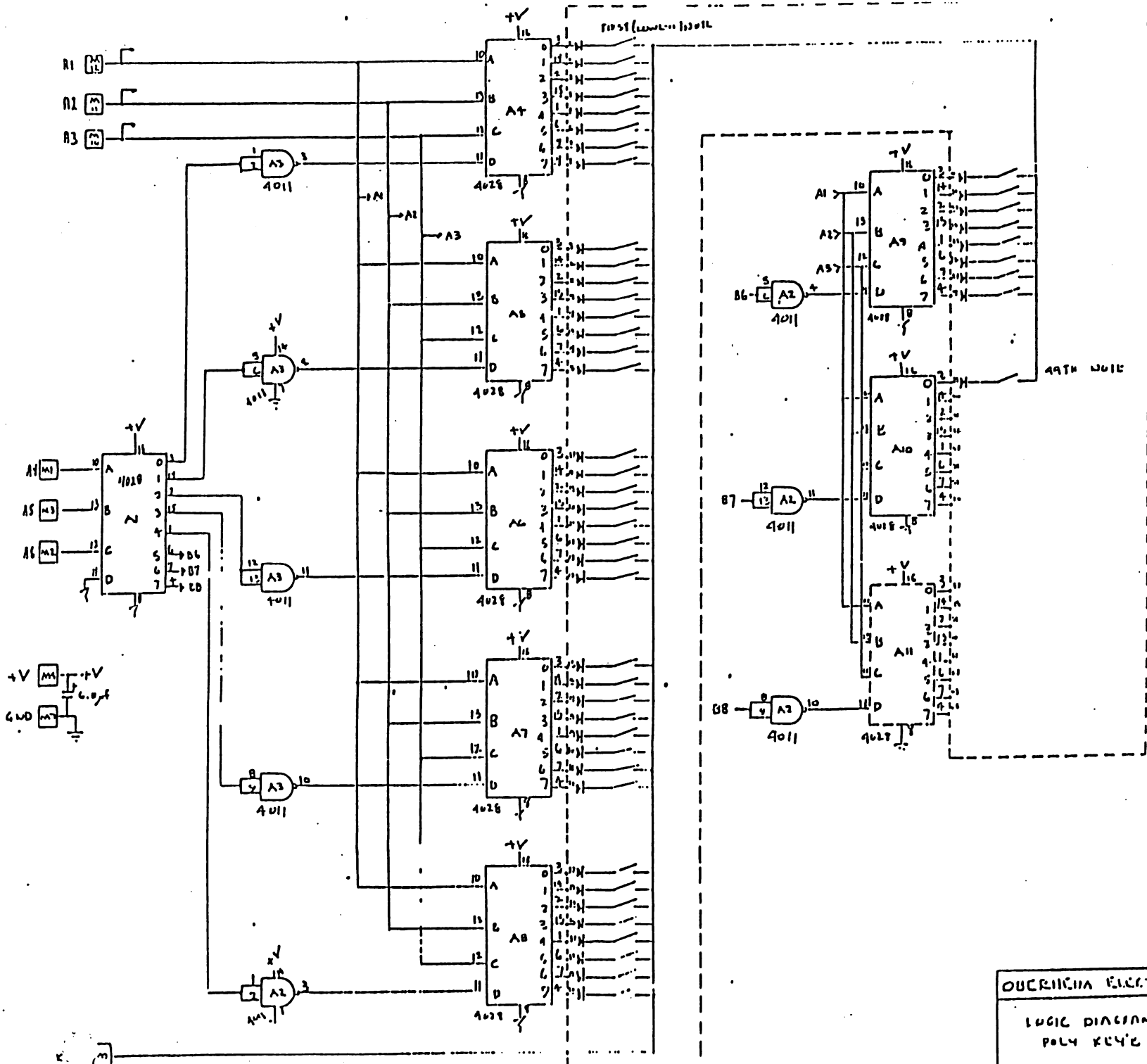
PROGRAMMER OVERHEAD





BLOCK DIAGRAM - PROGRAMMER -





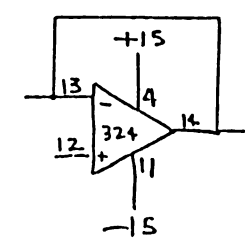
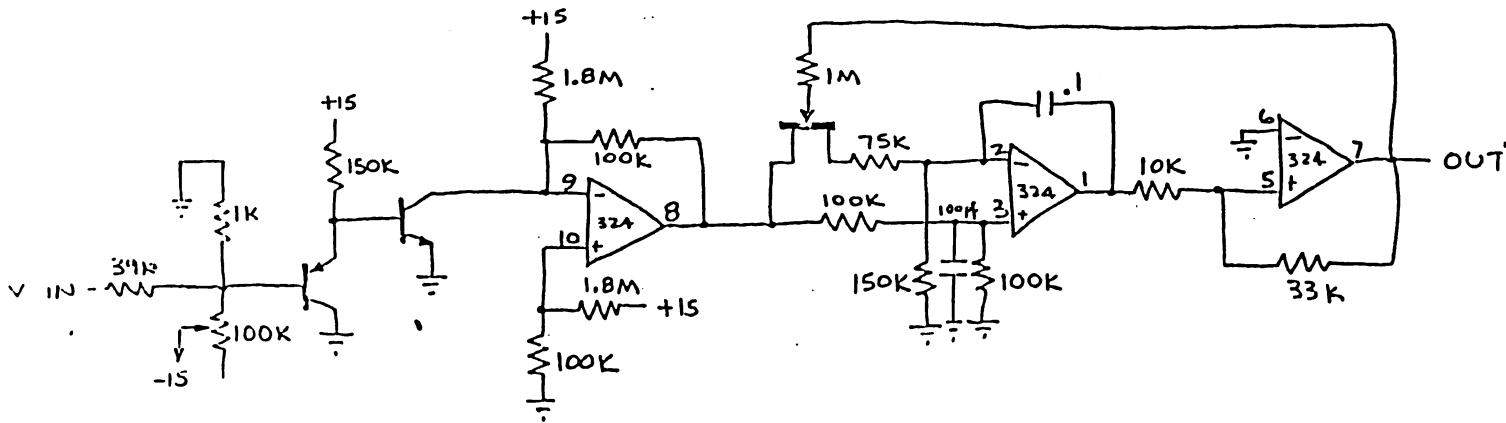
ОБЩАЯ ЭЛЕКТРОНИКА
 ЛОГИКА -
 ПОЧТОВЫЙ НОМЕР
 NOV. 11, 1975
 1126

FOUR VOICE INTER-CONNECT CABLE

(12 PIN MOLEX)

<u>FUNCTION</u>	<u>PIN</u>
CV #1	1
GATE #1	2
CV #2	3
GATE #2	4
CV #3	5
GATE #3	6
CV #4	7
GATE #4	8
GND	9
GND	10
VCO TUNE	11
VCF TUNE	12

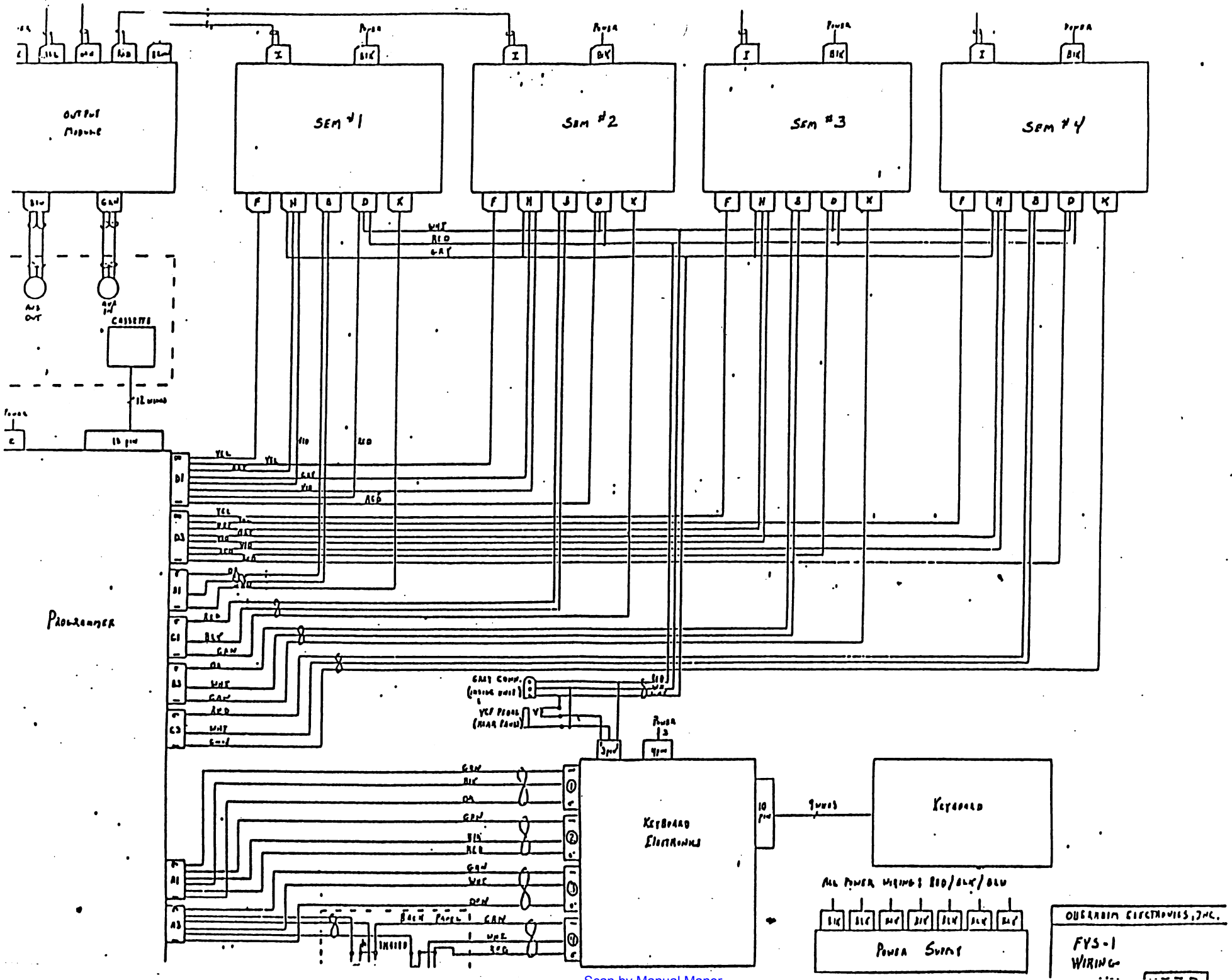
PROGRAMMER LFO



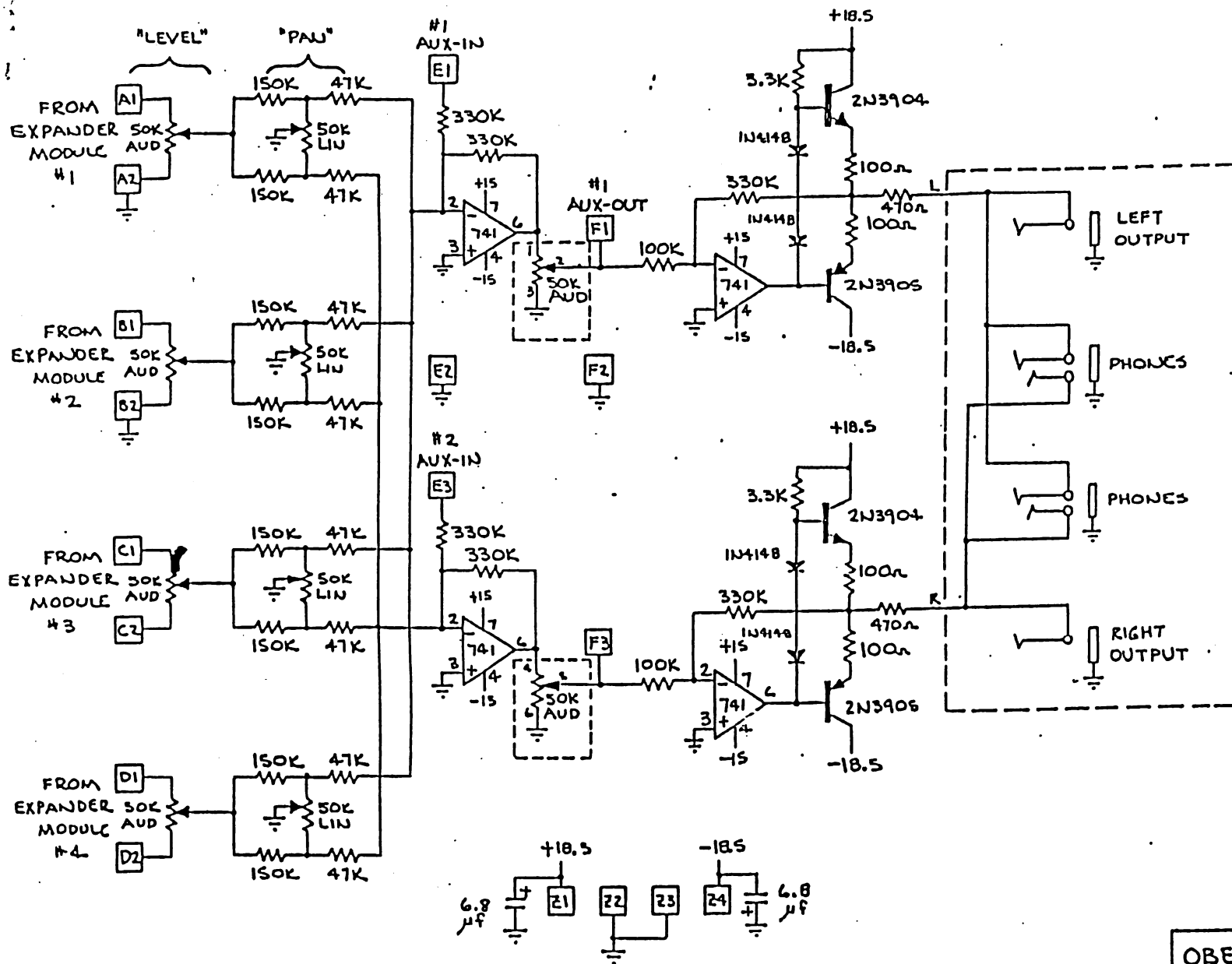
- NOTES:
 (UNLESS SPECIFIED OTHERWISE)
 1. ALL RESISTORS ARE $\pm 5\%$
 2. ALL CAPACITORS IN μ f
 3. ALL NPN TRANSISTORS - 2N5172
 4. ALL PNP TRANSISTORS - 2N3903
 5. ALL DIODES - 1N4148
 6. ALL FET'S - 2N4302

10-20-76





OVERLAIN ELECTRONICS, INC.
 FYS-1
 WIRING



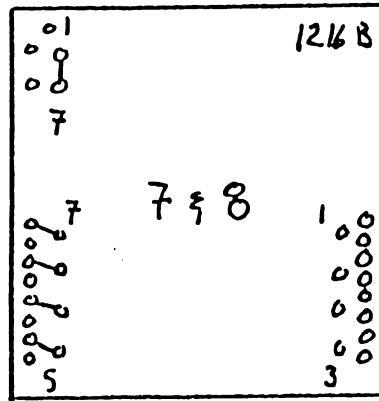
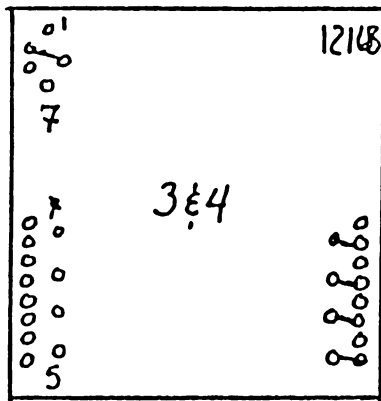
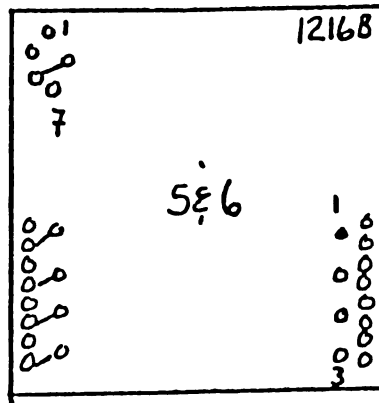
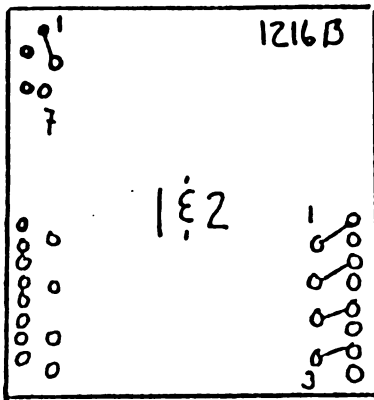
OBERHEIM ELECTRONICS, INC.

- SCHEMATIC -

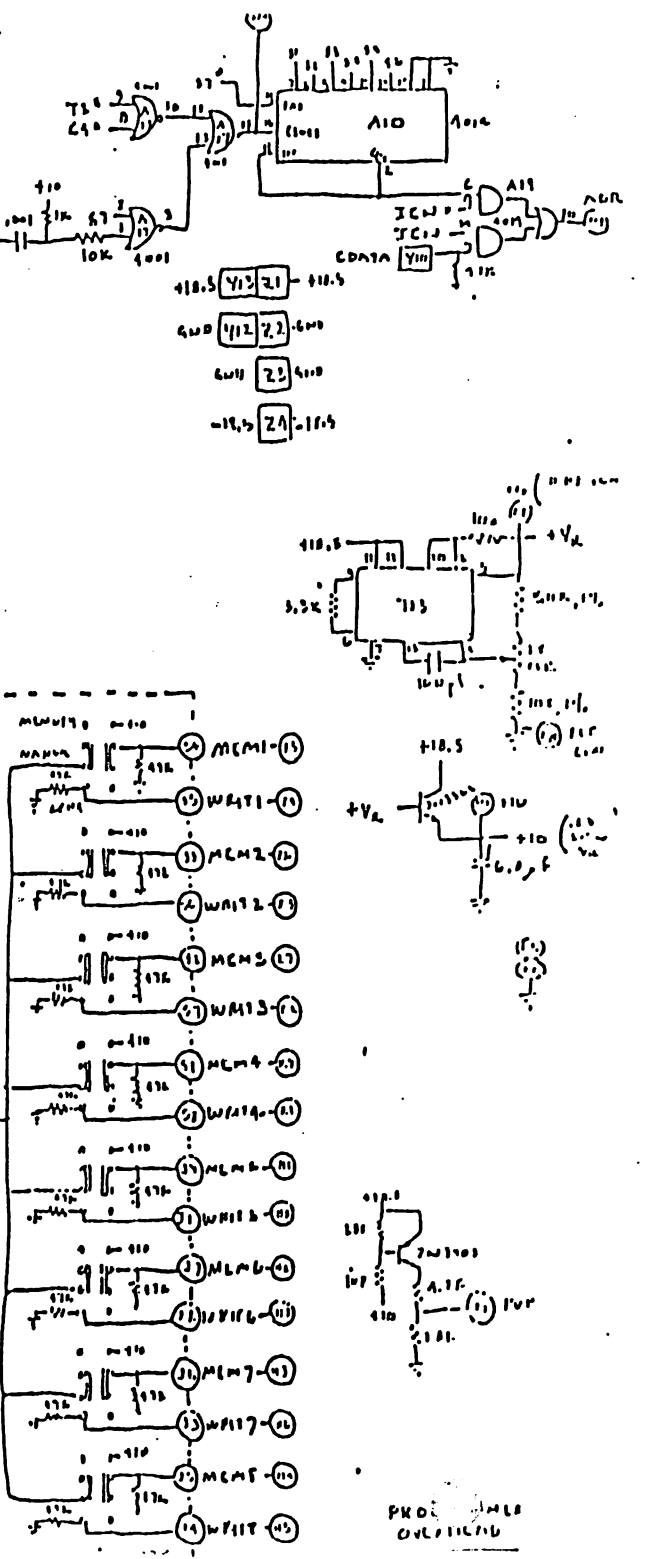
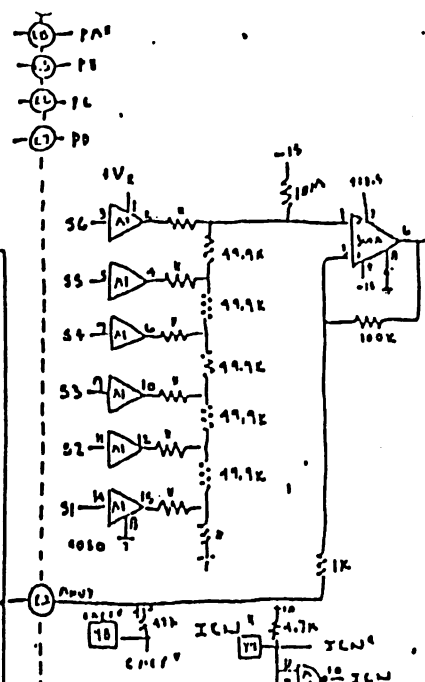
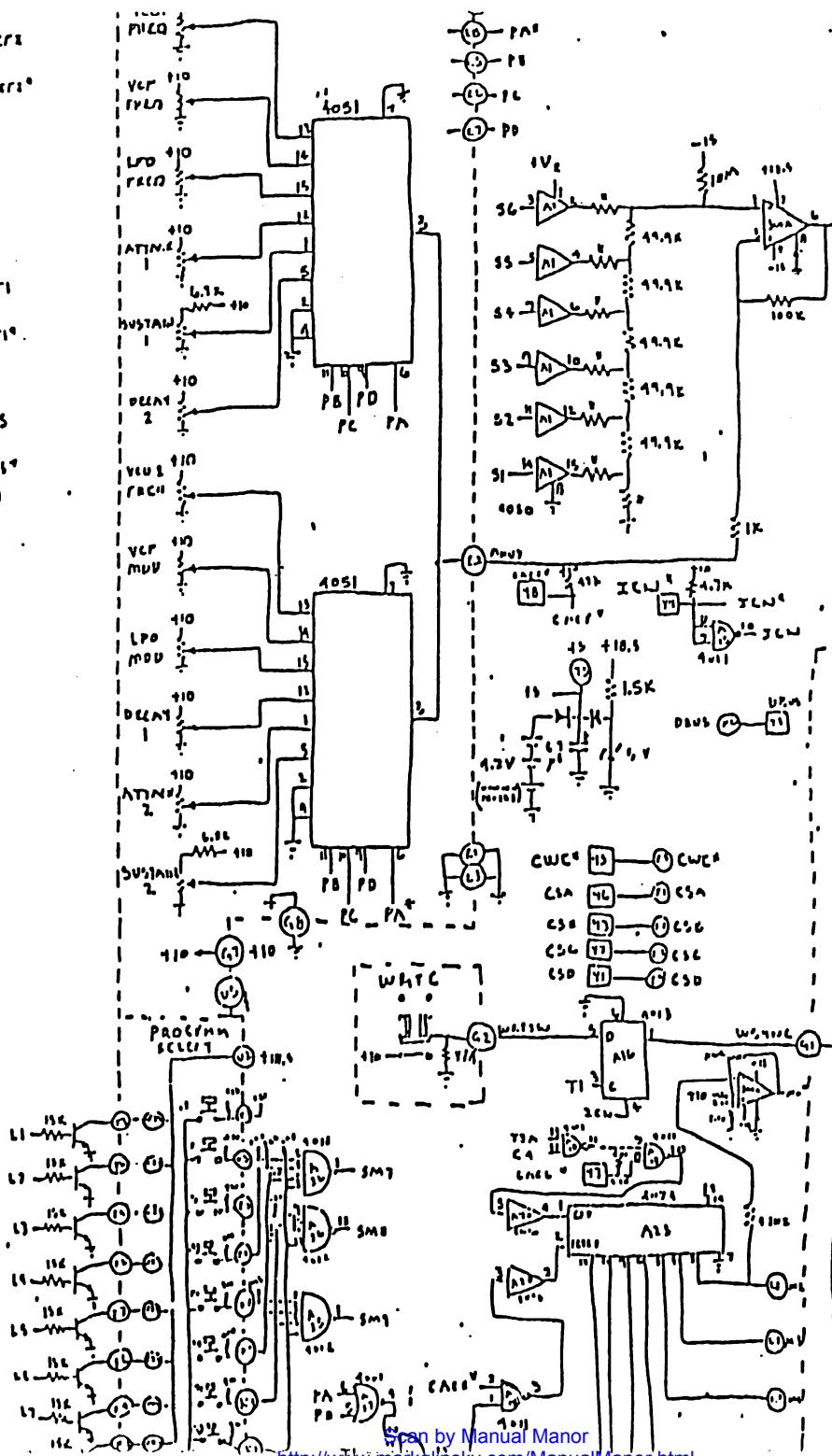
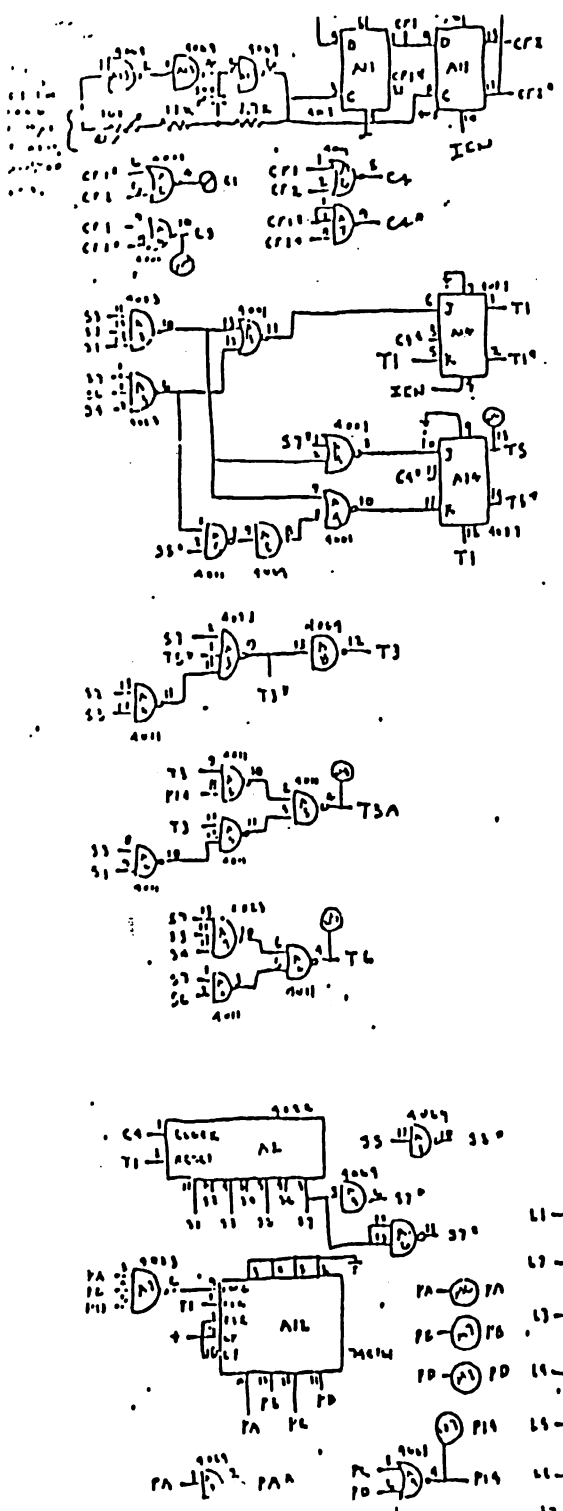
4 VOICE OUTPUT MODULE

MAR. 8, 1976

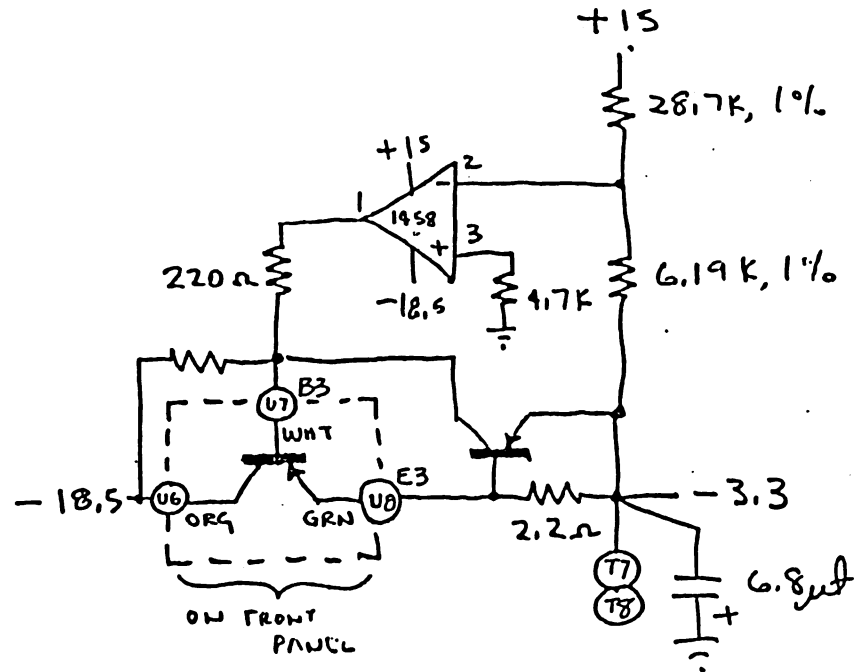
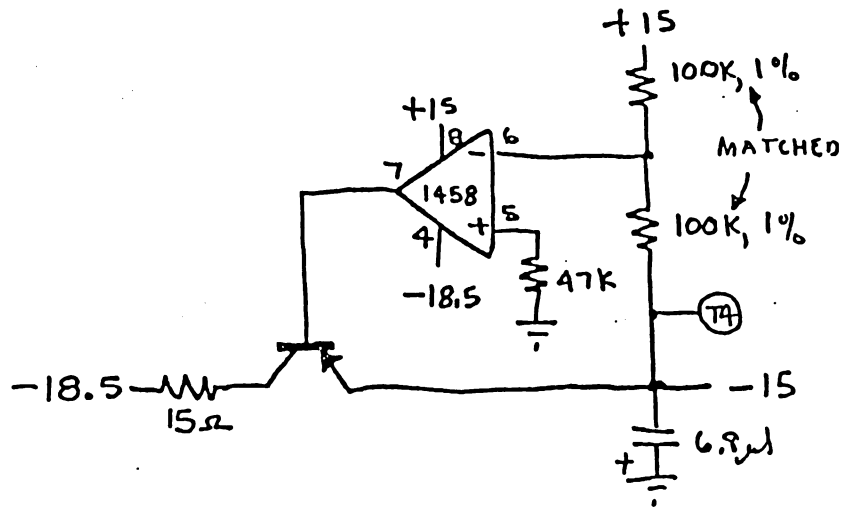
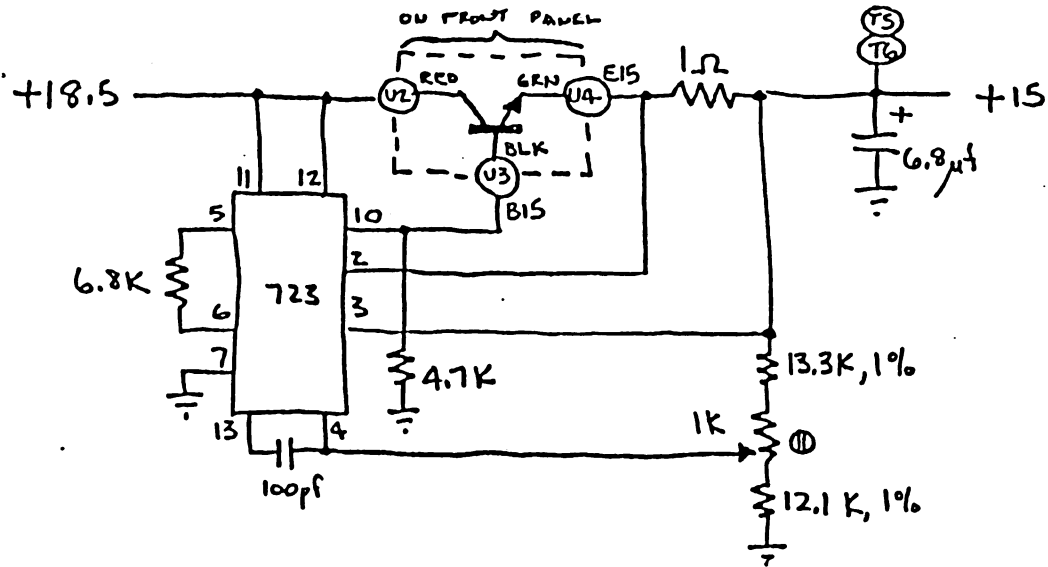
1173



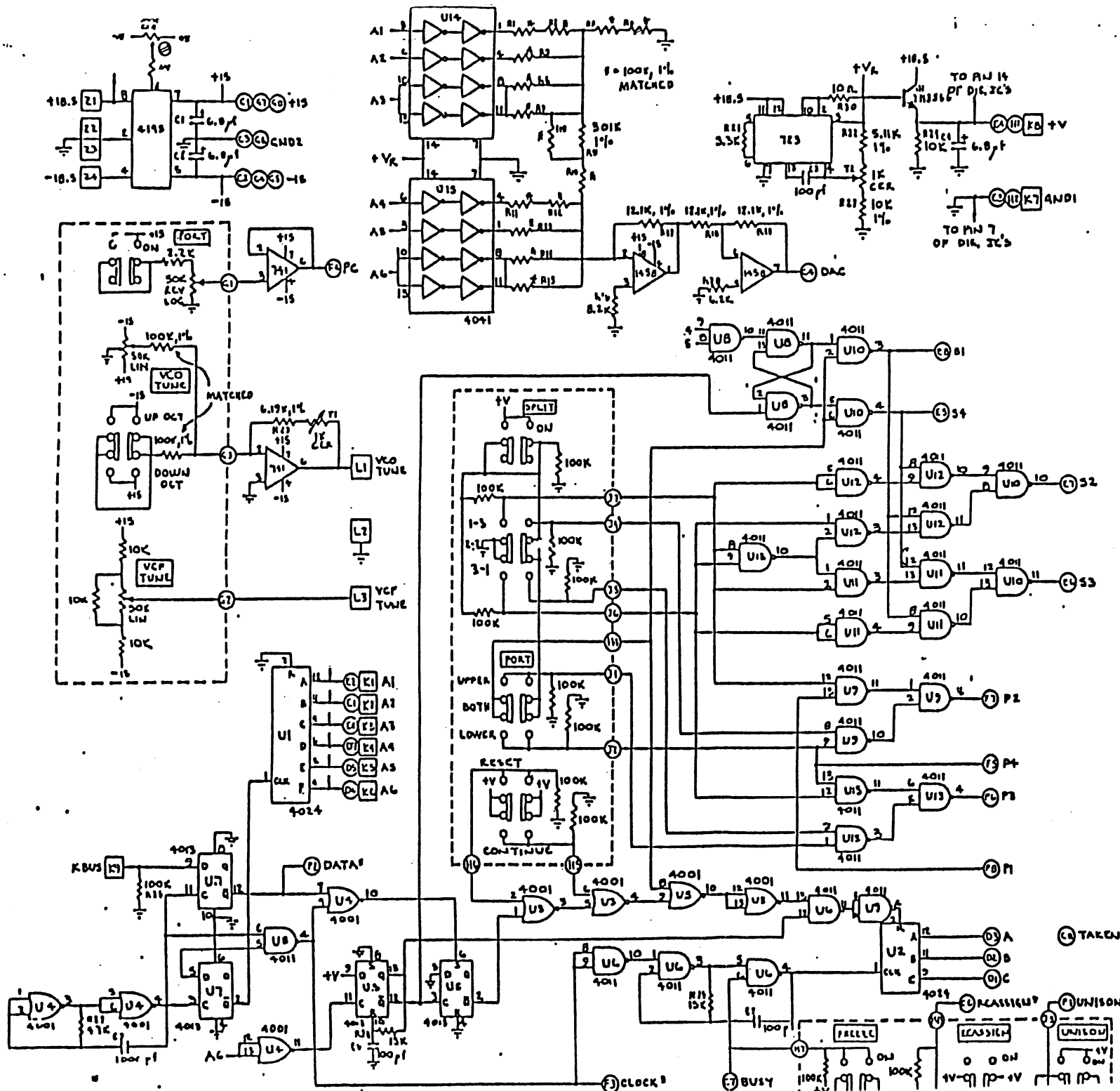
CHANNEL T.S.
 JUMPING
 PSP-1



PROGRAMMER POWER SUPPLIES (ON OVERHEAD BD)

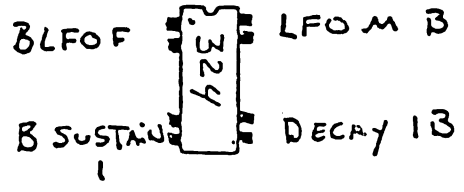
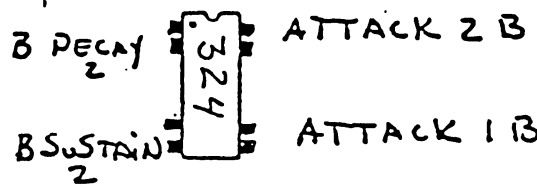
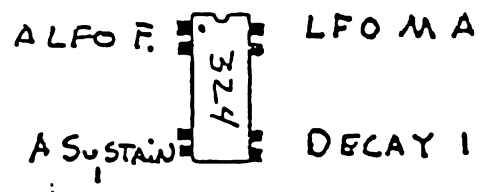
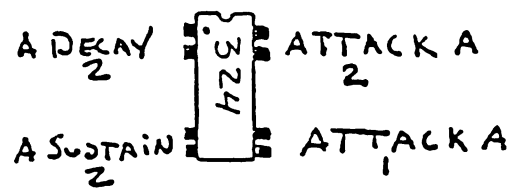
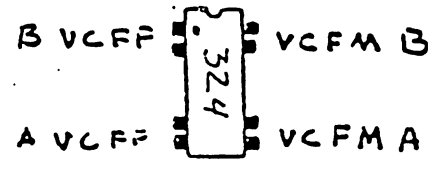
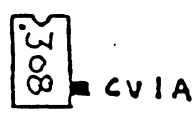
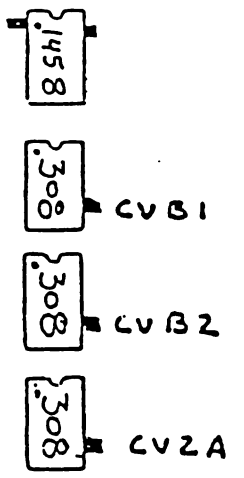


.10-20-76



NOTES:
 (UNLESS SPECIFIED OTHERWISE)
 1. ALL RESISTORS ARE 1/4W, 5%
 2. ALL CAPACITORS 10µF
 3. ALL NPN TRANSISTORS - 2N3172
 4. ALL PNP TRANSISTORS - 2N3703
 5. ALL DIODES - 1N4148
 6. ALL FET'S - 2N1302

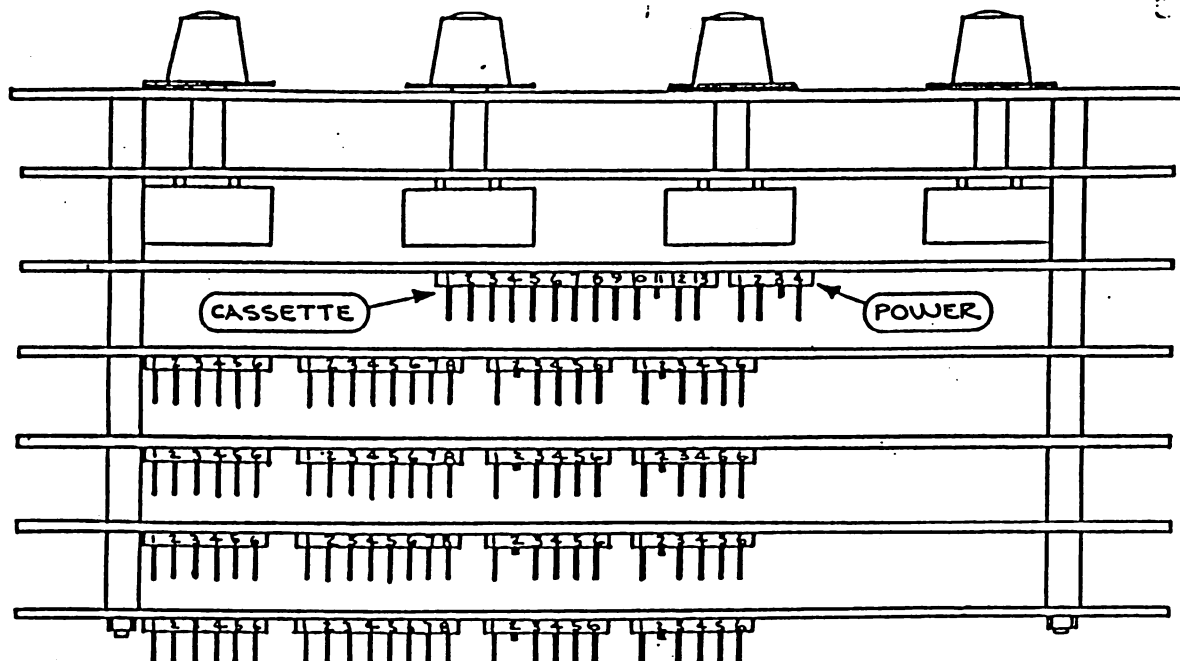
REV 1-21-76
 9-30-75



PSP S: H 12/16/13
PIN OUT
6-5-78 T.S.

FVS

TOP VIEW



FRONT PANEL

POT BOARD

OVERHEAD BOARD

CHANNEL BOARD #1 (CHANNELS 1 & 2)

CHANNEL BOARD #2 (CHANNELS 3 & 4)

CHANNEL BOARD #3 (CHANNELS 5 & 6)

CHANNEL BOARD #4 (CHANNELS 7 & 8)

A CONNECTOR		D CONNECTOR		B CONNECTOR		C CONNECTOR	
PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
1	KEY 'B' CV 'A' (CH'S 1,3,5,7)	1	VCO 2 CV 'B' (CH'S 2,4,6,8)	1	GATE 'A' (CH'S 1,3,5,7)	1	GATE 'B' (CH'S 2,4,6,8)
2	KEY 'B' CV 'B' (CH'S 2,4,6,8)	2	VCO 2 CV 'A' (CH'S 1,3,5,7)	2	KEY	2	KEY
3	GND	3	VCF ENV 'B' (CH'S 2,4,6,8)	3	GND	3	GND
4	GND	4	VCF ENV 'A' (CH'S 1,3,5,7)	4	NOT USED	4	NOT USED
5	KEY 'B' GATE 'B' (CH'S 2,4,6,8)	5	VCF CV 'B' (CH'S 2,4,6,8)	5	NOT USED	5	NOT USED
6	KEY 'B' GATE 'A' (CH'S 1,3,5,7)	6	VCF CV 'A' (CH'S 1,3,5,7)	6	VCO 1 CV 'A' (CH'S 1,3,5,7)	6	VCO 1 CV 'B' (CH'S 2,4,6,8)
		7	VCA ENV 'B' (CH'S 2,4,6,8)				
		8	VCA ENV 'A' (CH'S 1,3,5,7)				

POWER CONNECTOR	
PIN	FUNCTION
1	+18.5 V
2	GND
3	KEY
4	-18.5 V

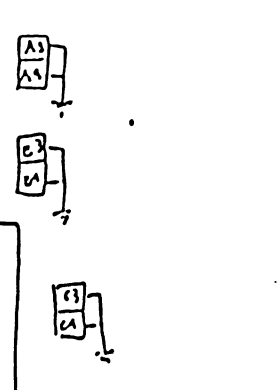
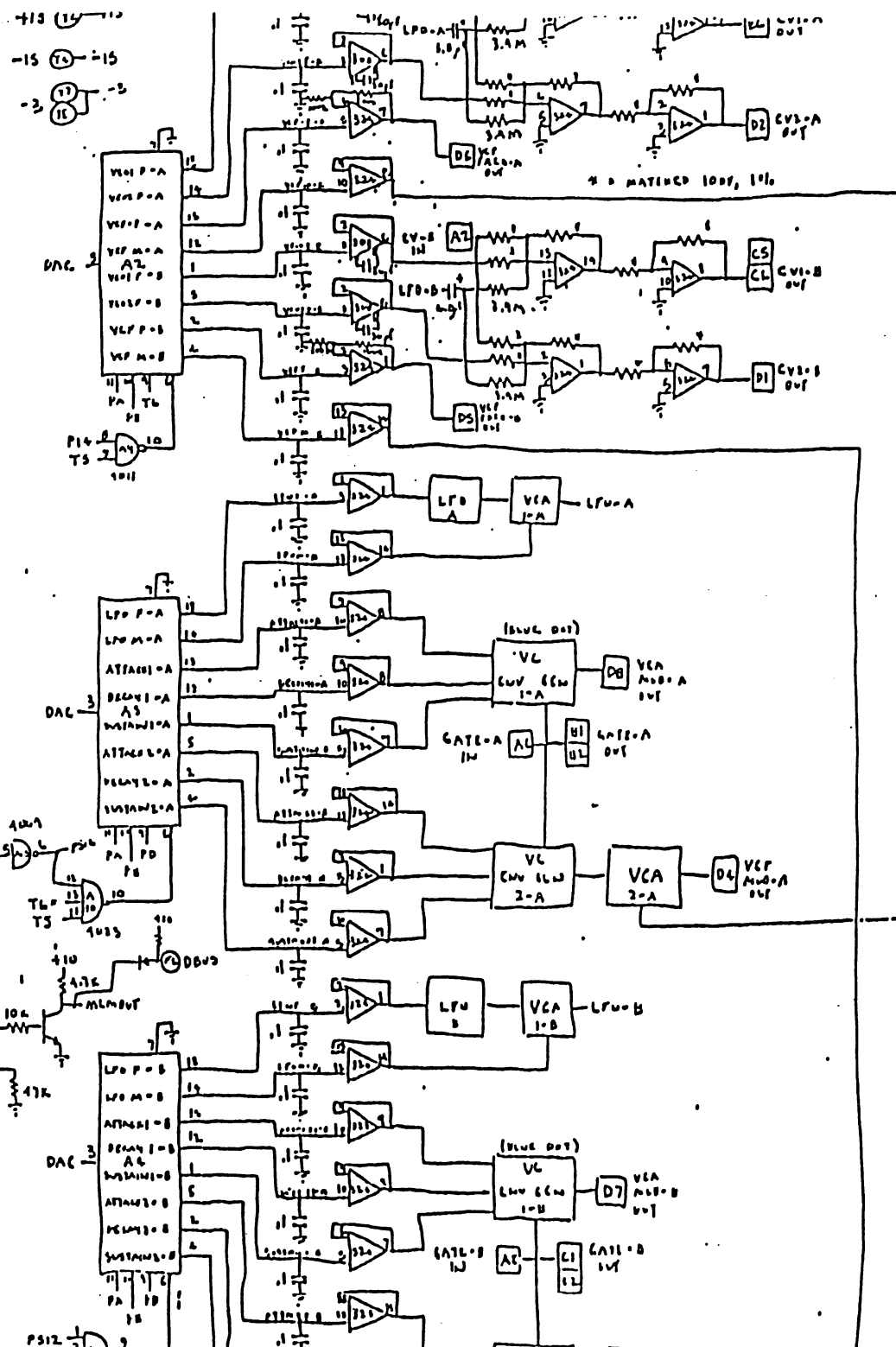
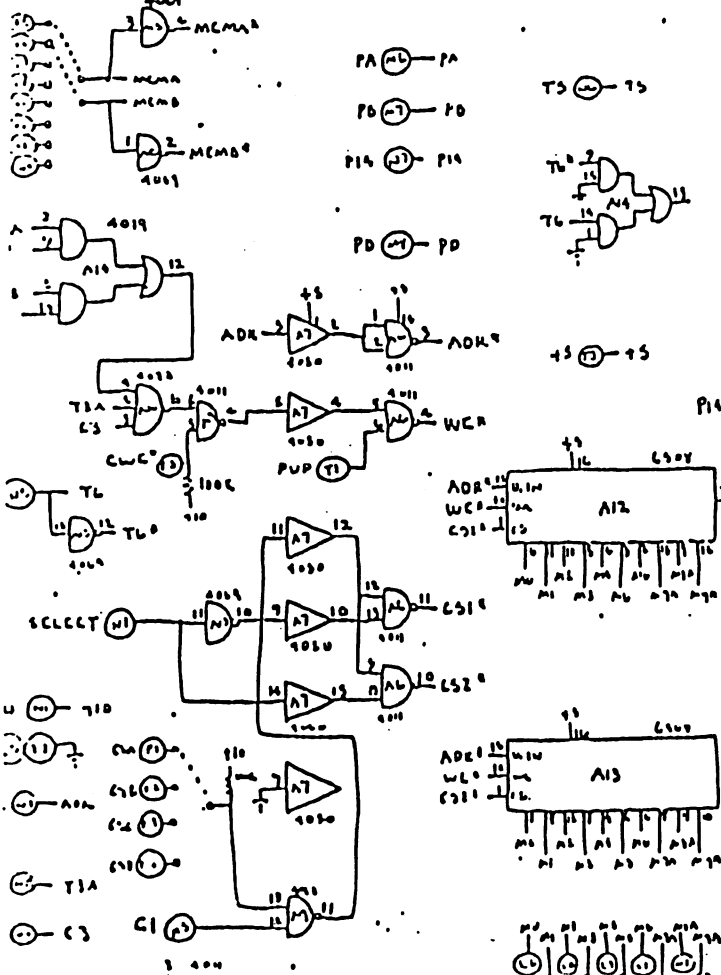
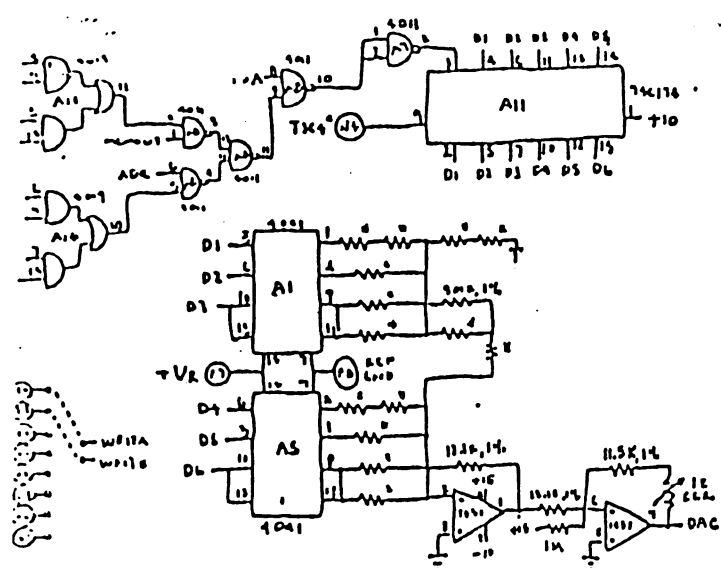
OBERHEIM ELECTRONICS, INC

PROGRAMMER

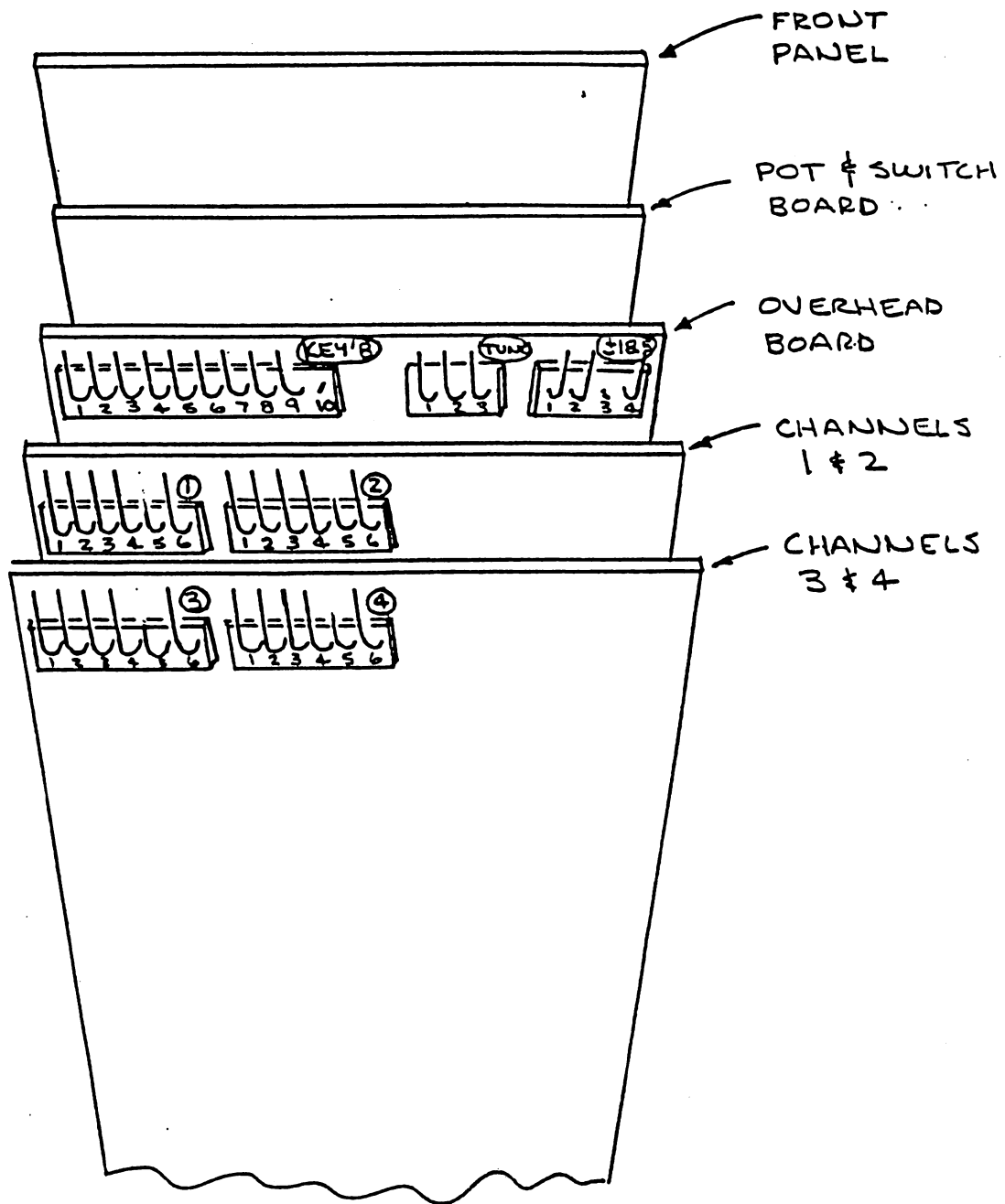
INPUT-OUTPUT CONNECTOR

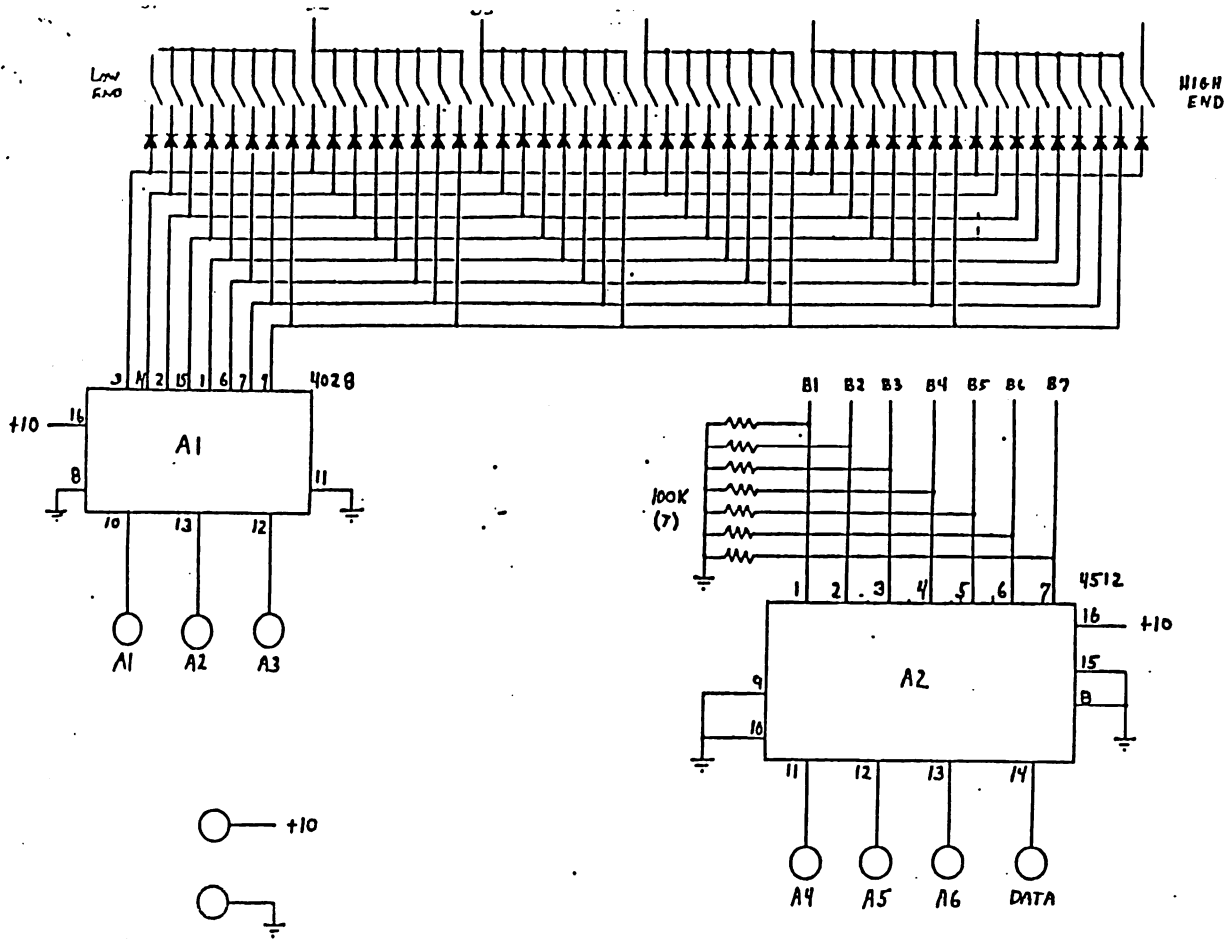
PLACEMENTS

DEC. 15, 1976



FOUR VOICE ELECTRONICS CONNECTIONS





NOTES:

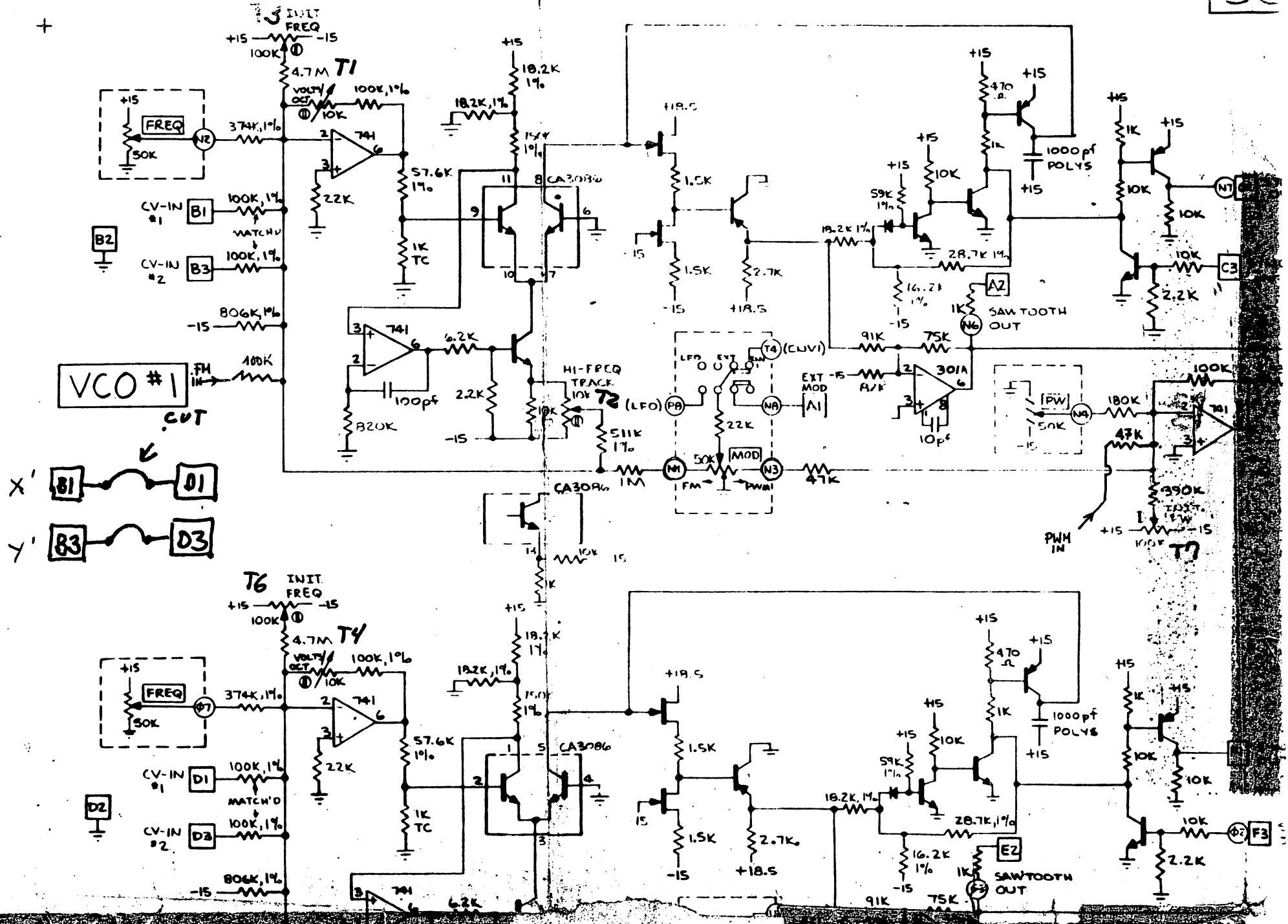
- 1) A1 AND A2 TO BE MOUNTED IN SOCKETS
- 2) ○ DENOTES SOLDERABLE PINS ON P.C BOARD
- 3) ALL RESISTORS 5% 1/4 OR 1/2 WATT


OBSCHEIM ELECTRONICS, INC.
 .49 NOTE KEYBOARD
 WITH DECODER
 7/77

SEM 1A

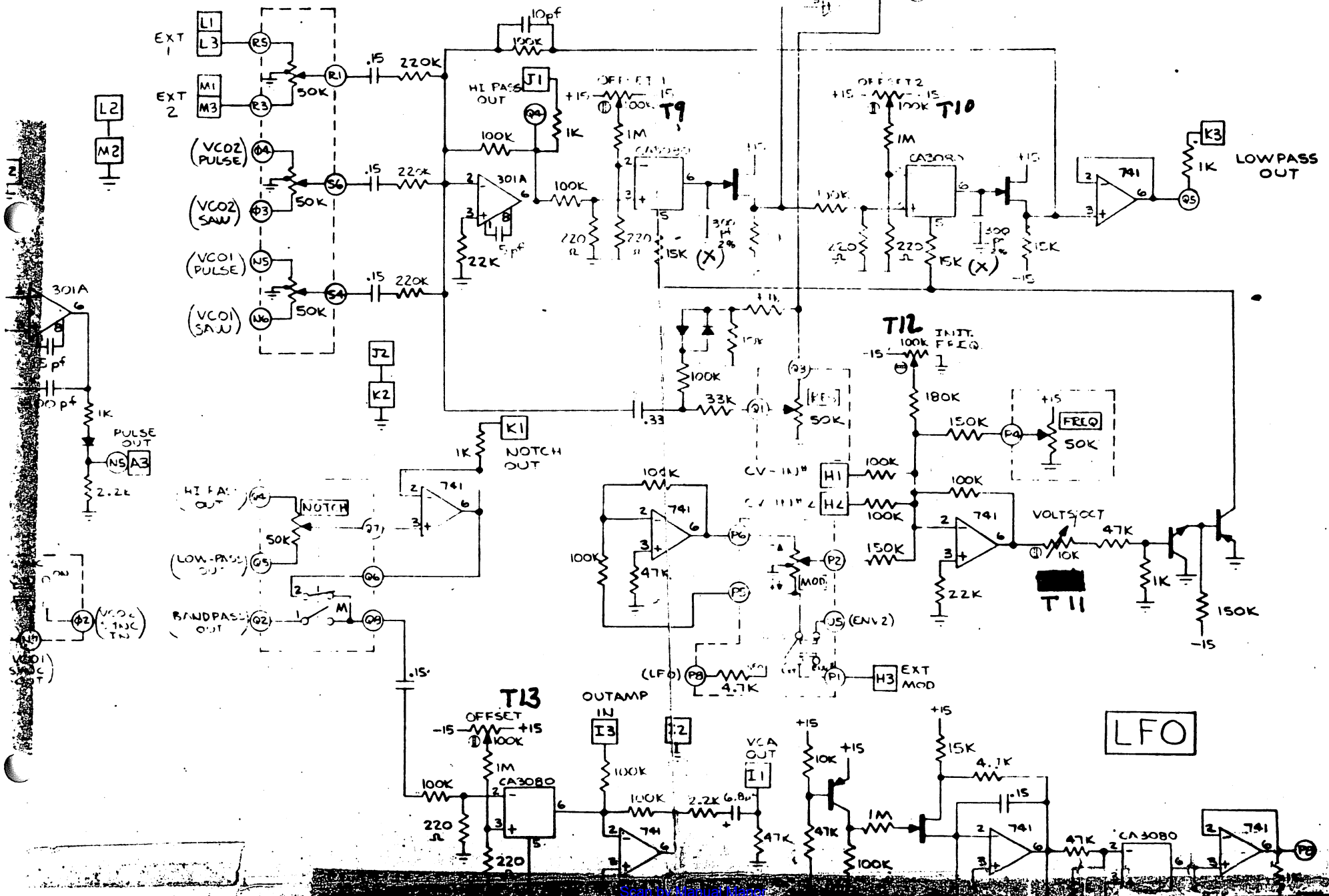
1

SE



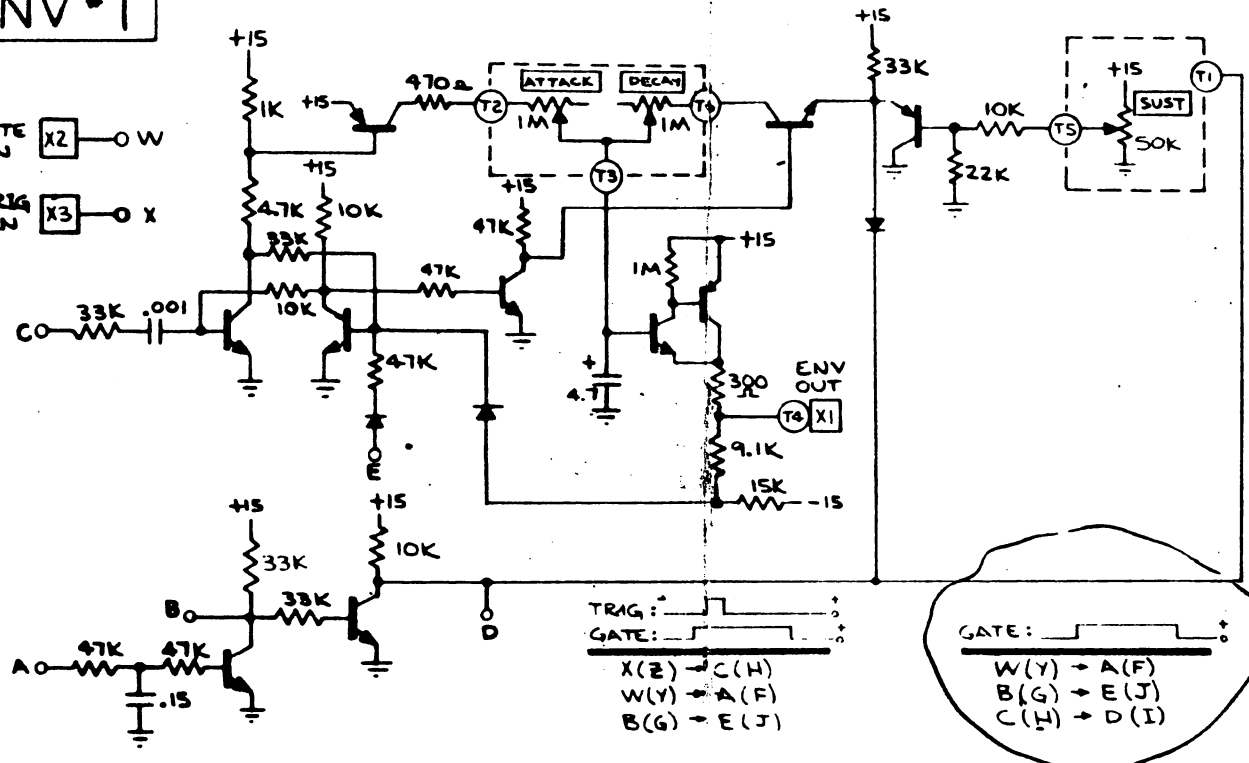
(X) Switch.  Replace 333n of 10n for low freq. ref

VCF



ENV #1

GATE IN X2 - O W
TRIG IN X3 - O X



TRIG: GATE:

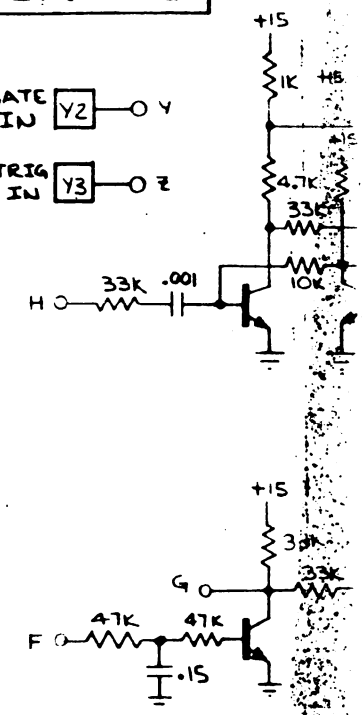
X(Z) → C(H)
W(Y) → A(F)
B(G) → E(J)

GATE:

W(Y) → A(F)
B(G) → E(J)
C(H) → D(I)

ENV #2

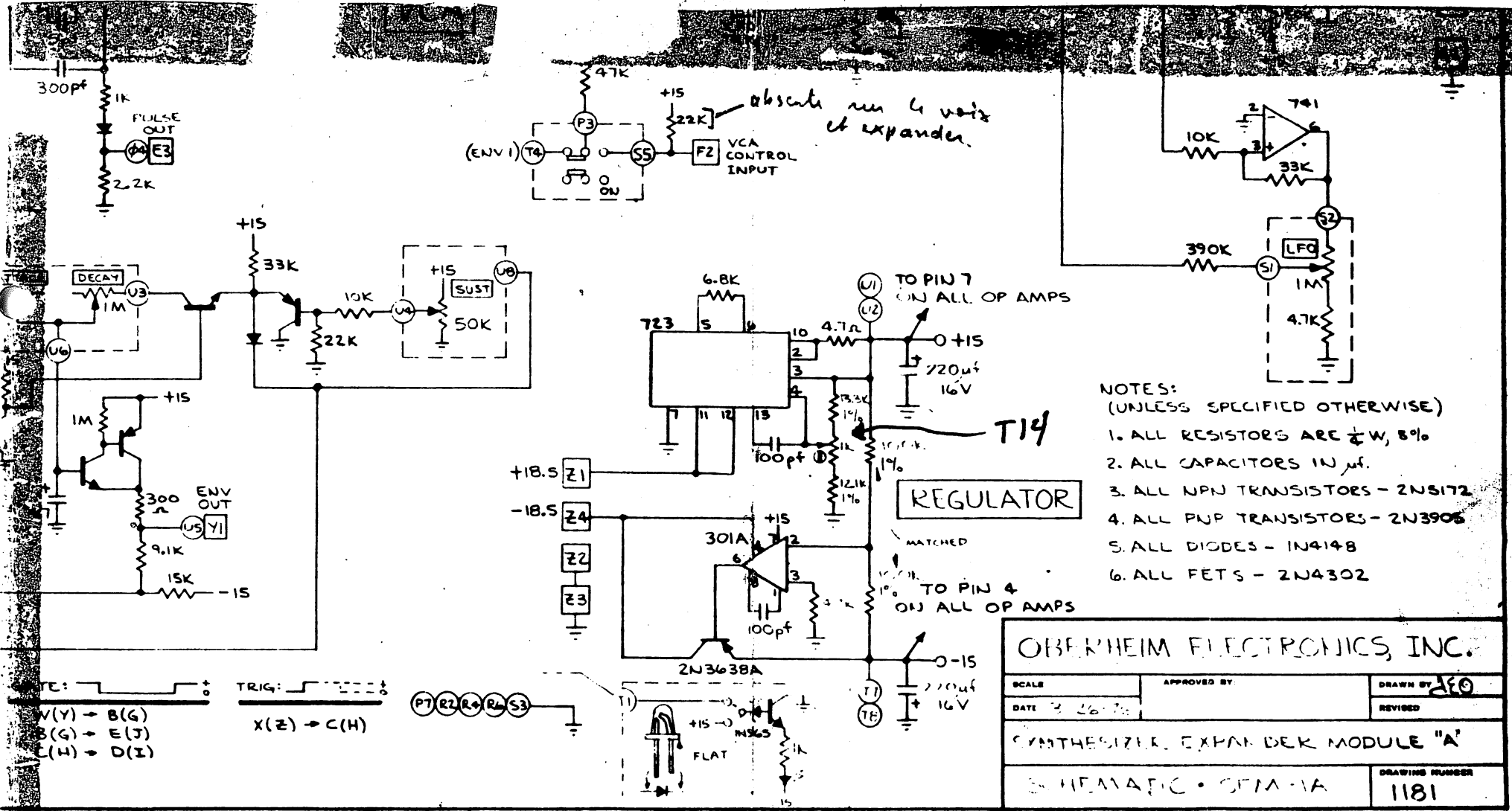
GATE IN Y2 - O Y
TRIG IN Y3 - O Z



4 voices
2 voices
P.S

X'	cut	Y'	on	22K VCA	gate and trigger
				cut	cut
	on	on	on		cut
	on	on	cut		Z → G on X → B on

B input for flow
A gate input 0 → 5V
C trigger input 0 → 5

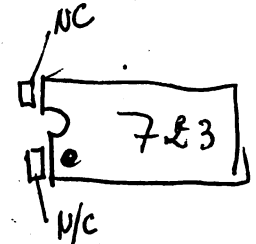
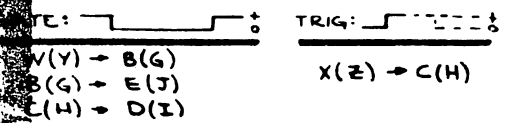


absorbir un 4 voix et expander.

- NOTES:
(UNLESS SPECIFIED OTHERWISE)
1. ALL RESISTORS ARE $\frac{1}{2}$ W, 5%
 2. ALL CAPACITORS IN μ F.
 3. ALL NPN TRANSISTORS - 2N5172
 4. ALL PNP TRANSISTORS - 2N3906
 5. ALL DIODES - 1N4148
 6. ALL FETS - 2N4302

REGULATOR

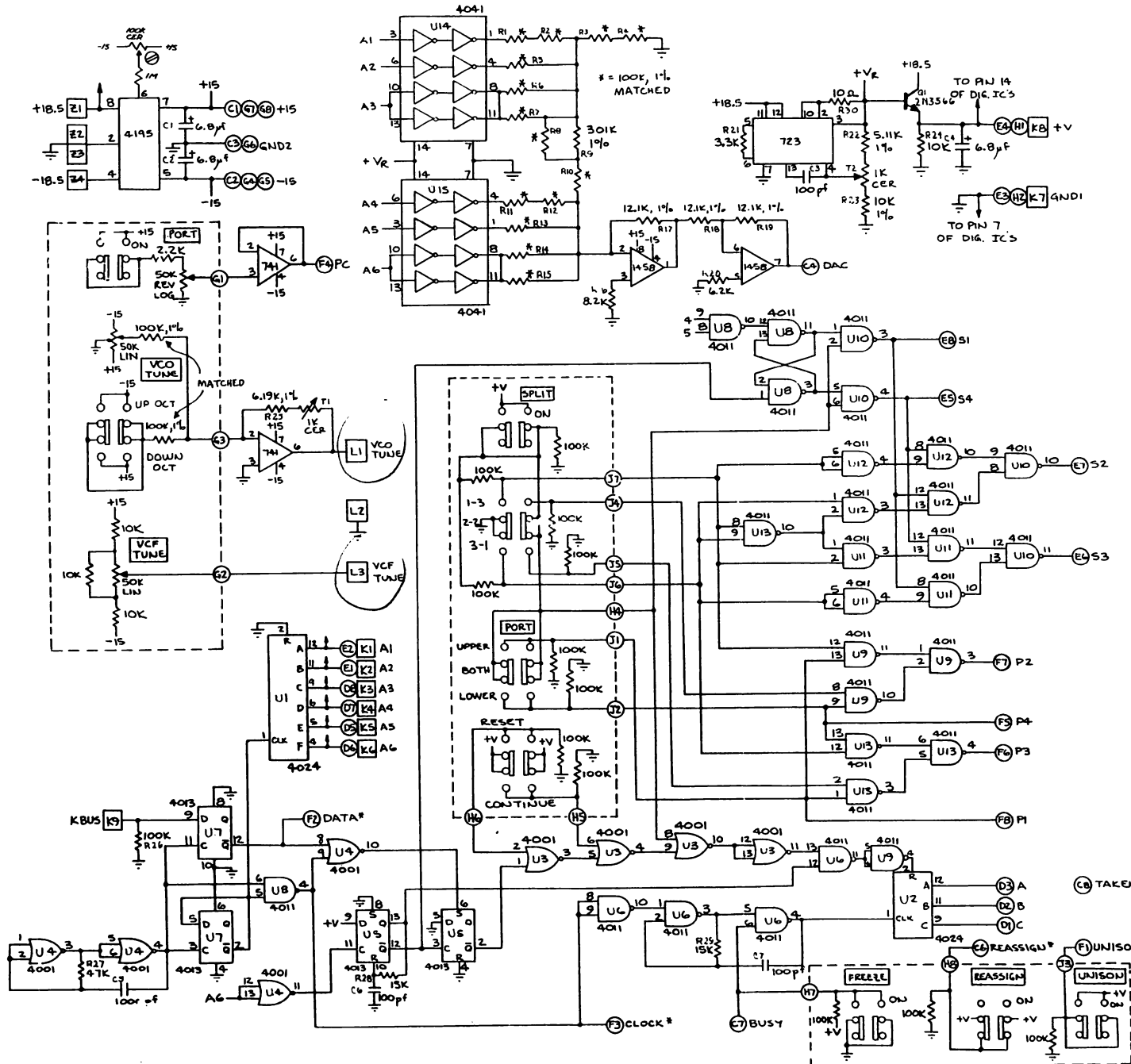
OBERHEIM ELECTRONICS, INC.		
SCALE	APPROVED BY:	DRAWN BY JEO
DATE 2/26/73		REVISED
SYNTHESIZER EXPANDER MODULE "A"		
SCHEMATIC - SEM-A		DRAWING NUMBER 1181



8 Voice KB

Connector "K"

K1	Color
2	Brown
3	Orange
4	Yellow
5	Green (light)
6	Blue (light)
7	Black
8	Red
9	Grey (light)



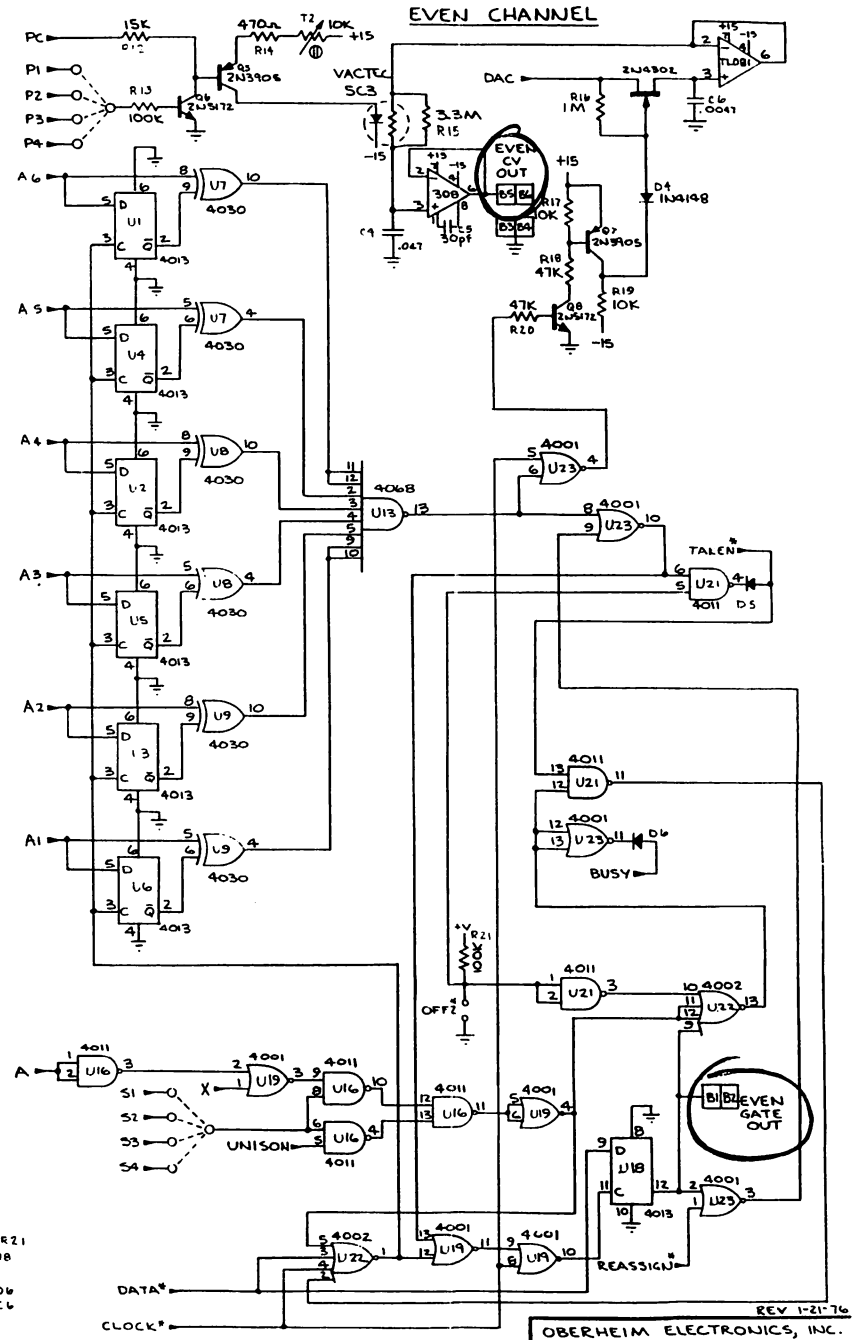
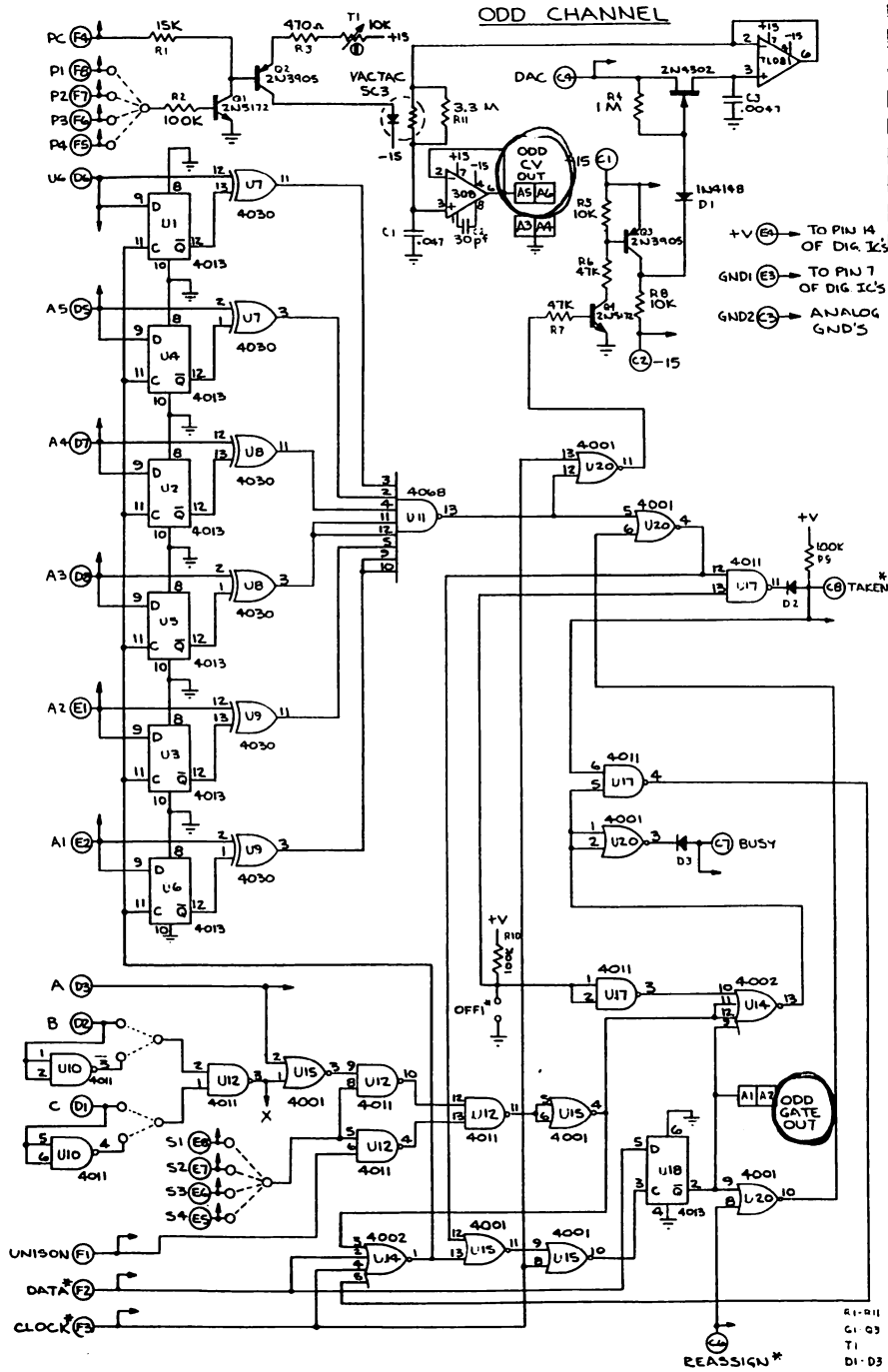
NOTES:
 (UNLESS SPECIFIED OTHERWISE)
 1. ALL RESISTORS ARE 1/4W, 5%
 2. ALL CAPACITORS IN μ F
 3. ALL NPN TRANSISTORS: 2N5172
 4. ALL PNP TRANSISTORS: 2N3905
 5. ALL DIODES: 1N4148
 6. ALL FET'S 2N4302

C1-C7
 D1
 T1 T2
 R1 R30

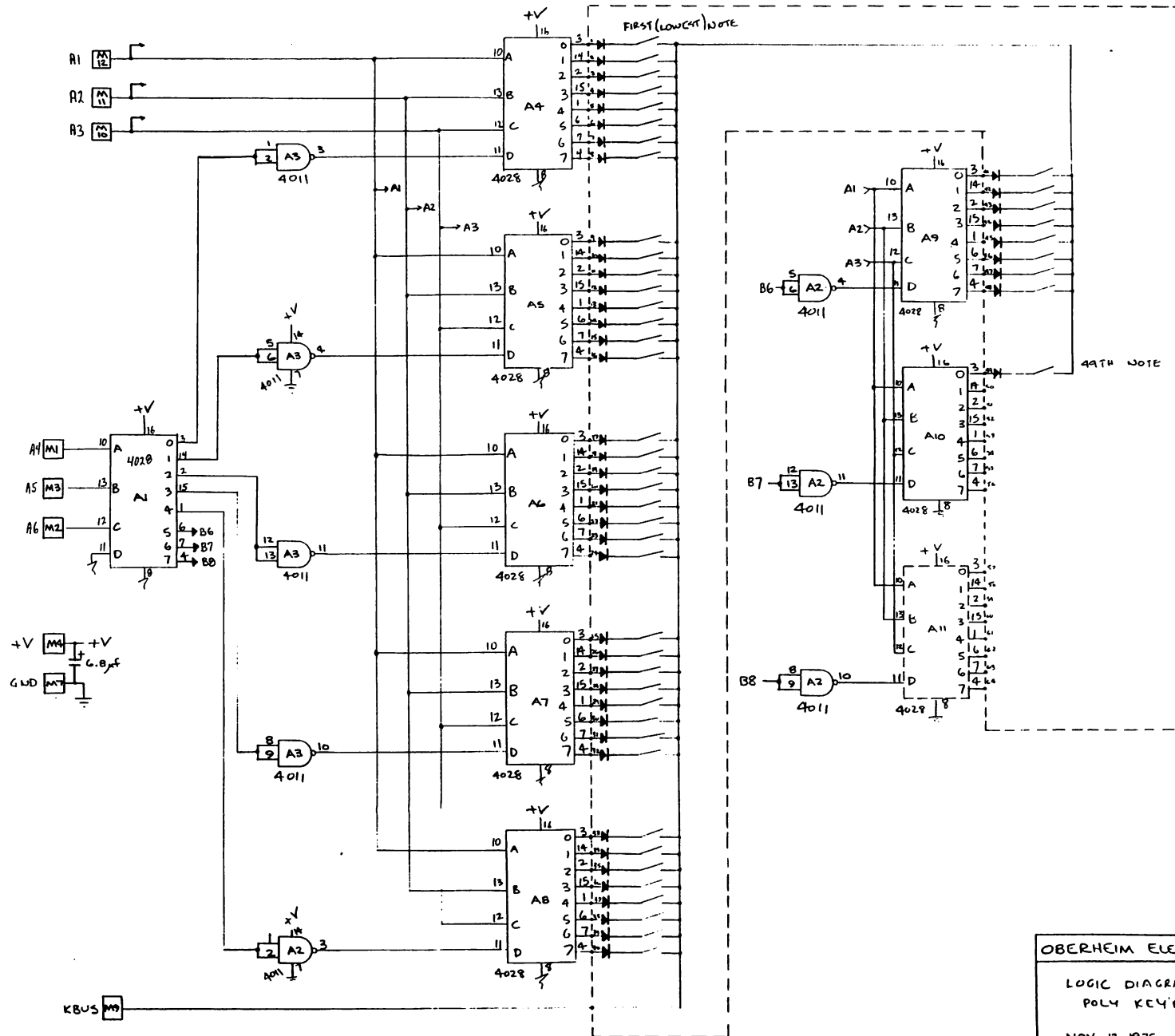
REV 8-1-79
 REV 1-21-76
 9-30-75

OBERHEIM ELECTRONICS, INC.
 POLYPHONIC KEYBOARD
 OVERHEAD LOGIC
 REVISED 3-19-79

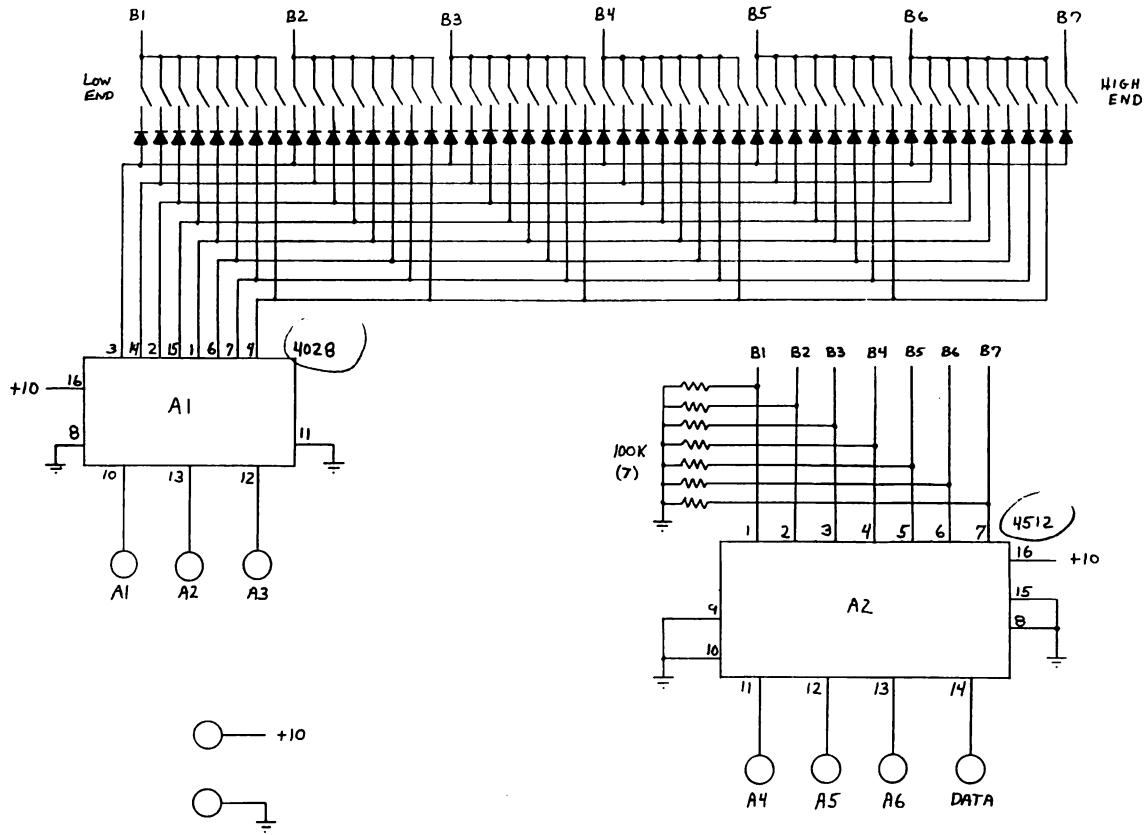
1112C



R1-R21
 05-08
 T2
 D4-D6
 C4-C6



OBERHEIM ELECTRONICS, INC.
 LOGIC DIAGRAM -
 POLY KEY'S DECODER
 NOV. 12, 1975
 1126

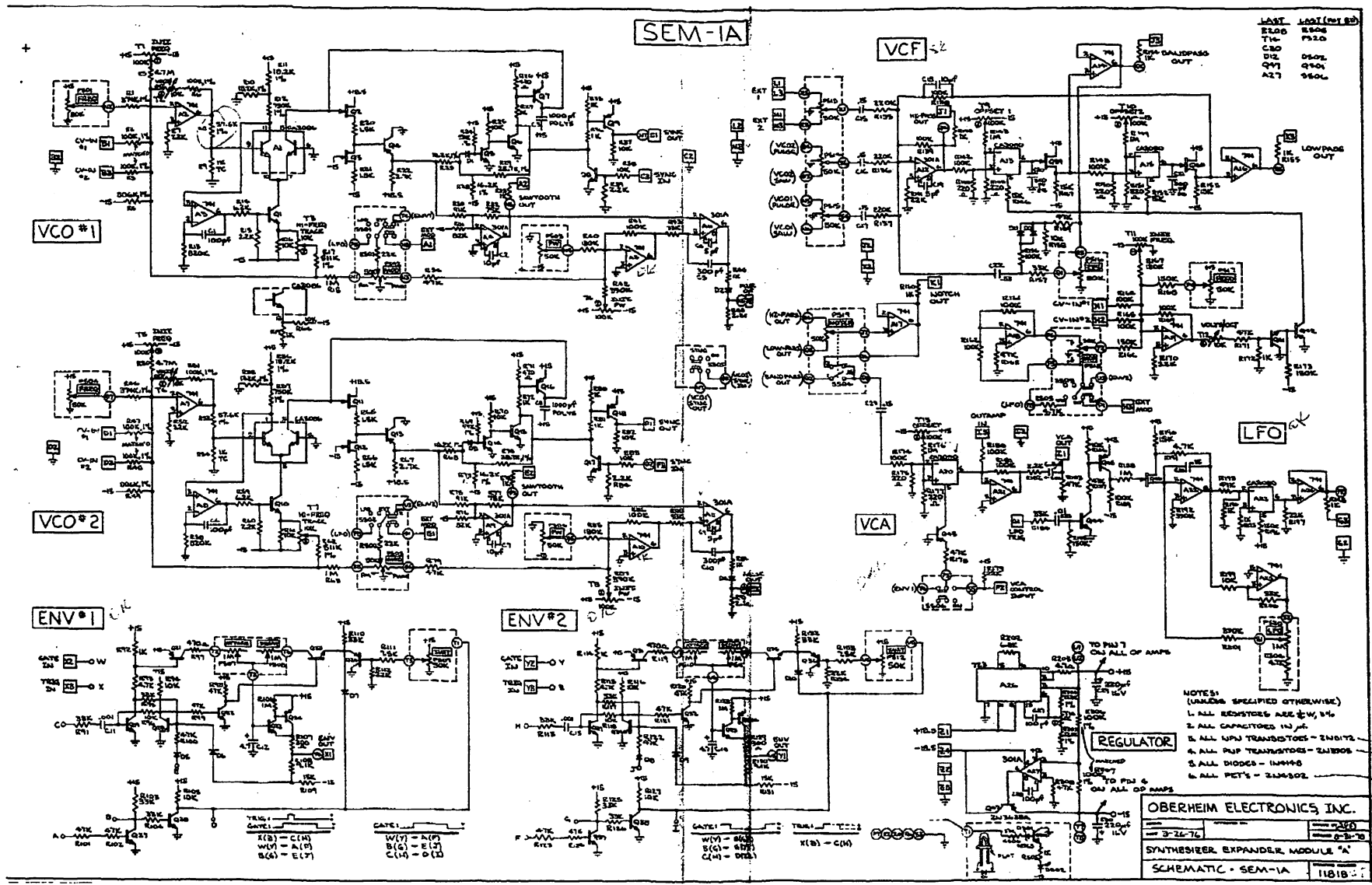


- NOTES:
- 1) A1 AND A2 TO BE MOUNTED IN SOCKETS
 - 2) ○ DENOTES SOLDERABLE PINS ON P.C BOARD
 - 3) ALL RESISTORS 5% 1/4 OR 1/2 WATT

OBERHEIM ELECTRONICS, INC

49 NOTE KEYBOARD
WITH DECODER
7/77

LAST	LAST (REV. ED)
E200	8508
A190	8320
E2	0507
E2	9301
9601	9601



- NOTES:
 1. UNLESS SPECIFIED OTHERWISE,
 L. ALL RESISTORS ARE 1/4 W, 5%
 2. ALL CAPACITORS IN μ F
 3. ALL LFP TRANSISTORS - 2N4012
 4. ALL PNP TRANSISTORS - 2N1606
 5. ALL DIODES - 1N4002
 6. ALL PCT'S - 2N3638

OBERHEIM ELECTRONICS, INC.
 9-24-76
 SYNTHESIZER EXPANDER MODULE "A"
 SCHEMATIC - SEM-1A 1181B