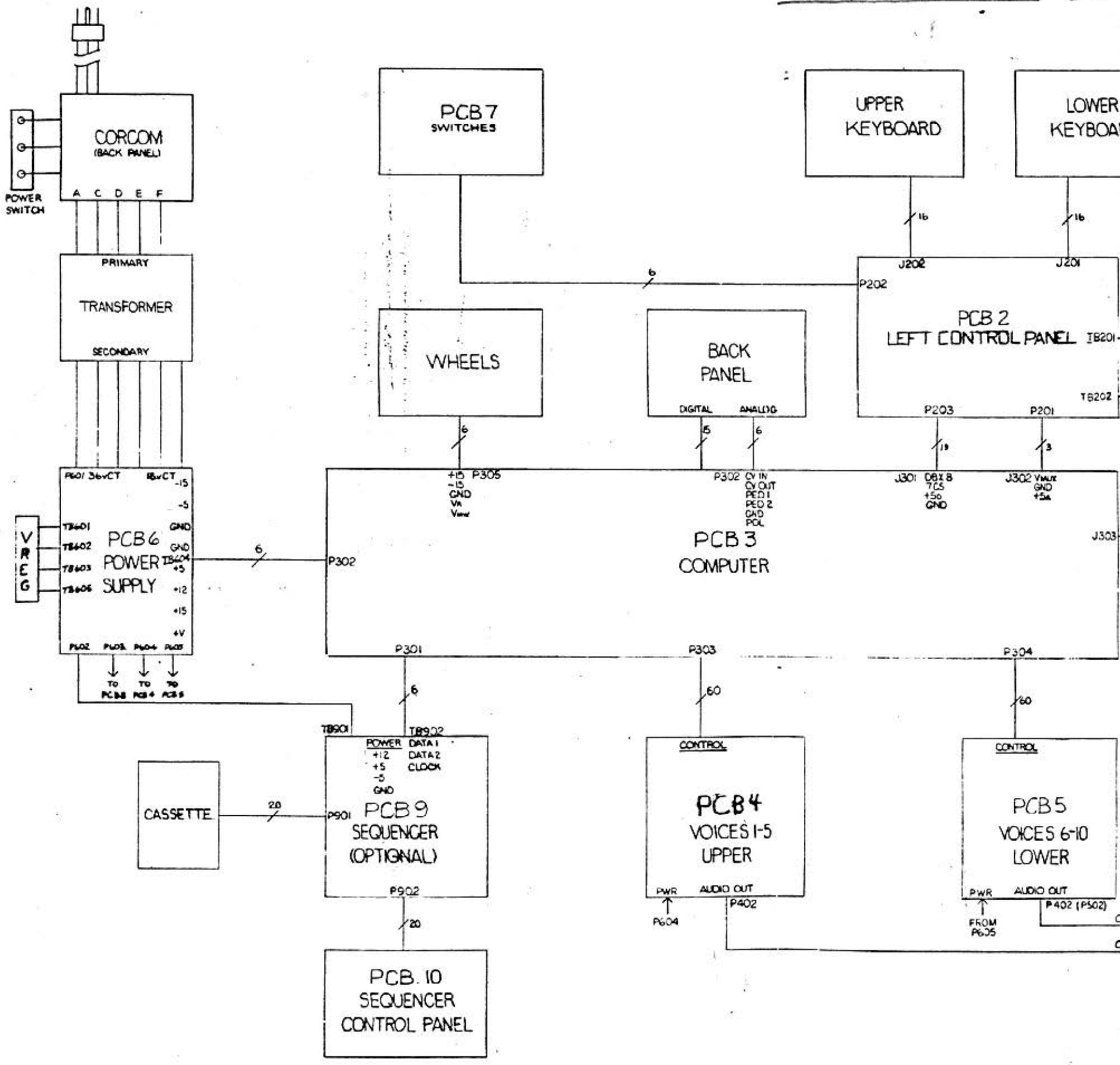
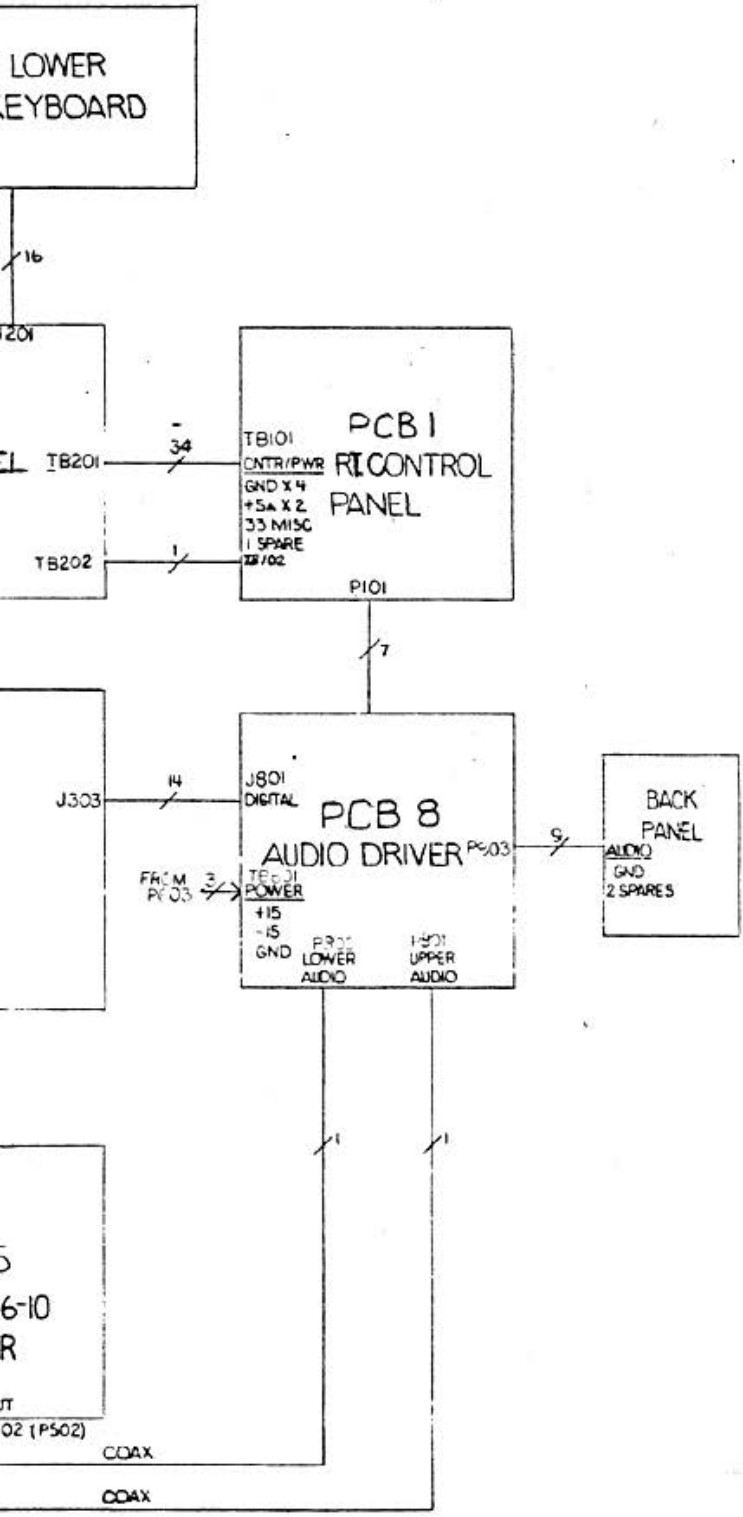

CORCOM
(BACK PANEL)
A C D E F
POWER SWITCH
PRIMARY
TRANSFORMER
SECONDARY
PCB7
SWITCHES
UPPER KEYBOARD
LOWER KEYBOA
16
16
J202
J201
P202
6
PCB 2
LEFT CONTROL PANEL
TB201
TB202
WHEELS
BACK PANEL
DIGITAL
ANALOG
P203
P201
6
15
6
19
3
P601 36vCT
15vCT
-15
-5
GND
GND
+5
+12
+15
+V
TB601
TB602
TB603
TB606
PCB6
POWER SUPPLY
VREG
P602
P603
P604
P605
TO PCB3
TO PCB4
TO PCB5
6
P302
+15 P305
-15
GND
VA
Vmw
P302 CV IN
CV OUT
PED 1
PED 2
GAD
POL
J301 DBX 8
7 CS
+5a
GND
J302 VMUX
GND
+5a
PCB 3
COMPUTER
J303
P301
P303
P304
6
60
60
TB901
TB902
POWER
+12
+5
-5
GND
DATA 1
DATA 2
CLOCK
CASSETTE
20
P901
PCB 9
SEQUENCER
(OPTIONAL)
P902
20
PCB. 10
SEQUENCER
CONTROL PANEL
CONTROL
PCB4
VOICES 1-5
UPPER
PWR
AUDIO OUT
P402
P604
CONTROL
PCB5
VOICES 6-10
LOWER
PWR
AUDIO OUT
P402 (P502)
FROM P605

(5) (SEQUENCER)

1



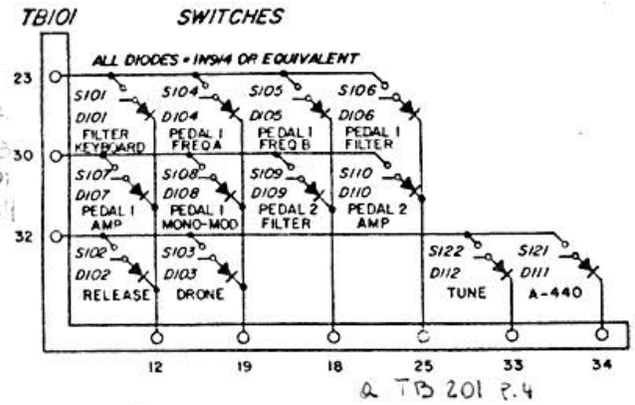
PRELIMINARY
REFER CHANGES TO ENGINEERING DEPT.

SEQUENTIAL CIRCUITS INC

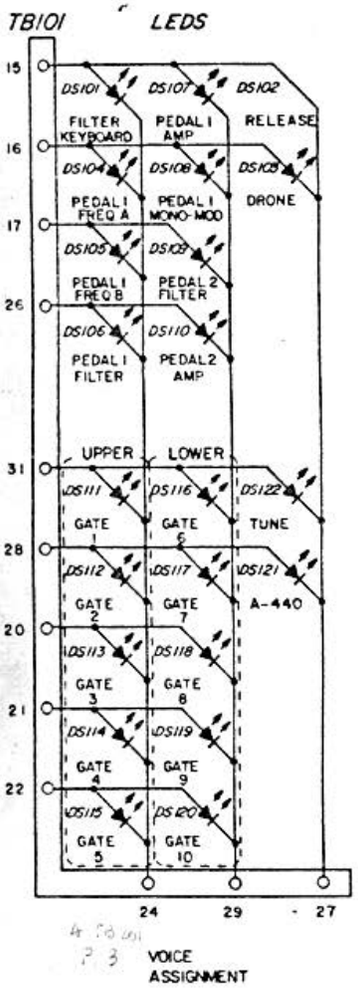
SYSTEM INTERCONNECT

D

1010

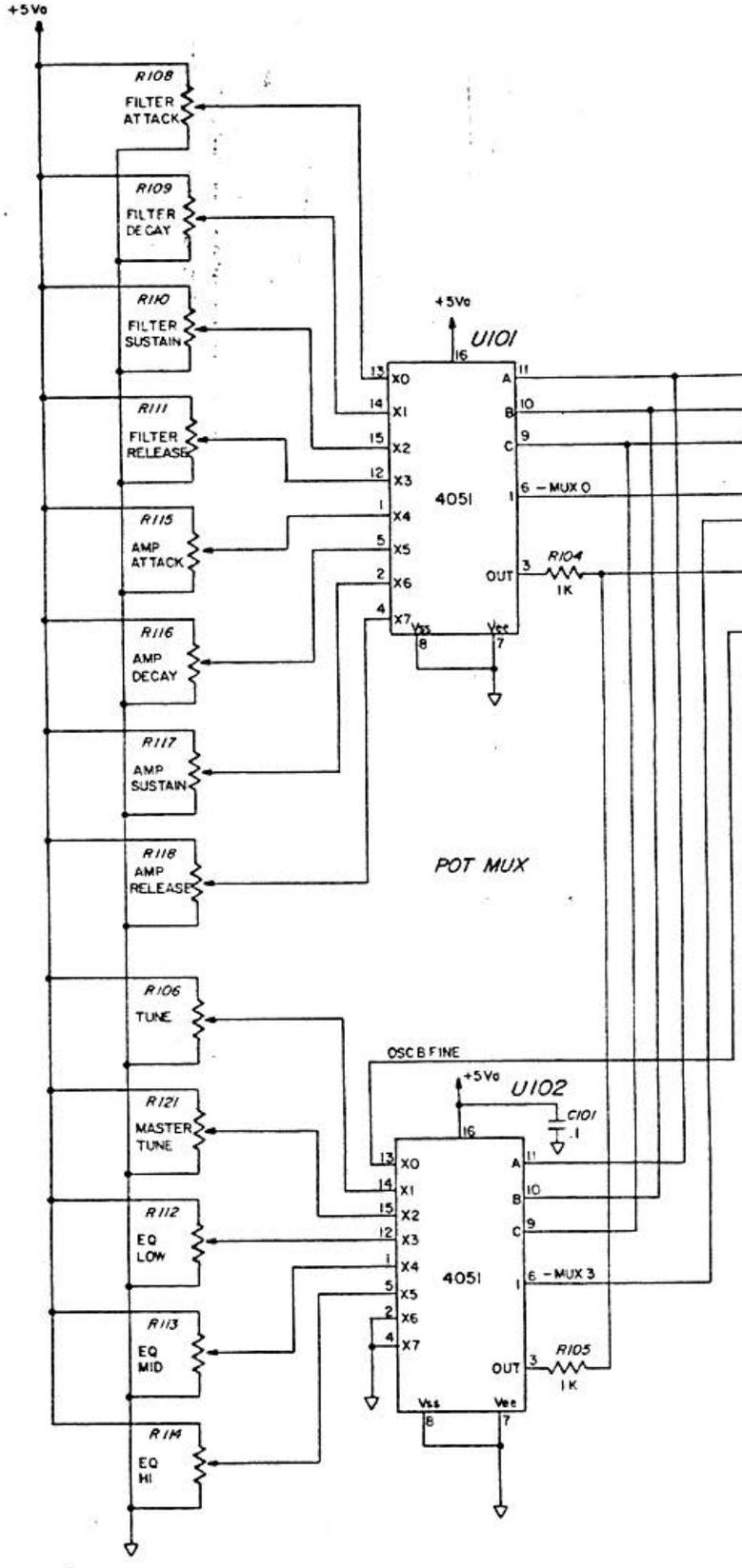


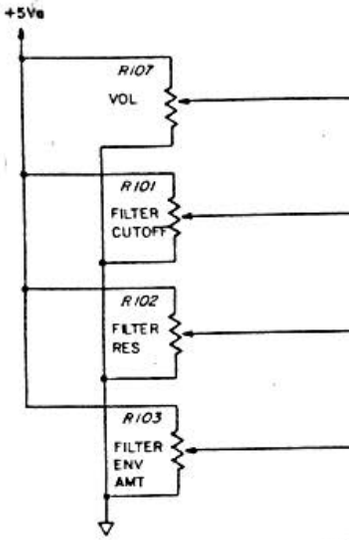
@ TB 201 P.4

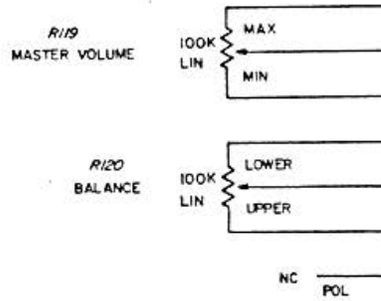


4 TB 201 P.3

VOICE ASSIGNMENT

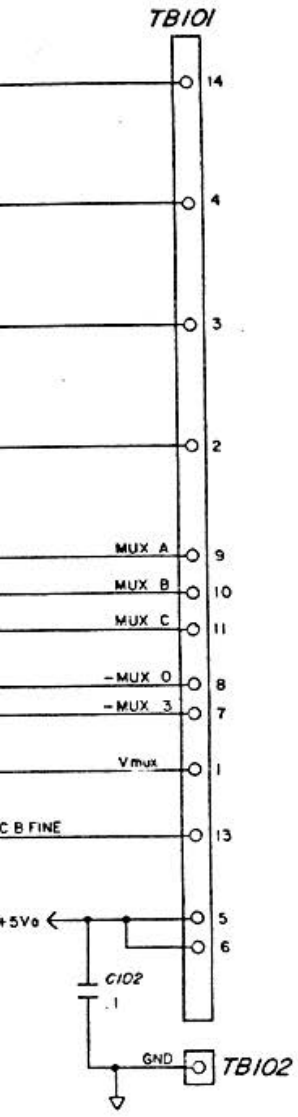


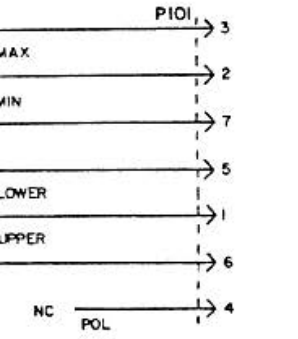




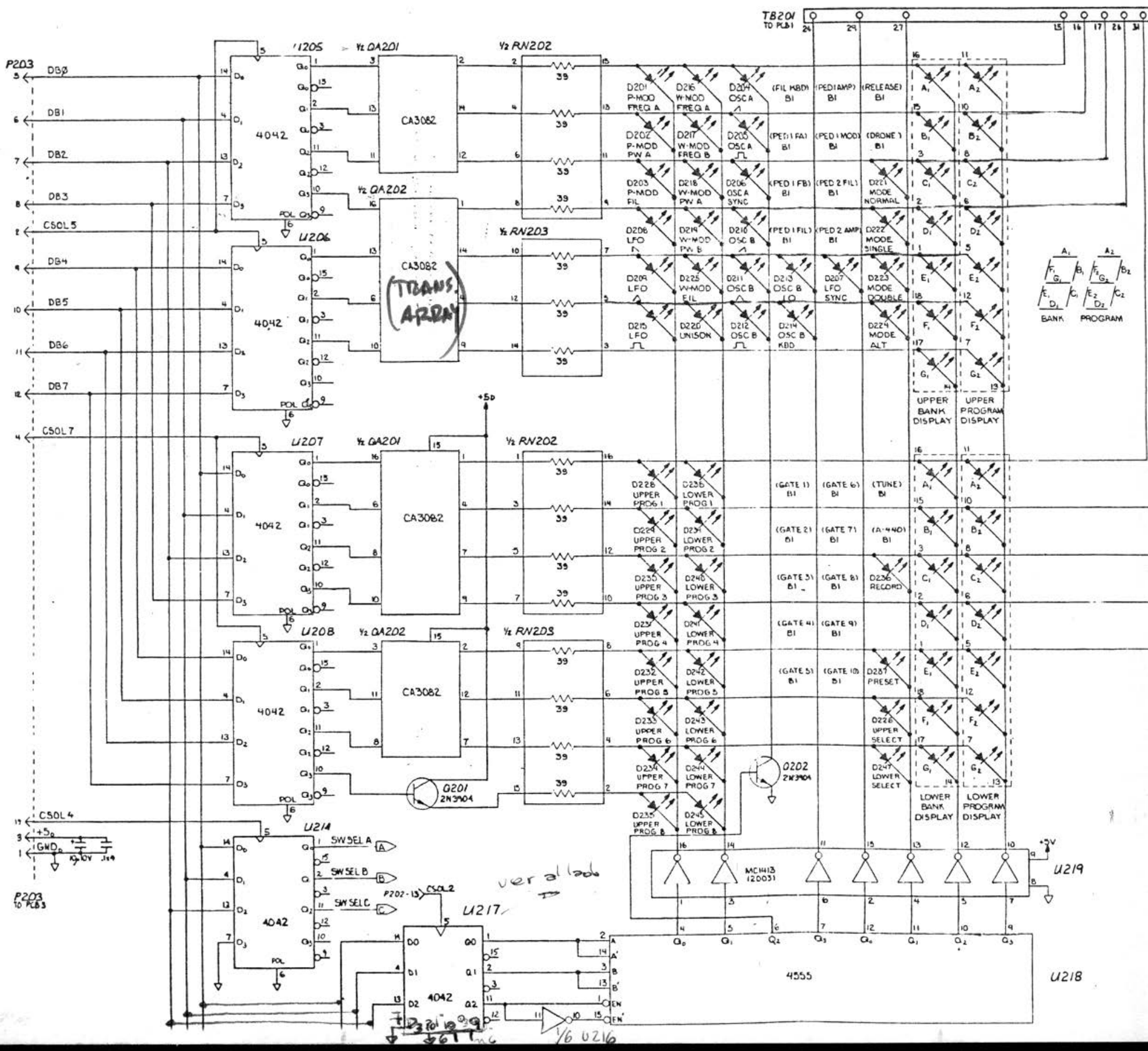
FRONT PANEL .
POTS. & leds

POTES: 10K LIN

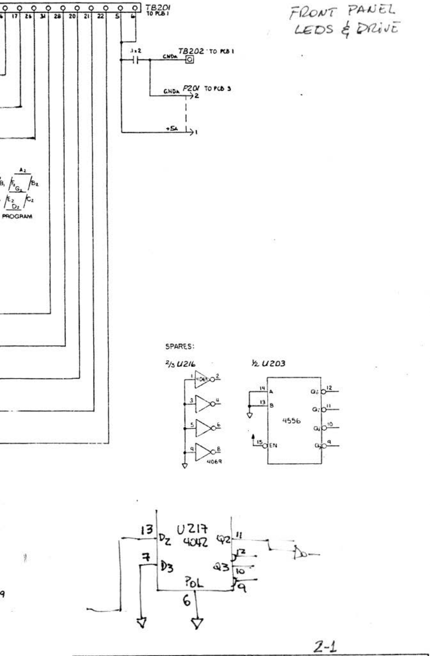




				SEQUENTIAL CIRCUITS INC	
				TITLE PCB1-POTMUX,SW,LED	
				DSN	SIZE D / DWG NO 1010
				DWG	DOCUMENT NO SD121 / REV 1
				APP	
DATE		REVISION		ISS	SHEET 1 OF 1


TB201 TO PCB1
P203
DB0
DB1
DB2
DB3
CSOL5
DB4
DB5
DB6
DB7
CSOL7
CSOL4
+5D
GND
P203 TO PCB3
U205
U206
U207
U208
U214
U217
U218
U219
4042
CA3082
½ QA201
½ QA202
½ RN202
½ RN203
39
D201 P-MOD FREQ A
D216 W-MOD FREQ A
D204 OSC A
D202 P-MOD PW A
D217 W-MOD FREQ B
D205 OSC A
D203 P-MOD FIL
D218 W-MOD PW A
D206 OSC A SYNC
D208 LFO
D219 W-MOD PW B
D210 OSC B
D209 LFO
D225 W-MOD FIL
D211 OSC B
D213 OSC B LO
D215 LFO
D220 UNISON
D212 OSC B
D214 OSC B KBD
(FIL KBD) BI
(PED1AMP) BI
(RELEASE) BI
(PED1 FA) BI
(PED1 MOD) BI
(DRONE) BI
(PED1 FB) BI
(PED 2 FIL) BI
D221 MODE NORMAL
(PED1 FIL) BI
(PED 2 AMP) BI
D222 MODE SINGLE
D207 LFO SYNC
D223 MODE DOUBLE
D224 MODE ALT
UPPER BANK DISPLAY
UPPER PROGRAM DISPLAY
BANK
PROGRAM
D226 UPPER PROG 1
D236 LOWER PROG 1
D227 UPPER PROG 2
D237 LOWER PROG 2
D230 UPPER PROG 3
D240 LOWER PROG 3
D229 UPPER PROG 4
D241 LOWER PROG 4
D232 UPPER PROG 5
D242 LOWER PROG 5
D233 UPPER PROG 6
D243 LOWER PROG 6
D234 UPPER PROG 7
D244 LOWER PROG 7
D235 UPPER PROG 8
D245 LOWER PROG 8
(GATE 1) BI
(GATE 6) BI
(TUNE) BI
(GATE 2) BI
(GATE 7) BI
(A-440) BI
(GATE 3) BI
(GATE 8) BI
D236 RECORD
(GATE 4) BI
(GATE 9) BI
(GATE 5) BI
(GATE 10) BI
D251 PRESET
D226 UPPER SELECT
D247 LOWER SELECT
LOWER BANK DISPLAY
LOWER PROGRAM DISPLAY
Q201 2N3904
Q202 2N3904
MC14013 120031
4555
SW SEL A
SW SEL B
SW SEL C
P202-13 CSOL2
+5V
(TRANS ARRAY)

FRONT PANEL
LEDS & DRIVE



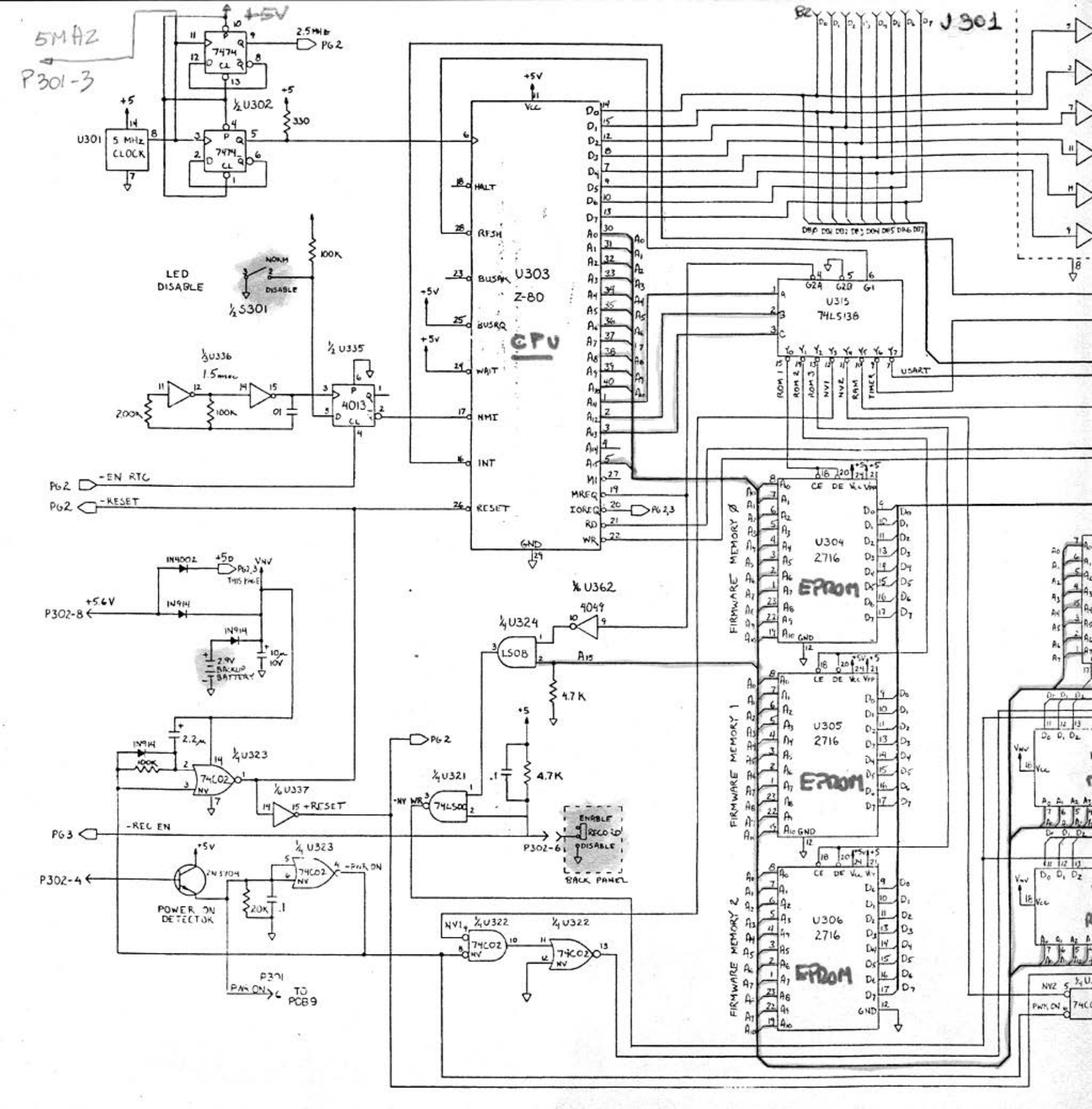
2-1

SEQUENTIAL CIRCUITS INC

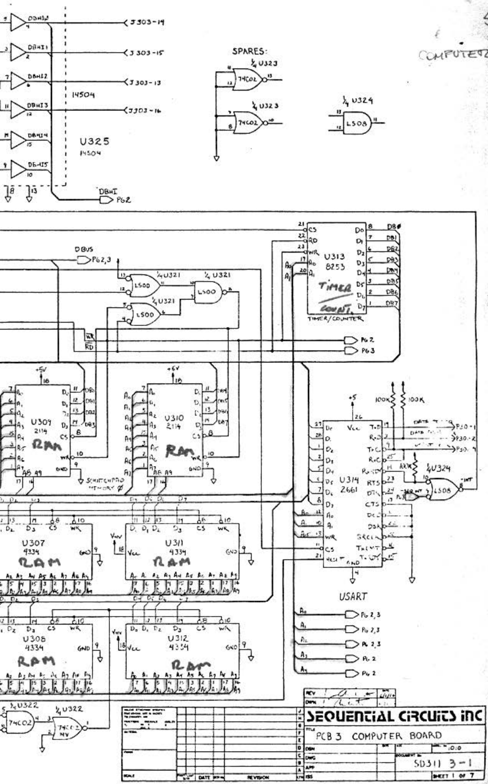
PCB2 (LEFT CONTROL PANEL)

D

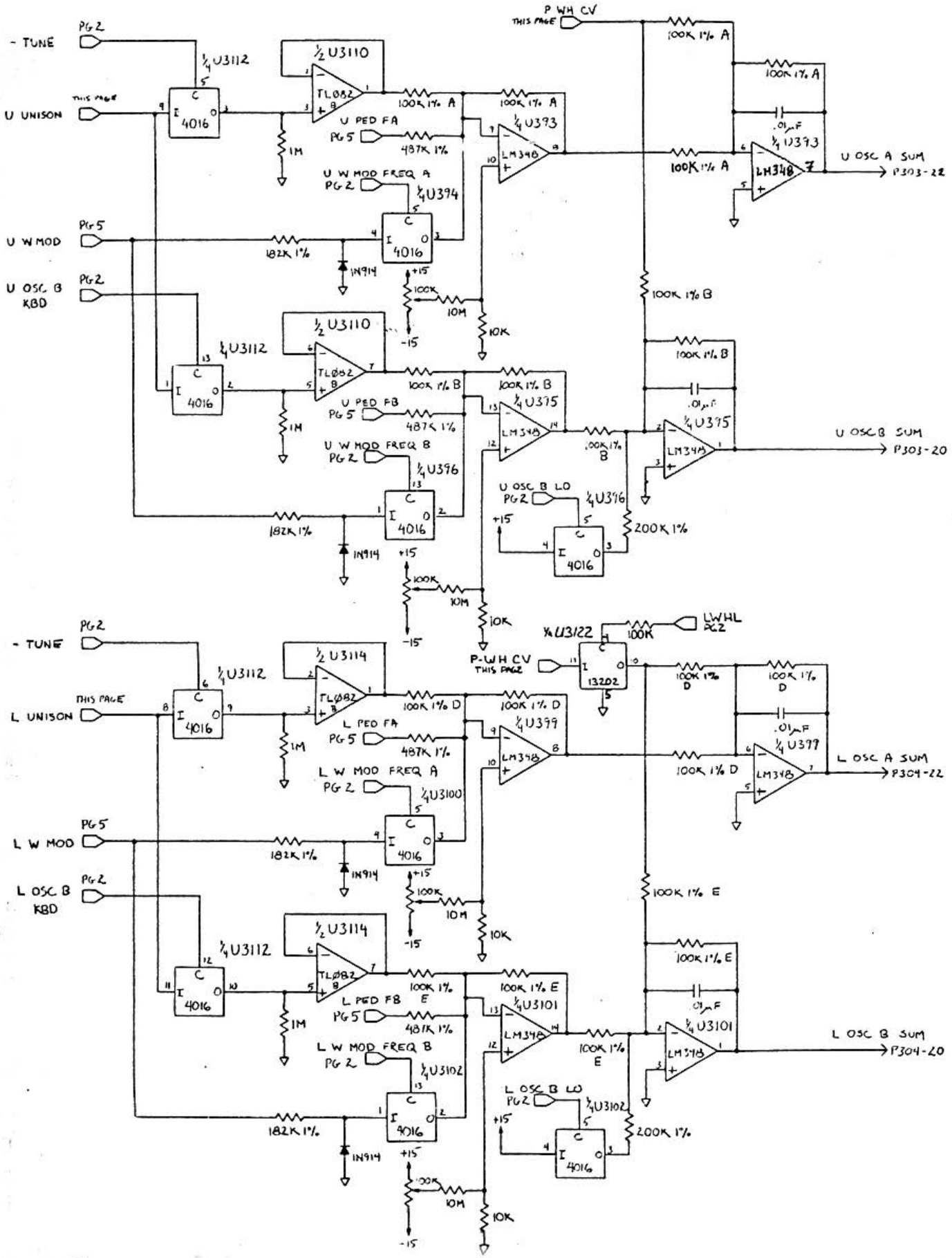
1010

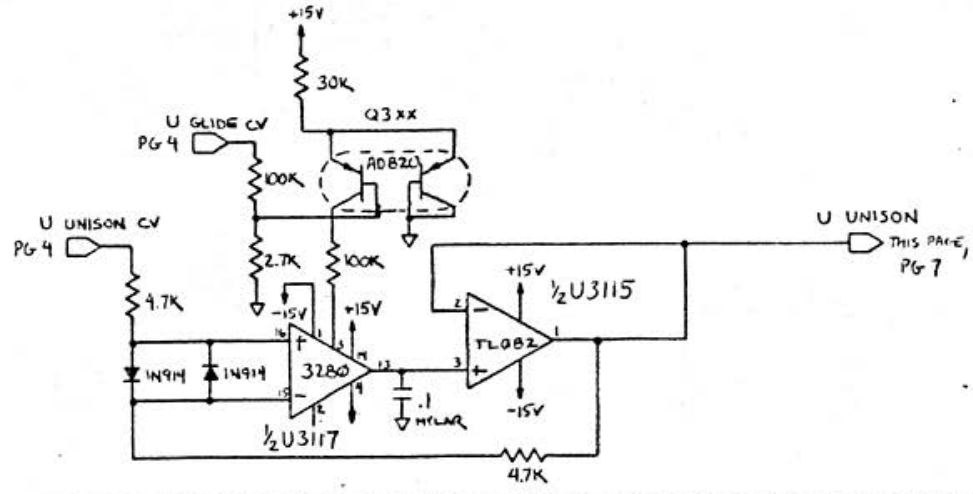

5MHZ
P301-3
+5V
2.5MHz
PG2
7474
½U302
330
U301
5 MHz CLOCK
LED DISABLE
NORM
DISABLE
½S301
100K
⅓U336
1.5msec
200K
100K
.01
½ U335
4013
U303
Z-80
CPU
HALT
REFSH
BUSAK
BUSRQ
WAIT
NMI
INT
RESET
MREQ
IORQ
RD
WR
PG2,3
GND
PG2
-EN RTC
PG2
-RESET
J301
U315
74LS138
USART
ROM 1
ROM 2
ROM 3
NVI
NVR
RAM
TIMER
U304
2716
EPROM
FIRMWARE MEMORY 0
U305
FIRMWARE MEMORY 1
U306
FIRMWARE MEMORY 2
IN4002
IN914
+5D
VNV
THIS PAGE
+5.6V
P302-8
2.9V BACKUP BATTERY
10µ 10V
2.2µ
100K
¼U323
74C02
NV
½U337
+RESET
-REC EN
PG3
¼U324
LS08
½U362
4049
A15
4.7K
¼U321
74LS00
-NV WR
.1
4.7K
P302-6
ENABLE
RECORD
DISABLE
BACK PANEL
2N5704
POWER ON DETECTOR
20K
.1
+PWR ON
P302-4
¼U322
¼U322
NV1
P301
PWR ON
TO PCB9

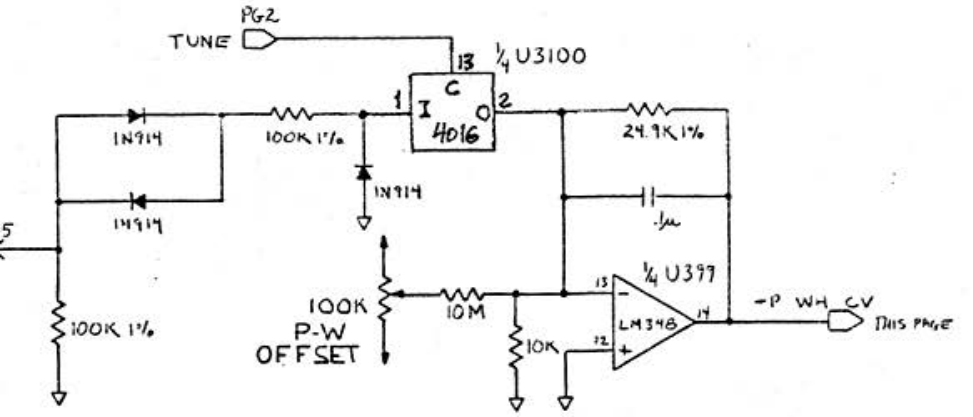
COMPUTER

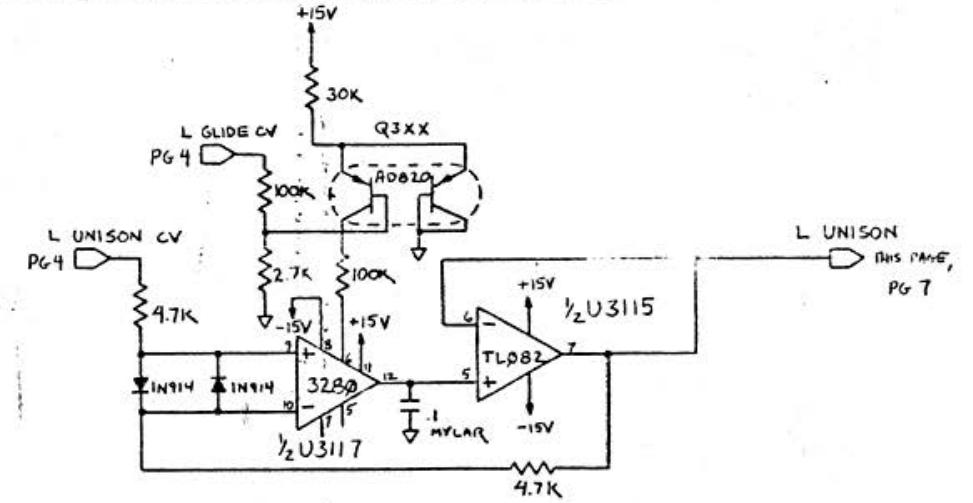


SPARES:
¼ U323
74C02
¼ U324
LS08
U325
14504
DBHI
P62
DBUS
P62,3
¼ U321
LS00
U313
8253
TIMER COUNT
TIMER/COUNTER
WR
RD
PG 2
PG 3
U309
2114
RAM
U310
2114
RAM
SCRATCHPAD MEMORY Ø
U307
4334
RAM
U311
4334
RAM
U308
4334
RAM
U312
4334
RAM
U314
2661
USART
100K
¼ U324
LS08
INT
¼ U322
74C02
SEQUENTIAL CIRCUITS INC
PCB 3 COMPUTER BOARD
SD311 3-1
SHEET 1 OF 7

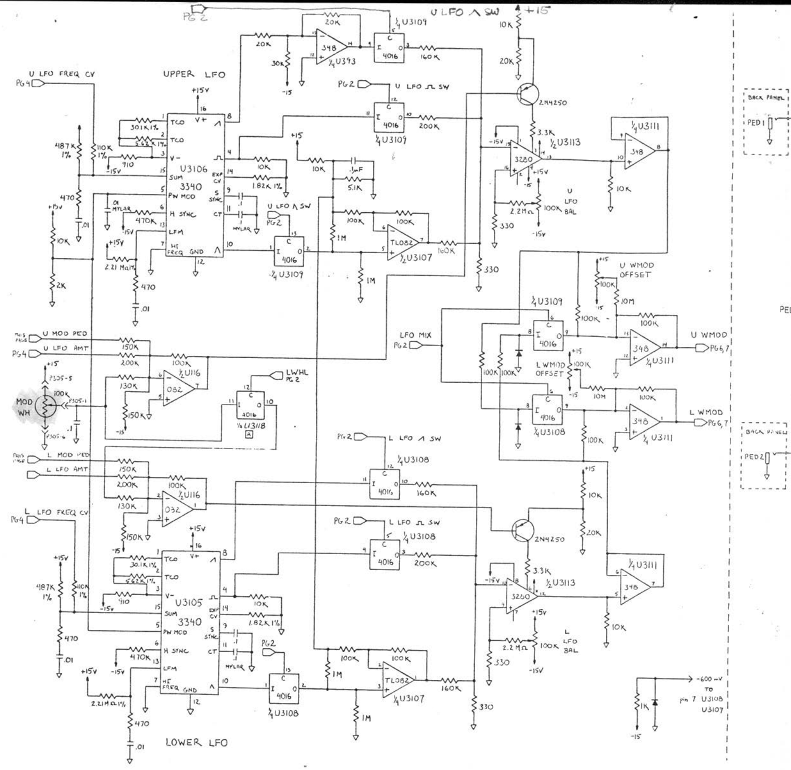

- TUNE
PG2
¼U3112
½U3110
TL082
4016
U UNISON
THIS PAGE
1M
U PED FA
PG5
487K 1%
¼U393
LM348
P WH CV
THIS PAGE
100K 1% A
.01µF
¼U393
U OSC A SUM
P303-22
U W MOD FREQ A
PG2
¼U394
U W MOD
PG5
182K 1%
1N914
+15
100K
10M
10K
-15
U OSC B KBD
PG2
100K 1% B
¼U375
U OSC B SUM
P303-20
U PED FB
U W MOD FREQ B
¼U396
U OSC B LO
200K 1%
- TUNE
½U3114
L UNISON
L PED FA
L W MOD FREQ A
¼U3100
L W MOD
L OSC B KBD
100K 1% D
¼U399
L OSC A SUM
P304-22
100K 1% E
¼U3101
L OSC B SUM
P304-20
L PED FB
L W MOD FREQ B
¼U3102
L OSC B LO
¼U3122
LWHL
PG2
P-WH CV
13202
4.7K
P305
100K
PITCH WHEEL
PW TRIM
10K
SPARES
¼ U3122



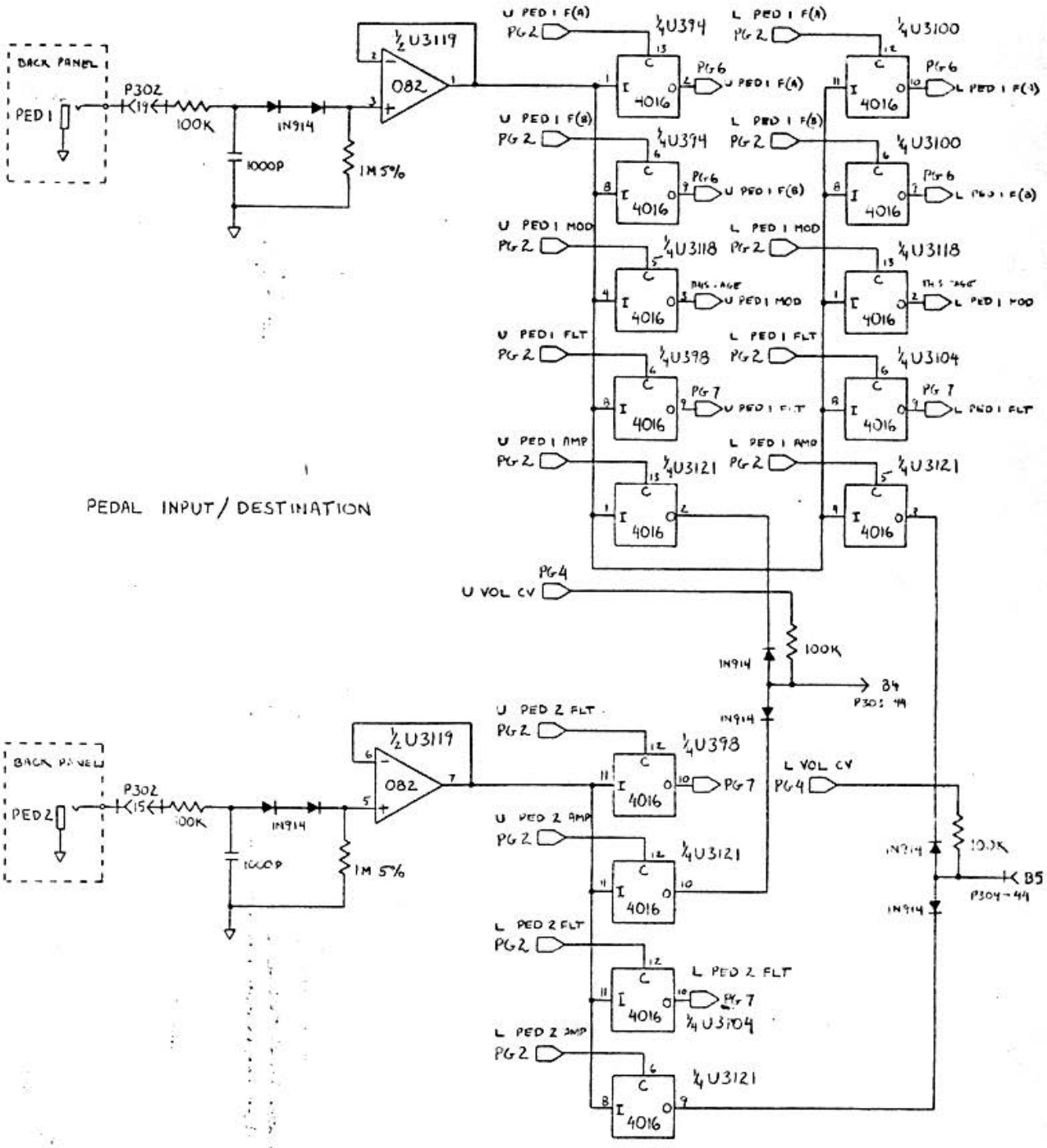




REV			
DWN			
	SEQUENTIAL CIRCUITS INC		
	COMMON AND 206		3-6
DSN		SIZE D	4010
DWG		DOCUMENT NO.	

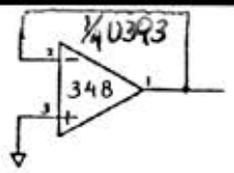


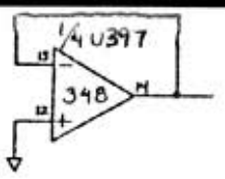
UPPER LFO
U LFO FREQ CV
PG4
U3106
3340
U LFO Λ SW
PG2
U LFO ⊓ SW
2N4250
½ U3113
3280
U LFO BAL
¼ U3109
4016
¼ U393
348
TL082
½ U3107
¼ U3111
U WMOD OFFSET
L WMOD OFFSET
LFO MIX
U WMOD
PG6,7
L WMOD
¼ U3108
U MOD PED
THIS PAGE
U LFO AMT
MOD WH
½ U116
082
LWHL
¼ U3118
L MOD PED
L LFO AMT
L LFO FREQ CV
L LFO Λ SW
L LFO ⊓ SW
U3105
LOWER LFO
L LFO BAL
BACK PANEL
PED1
PED2
-600 mV TO pin 7 U3108 U3109

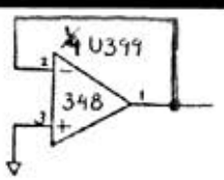


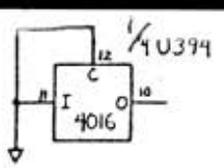
							J	REV	
							H	DRN	
							G	SEQUENTIAL CIRCUITS INC	
							F	TITLE COMMON ANALOG II	
							E		SIZE D
							D	DSN	
							C	DWG	DOCUMENT NO. SD 311 3-5
							B	APP	
							A	ISS	SHEET 5 OF 7
SCALE		9/11/80	JJ ¼ U3118					DATE	REVISION

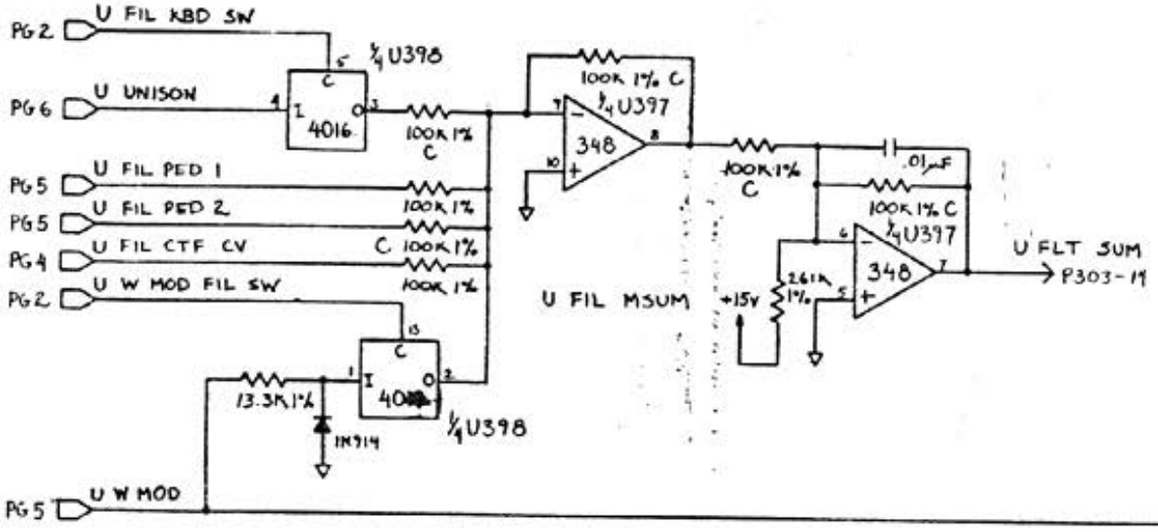
SPARES:

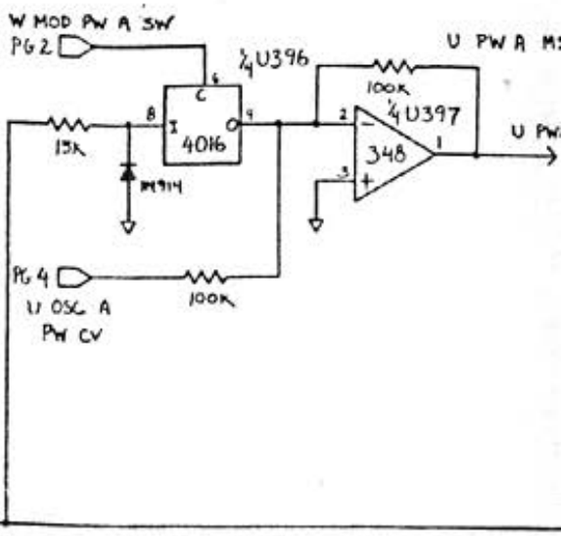


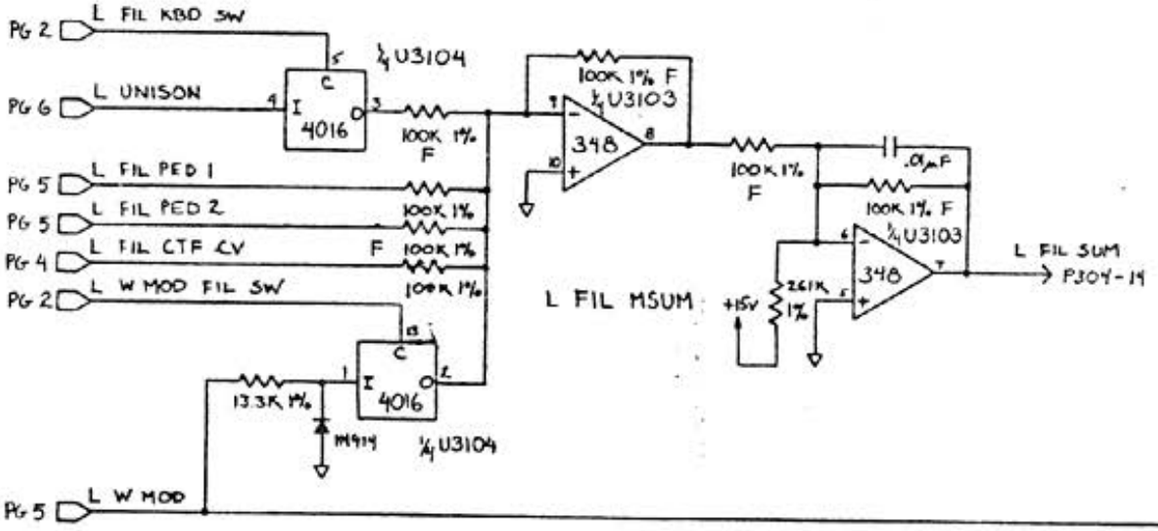


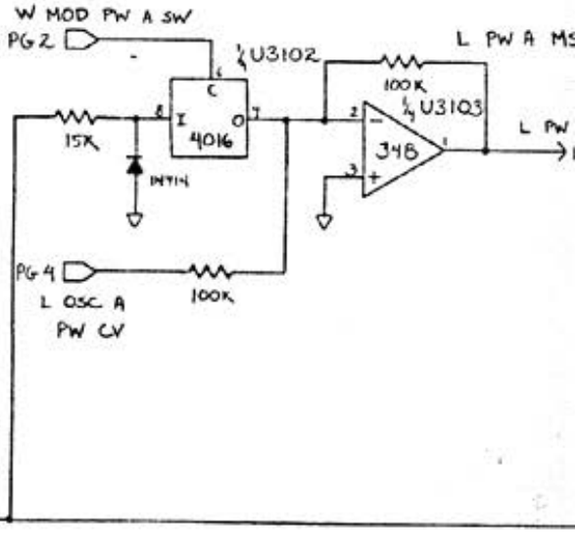


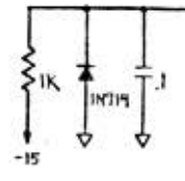






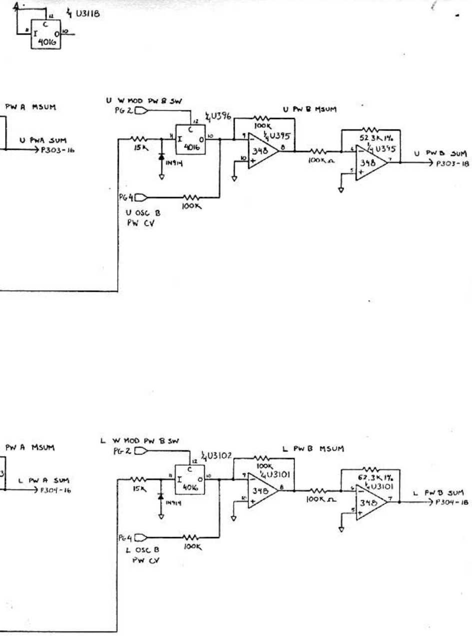






-600 mV
pin 7

U394	U3100
U396	U3102
U398	U3104



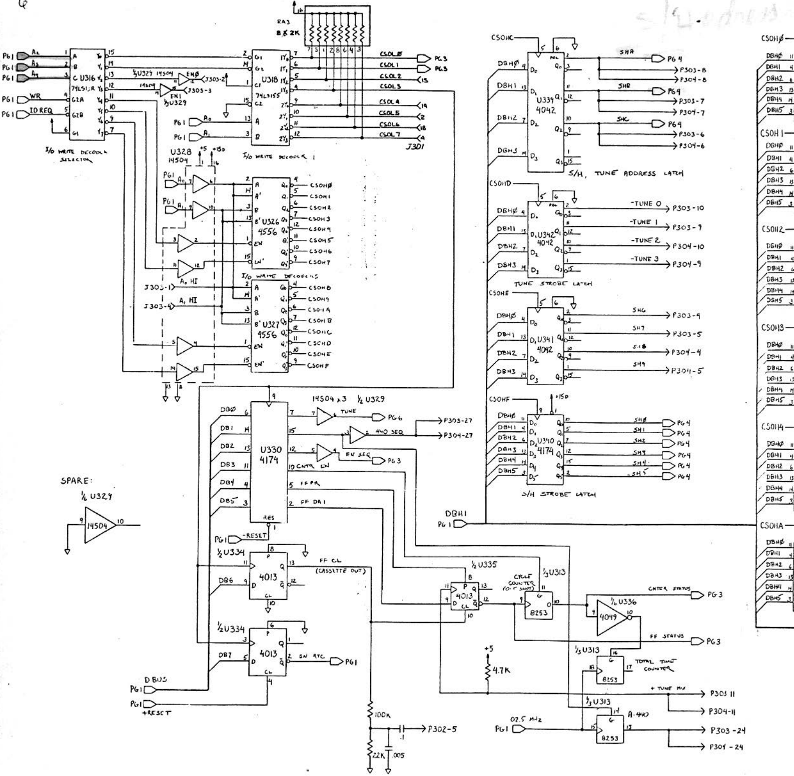
SEQUENTIAL CIRCUITS INC

COMMON ANALOG II 3-7

D 1010

SD311

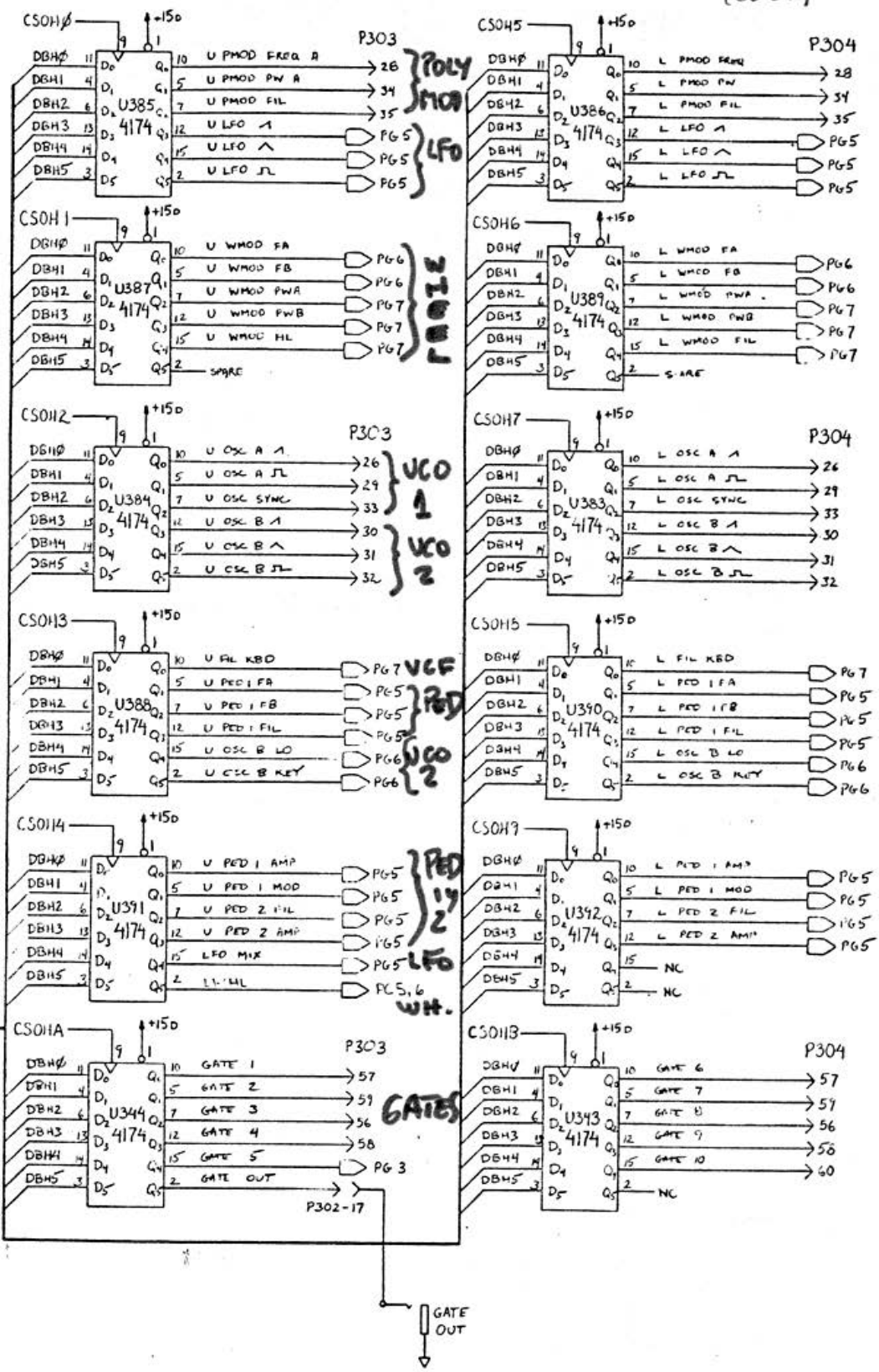
SHEET 7 OF 7



I/O WRITE DECODER SELECTOR
I/O WRITE DECODER 1
I/O WRITE DECODERS
J301
RA3 8 X 2K
U316 74LS138
U318 74LS155
U326 4556
U327 4556
U328 14504
U339 4042
S/H, TUNE ADDRESS LATCH
U342 4042
TUNE STROBE LATCH
U341 4042
U340 4174
S/H STROBE LATCH
U330 4174
14504 x3 ½ U329
TUNE
440 SEQ
EN SEQ
FF PR
FF DA1
SPARE:
⅙ U329
14504
½ U334
4013
FF CL (CASSETTE OUT)
EN RTC
D BUS
+RESET
½ U335
⅓ U313
CYCLE COUNTER (ONE SHOT)
8253
CNTR STATUS
⅙ U336
4049
FF STATUS
TOTAL TIME COUNTER
+ TUNE MIX
A-440
02.5 MHz
4.7K
100K
22K
.005
.1

SWITCHES STATUS

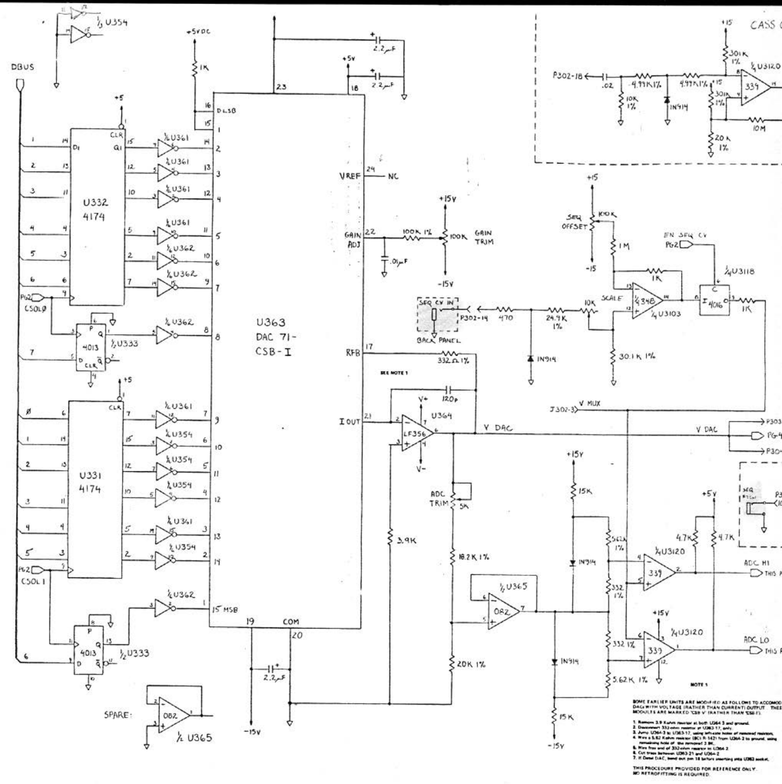
COMPUTER (CONT)



SEQUENTIAL CIRCUITS INC

PCB3 COMPUTER BOARD 3-2

1010

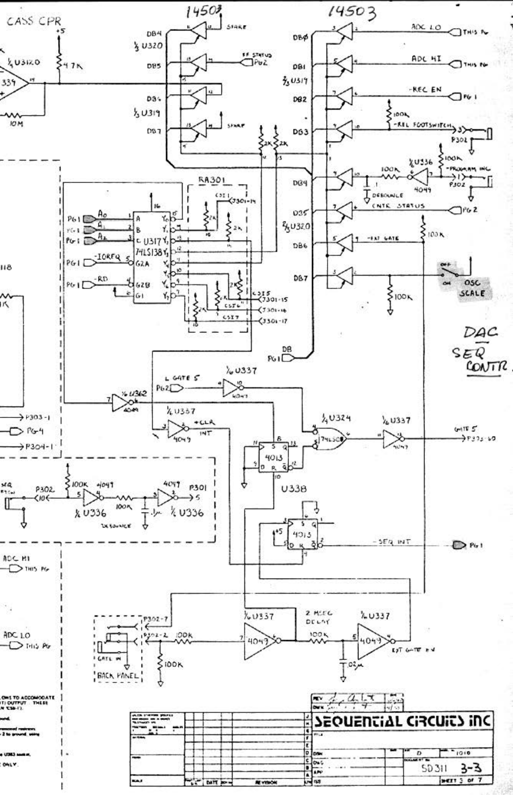


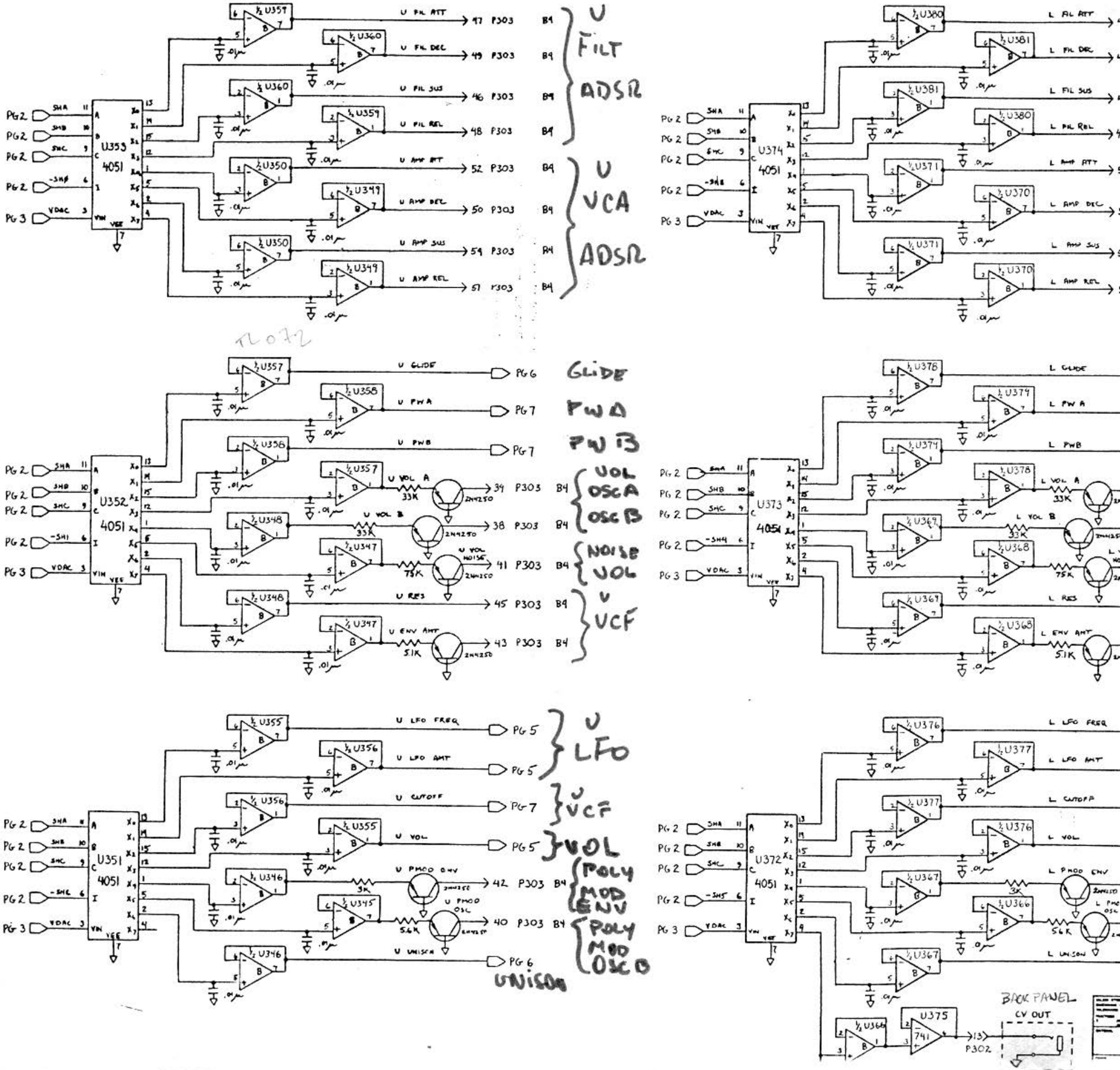
NOTE 1

SOME EARLIER UNITS ARE MODIFIED AS FOLLOWS TO ACCOMOD[ATE] DACs WITH VOLTAGE (RATHER THAN CURRENT) OUTPUT. THES[E] MODULES ARE MARKED 'CSB-V' (RATHER THAN 'CSB-I').

1. Remove 3.9 Kohm resistor at both U364-3 and ground.
2. Disconnect 332-ohm resistor at U363-17, only.
3. Jump U364-3 to U363-17, using left-side holes of removed resistors.
4. Wire a 5.62 Kohm resistor (BC) R-142) from U364-2 to ground, using remaining hole of the removed 3.9K.
5. Wire free end of 332-ohm resistor to U364-2.
6. Cut trace between U363-21 and U364-2.
7. If Datel DAC, bend out pin 18 before inserting into U363 socket.

THIS PROCEDURE PROVIDED FOR REFERENCE ONLY.
NO RETROFITTING IS REQUIRED.


CASS CPR
14503
14503
DB4
1/3 U320
DB5
DB6
1/3 U319
DB7
STARE
FF STATUS
PG2
DBØ
DB1
2/3 U319
DB2
DB3
DB4
DB5
2/3 U320
DB6
DB7
ADC LO
ADC HI
THIS PG
-REC EN
PG 1
-REL FOOTSWITCH
P302
-PROGRAM INC
1/6 U336
4049
DEBOUNCE
CNTR STATUS
PG 2
-EXT GATE
OFF
ON
OSC SCALE
100K
1/4 U3120
339
4.7K
10M
RA301
U317
74LS138
A0
A1
A2
-IORFQ
-RD
G2A
G2B
G1
PG1
CS15
CS16
CS17
J301-14
J301-15
J301-16
J301-17
DAC
SEQ
CONTR
DB
PG1
L GATE 5
1/6 U337
1/6 U362
1/6 U337
+CLR INT
1/4 U324
74LS08
4013
GATE 5
P303-60
U338
-SEQ INT
P303-1
PG-4
P304-1
P302
P301
1/6 U336
DEBOUNCE
ADC HI
THIS PG
ADC LO
THIS PG
P302-7
P302-2
GATE IN
BACK PANEL
2 MSEC DELAY
.02µ
EXT GATE EN
SEQUENTIAL CIRCUITS INC
REV
DWN
DSN
CHK
APP
TITLE
D
1010
SD311 3-3
SHEET 3 OF 7
DATE
REVISION


U FIL ATT
U FIL DEC
U FIL SUS
U FIL REL
U AMP ATT
U AMP DEC
U AMP SUS
U AMP REL
U FILT ADSR
U VCA ADSR
L FIL ATT
L FIL DEC
L FIL SUS
L FIL REL
L AMP ATT
L AMP DEC
L AMP SUS
L AMP REL
TL072
U GLIDE
U PW A
U PW B
GLIDE
PW A
PW B
L GLIDE
L PW A
L PW B
U VOL A
U VOL B
U VOL NOISE
U RES
U ENV AMT
VOL OSC A
VOL OSC B
VOL NOISE
U VCF
L VOL A
L VOL B
L RES
L ENV AMT
U LFO FREQ
U LFO AMT
U CUTOFF
U VOL
U PMOD ENV
U PMOD OSC
U UNISON
U LFO
U VCF
U VOL
POLY MOD ENV
POLY MOD OSC
UNISON
L LFO FREQ
L LFO AMT
L CUTOFF
L VOL
L PMOD ENV
L PMOD OSC
L UNISON
BACK PANEL
CV OUT
U353 4051
U352 4051
U351 4051
U374 4051
U373 4051
U372 4051
U375 741
2N4250
33K
75K
5.1K
3K
5.6K

COMPUTER
S&H

→ 47 P304 B5

→ 49 P304 B5

→ 46 P304 B5

→ 48 P304 B5

→ 52 P304 B5

→ 50 P304 B5

→ 54 P304 B5

→ 51 P304 B5

→ PG 6

→ PG 7

→ PG 7

→ 39 P304 B5

→ 38 P304 B5

L VOL NOISE → 41 P304 B5

→ 45 P304 B5

→ 43 P304 B5

FREQ → PG 5

→ PG 5

→ PG 7

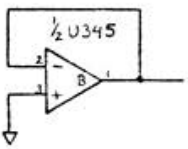
→ PG 5

ENV → 42 P304 B5

L PMOD OSC → 40 P304 B5

→ PG 6

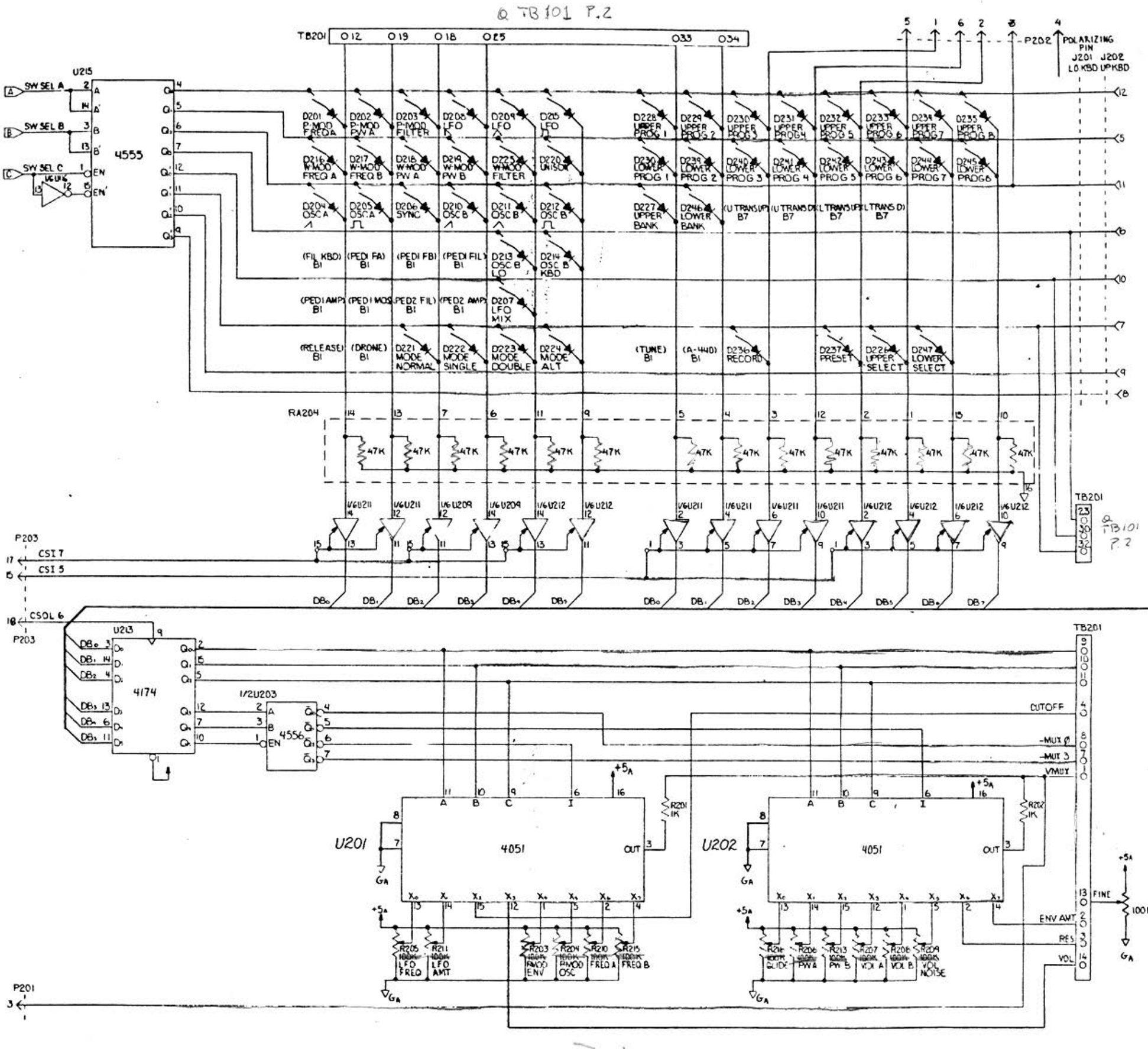
SPARE



SEQUENTIAL CIRCUITS INC

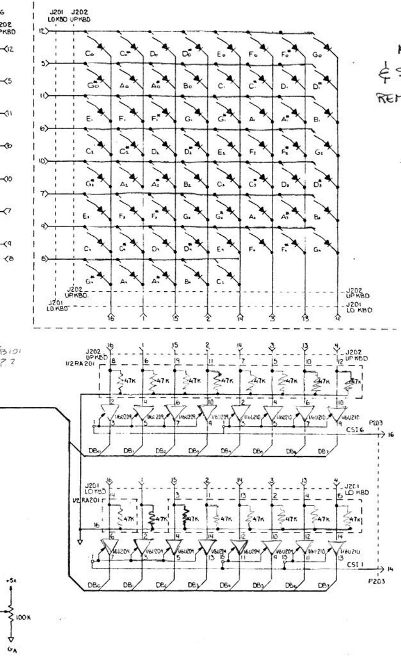
PCB3 COMPUTER BOARD 9-4

@ TB101 P.2


TB201
U215
4555
SW SEL A
SW SEL B
SW SEL C
D201 P-MOD FREQ A
D202 P-MOD PW A
D203 P-MOD FILTER
D206 LFO
D209 LFO
D205 LFO
D228 UPPER PROG 1
D229 UPPER PROG 2
D230 UPPER PROG 3
D231 UPPER PROG 4
D232 UPPER PROG 5
D233 UPPER PROG 6
D234 UPPER PROG 7
D235 UPPER PROG 8
D216 W-MOD FREQ A
D217 W-MOD FREQ B
D218 W-MOD PW A
D219 W-MOD PW B
D225 W-MOD FILTER
D220 UNISON
D238 LOWER PROG 1
D239 LOWER PROG 2
D240 LOWER PROG 3
D241 LOWER PROG 4
D242 LOWER PROG 5
D243 LOWER PROG 6
D244 LOWER PROG 7
D245 LOWER PROG 8
D204 OSC A
D205 OSC A
D206 SYNC
D210 OSC B
D211 OSC B
D212 OSC B
D227 UPPER BANK
D246 LOWER BANK
(U TRANS UP) B7
(U TRANS DN) B7
(L TRANS UP) B7
(L TRANS D) B7
(FIL KBD) B1
(PEDI FA) B1
(PEDI FB) B1
(PEDI FIL) B1
D213 OSC B LO
D214 OSC B KBD
(PEDI AMP) B1
(PEDI MOD) B1
(PED2 FIL) B1
(PED2 AMP) B1
D207 LFO MIX
(RELEASE) B1
(DRONE) B1
D221 MODE NORMAL
D222 MODE SINGLE
D223 MODE DOUBLE
D224 MODE ALT
(TUNE) B1
(A-440) B1
D236 RECORD
D237 PRESET
D226 UPPER SELECT
D247 LOWER SELECT
RA204
47K
P202
POLARIZING PIN
J201 LO KBD
J202 UP KBD
P203
CSI 7
CSI 5
CSOL 6
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7
U213
4174
1/2U203
4556
U201
U202
4051
+5A
R201 1K
R202 1K
CUTOFF
-MUX Ø
-MUX 3
VMUX
FINE
100K
ENV AMT
RES
VOL
R205 LFO FREQ
R211 LFO AMT
R203 P-MOD ENV
R204 P-MOD OSC
R210 FREQ A
R215 FREQ B
R214 GLIDE
R208 PW A
R213 PW B
R207 VOL A
R206 VOL B
R209 VOL NOISE
P201
Potes = 10K

4

KEYBOARD
& SWITCHES
REM. POTS



SEQUENTIAL CIRCUITS INC

PCB 2 (LEFT CONTROL PANEL)

D 1010

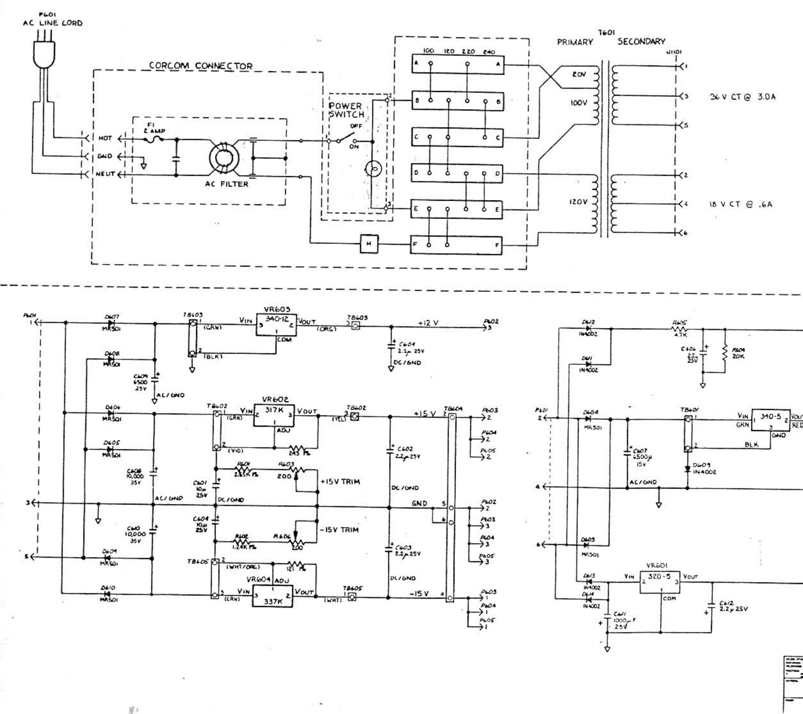
SD212 2-2

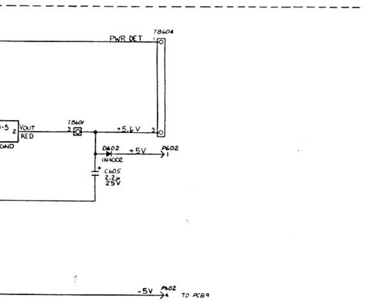
SHEET 2 OF 2

SCALE NONE

2-20-79 GA, RN, PERIPH DRVR

DATE REVISION

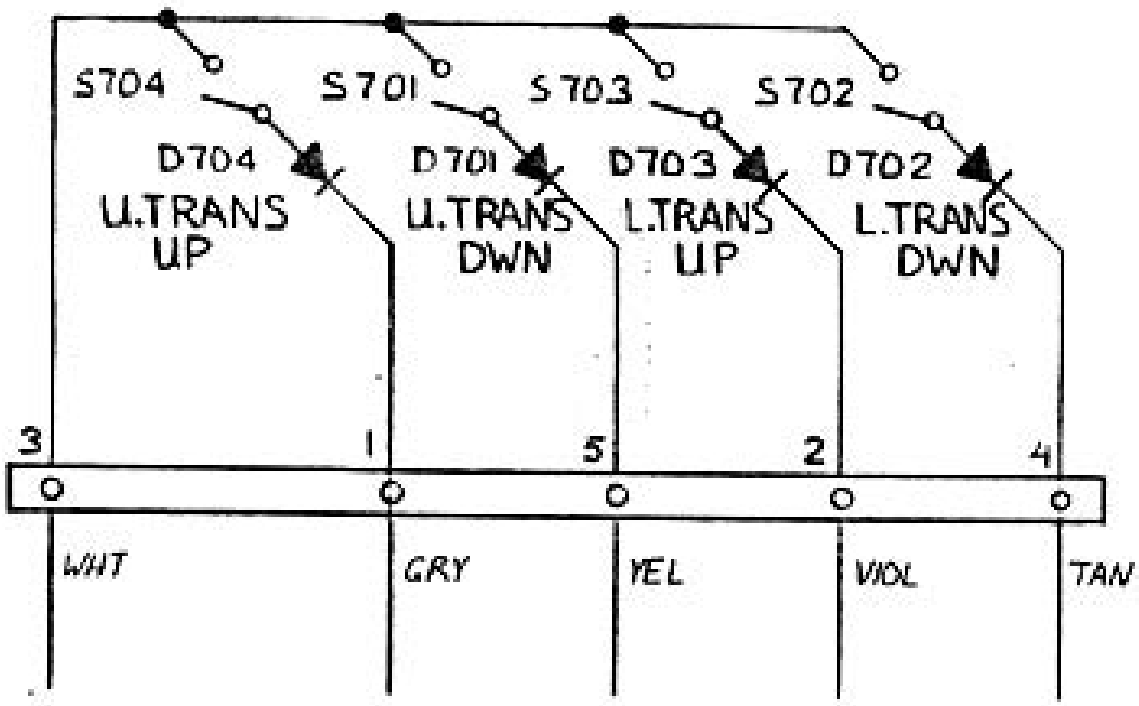

P601
AC LINE CORD
CORCOM CONNECTOR
F1
2 AMP
HOT
GND
NEUT
AC FILTER
POWER SWITCH
OFF
ON
100 120 220 240
T601
PRIMARY
SECONDARY
J1101
20V
100V
120V
36 V CT @ 3.0A
18 V CT @ .6A
VR603
340-12
VIN
VOUT
COM
TB603
+12 V
C604
2.2μ 25V
DC/GND
D607
MR501
D608
MR501
C609
6500
25V
AC/GND
VR602
317K
ADJ
TB602
+15 V
TB604
243
C602
2.2μ 25V
R601
2.55K
R603
200
+15V TRIM
C601
10μ
25V
C608
10,000
35V
D606
MR501
D605
MR501
GND
-15V TRIM
R602
1.24K
R606
200
C603
2.2μ 25V
C610
10,000
35V
D609
MR501
D610
MR501
TB605
VR604
337K
121
-15 V
P602
P603
P604
P605
D612
IN4002
D611
IN4002
R605
4.7K
C606
2.2
25V
R604
20K
D604
MR501
TB601
340-5
BLK
C607
6500μ
15V
D609
IN4002
AC/GND
D603
MR501
VR601
320-5
D613
IN4002
D614
IN4002
C611
1000μF
25V
C612
2.2μ 25V



SEQUENTIAL CIRCUITS INC

PCB-6 POWER SUPPLY

D 1010



TB701

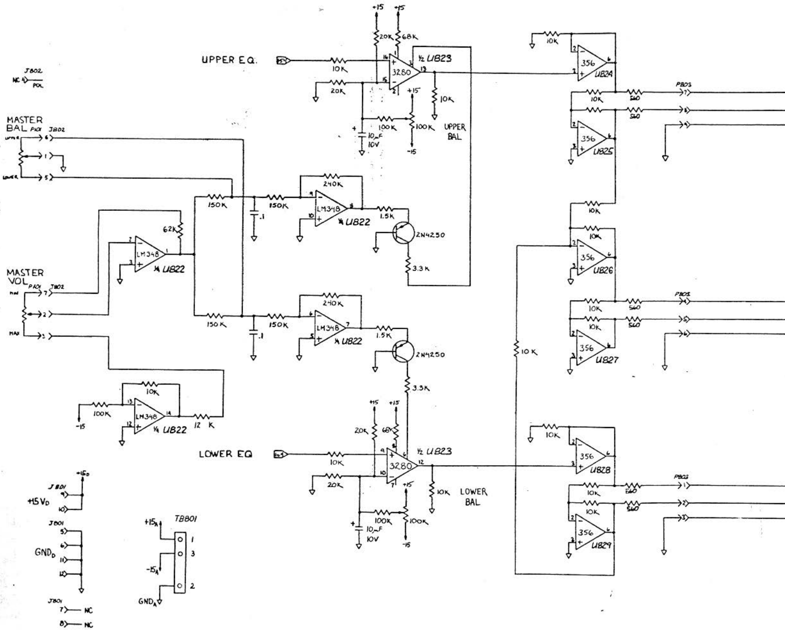
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS ± DECIMALS .XX ± .XXX ± ANGLES ±					
MATERIAL					
Finish:					
SCALE NONE		FIRST / LAST S/N	DATE	ECR No.	REVISION

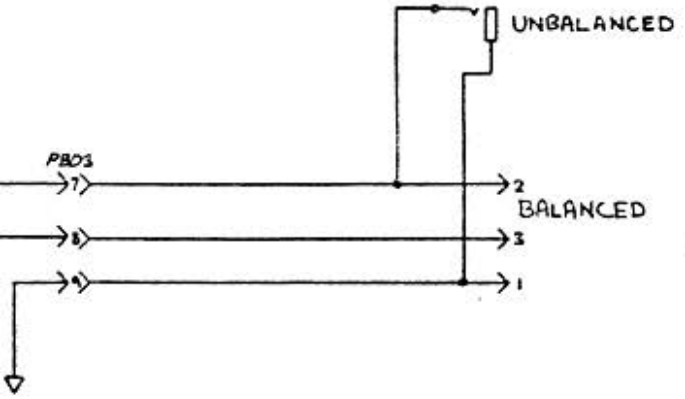
REVISION	LTR
	J
	H
	G
	F
	E
	D
	C
	B
	A

SEQUENTIAL CIRCUITS INC

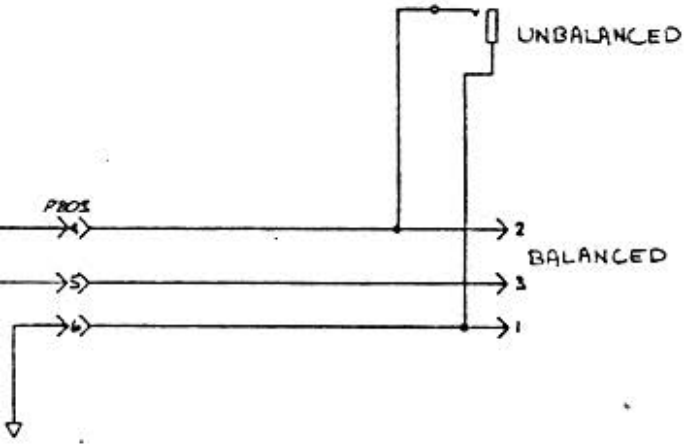
TITLE: PCB 7
TRANSPOSITION CONTROLLER

DSN	DATE	SIZE: D	MODEL No: 1010
DWG: D. Smith	5/1/80	DOCUMENT No: SD 711	
APP			
ISS		SUPERSEDES: SD701	SHEET 1 OF 1

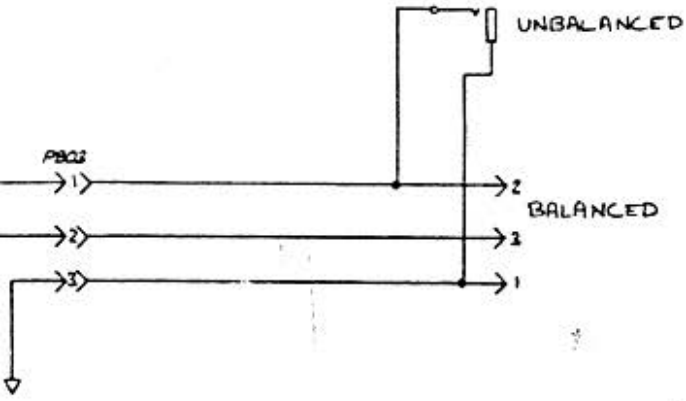

UPPER EQ.
LOWER EQ
MASTER BAL
MASTER VOL
UPPER BAL
LOWER BAL
½ U823
¼ U822
U824
U825
U826
U827
U828
U829
LM348
3280
356
2N4250
J802
J801
P802
P803
TB801
+15VD
GNDD
+15A
-15A
GNDA



UPPER
AUDIO
OUT



MONO
AUDIO
OUT



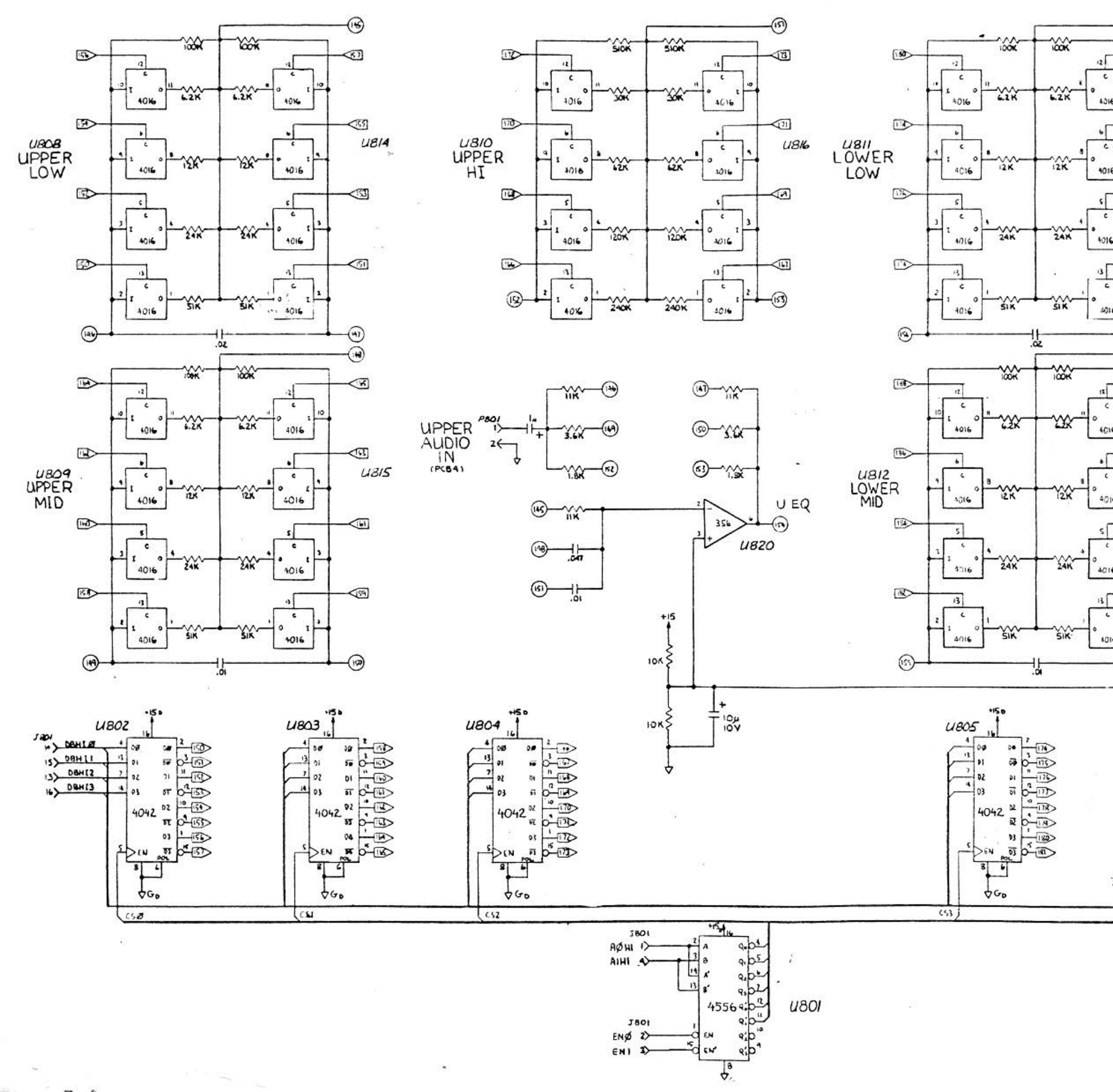
LOWER
AUDIO
OUT

SEQUENTIAL CIRCUITS INC

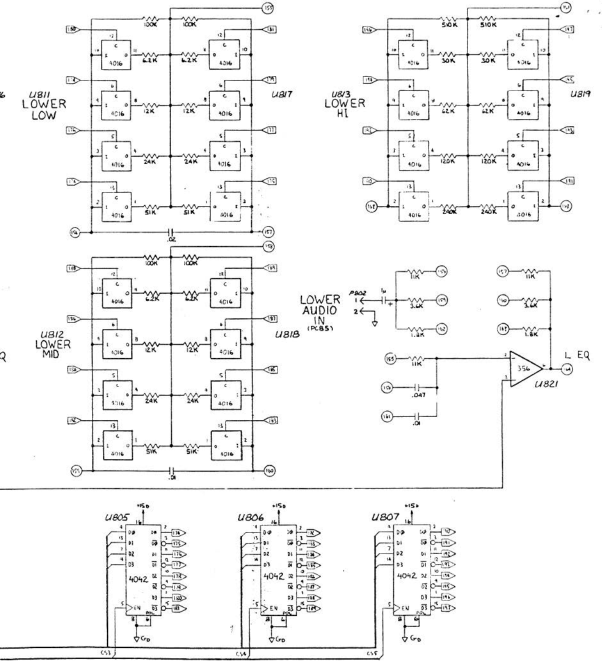
PCB 8
EQ / AUDIO OUTPUT

D

SD811

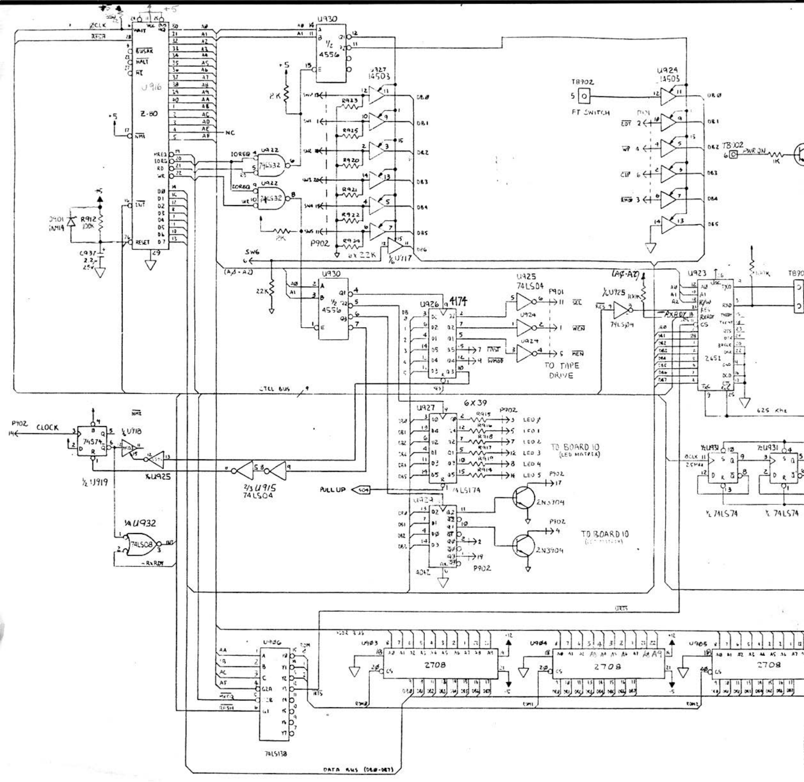

U808
UPPER
LOW
U814
U810
UPPER
HI
U816
U811
LOWER
LOW
U809
UPPER
MID
U815
U812
LOWER
MID
UPPER
AUDIO
IN
(PCB4)
P801
U EQ
U820
356
4016
4042
4556
U802
U803
U804
U805
U801
J801
+15
CS0
CS1
CS2
CS3
100K
6.2K
12K
24K
51K
510K
30K
62K
120K
240K
11K
3.6K
1.8K
5.6K
1.5K
.047
.01
.02
10K
10µ
10V

16

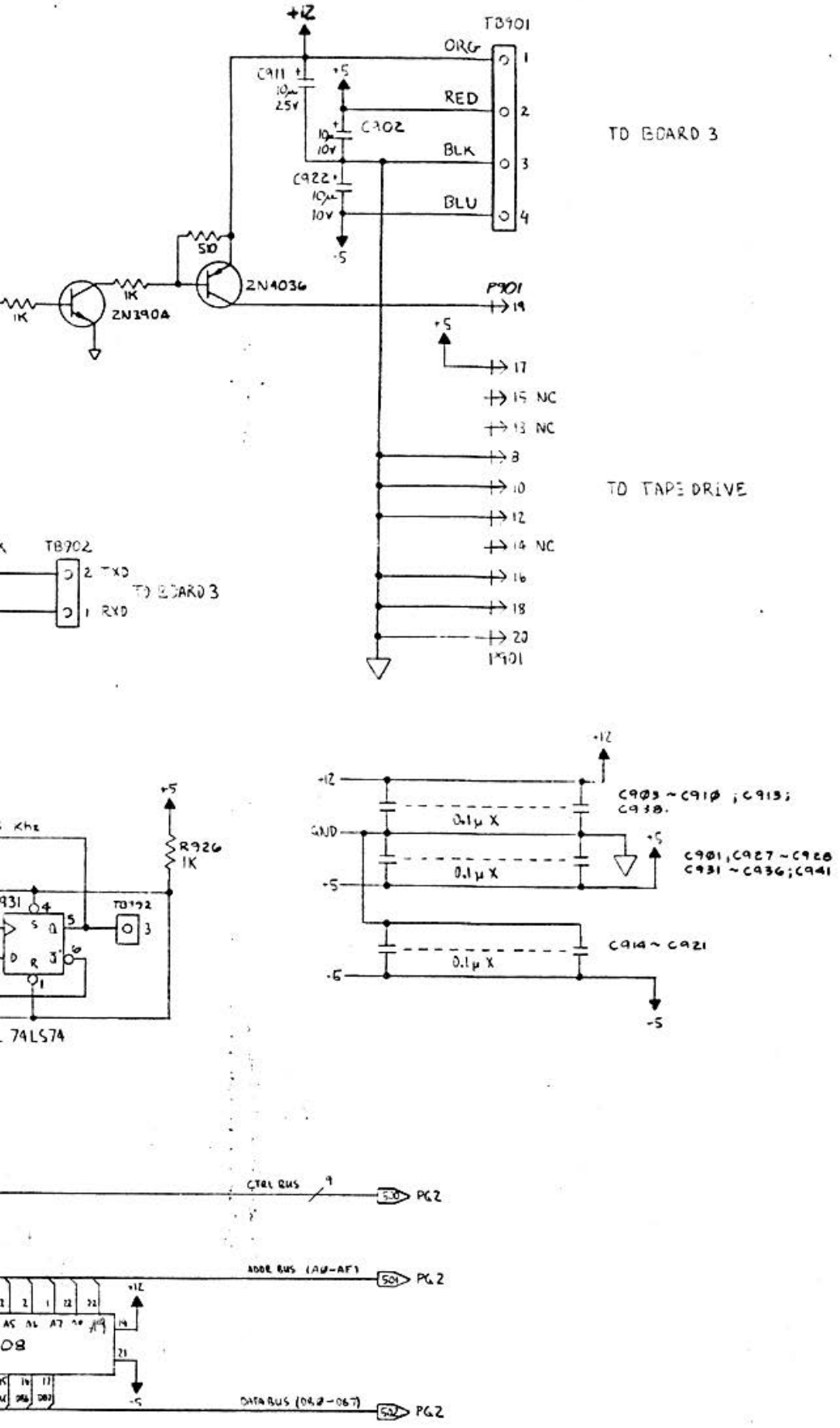


801

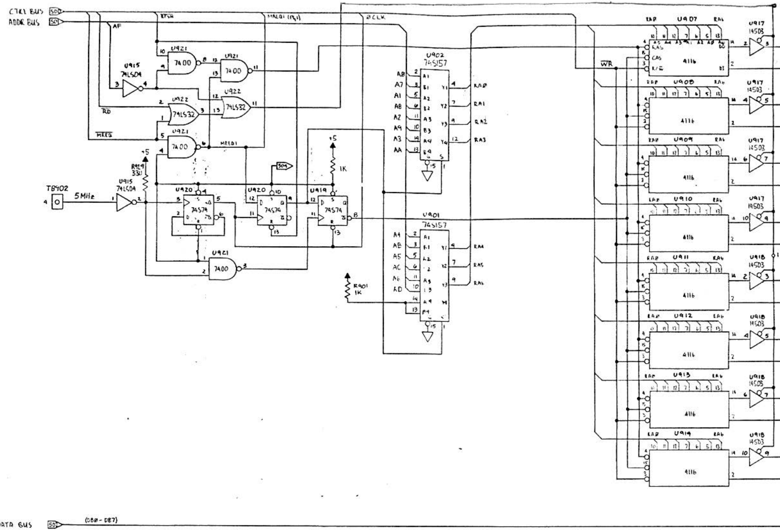
SEQUENTIAL CIRCUITS INC
PCB 8 EQ / AUDIO OUT
D 1010
MD811



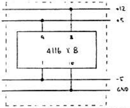
Z-80
U916
U930
1/2 4556
U927
14503
U924
14503
TB902
FT SWITCH
PWR ON
U922
74LS32
IOREQ
P902
6X22K
1/6 U717
SW6
U930
1/2 4556
U925
74LS04
U926
4174
TO TAPE DRIVE
U923
2651
625 Khz
CTRL BUS
CLOCK
74S74
1/2 U919
1/4 U918
1/6 U925
2/3 U915
74LS04
PULL UP
U927
6X39
74LS174
TO BOARD 10
(LED MATRIX)
LED 0
LED 1
LED 2
LED 3
LED 4
LED 5
2N3904
4042
1/4 U932
74LS08
1/2 U931
1/2 74LS74
U906
74LS138
U903
2708
U904
2708
U905
2708
DATA BUS (DB0-DB7)



		SEQUENTIAL CIRCUITS INC	
		TITLE PCB9 - POLYPHONIC SEQUENCER CPU	
		DSN	SIZE D / 1010
		DWN	DOCUMENT No. SD911
		APP	
SCALE NONE	2/15/80 DATE	ISS LAST REVISION	SHEET 1 OF 2

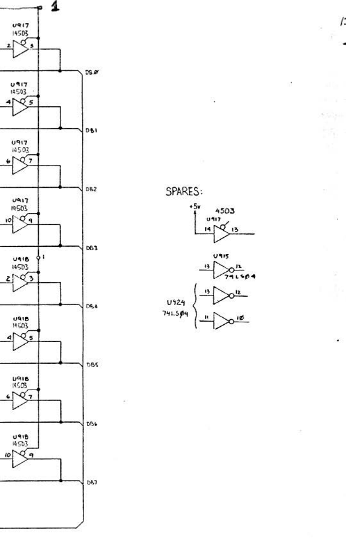


LAST	NOT USED
C941	
D901	
J903	
P901	
Q902	
R929	
504	



4116 POWER CONNECTION

SCALE NONE



SEQUENTIAL CIRCUITS INC

TITLE PCB9 - POLYPHONIC SEQUENCER 2.5MHz CLOCK/RAM

SIZE D

DOCUMENT No. SD912

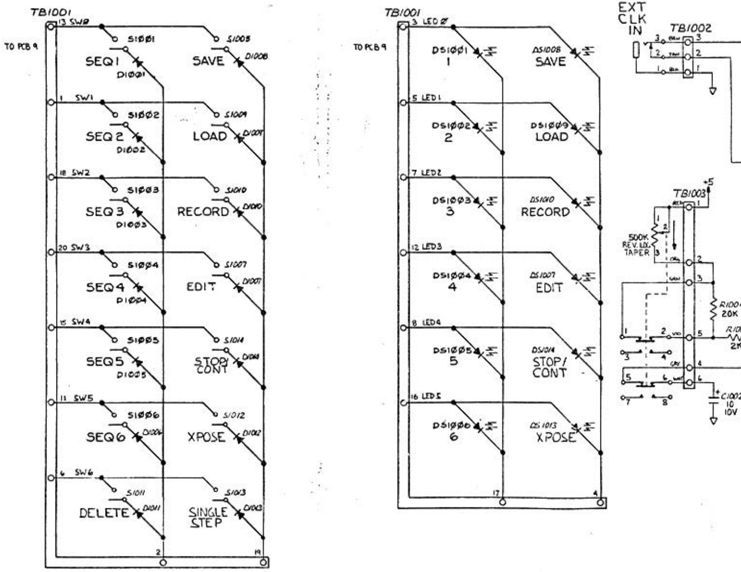
SHEET 2 OF 2

DATE 7-12-79

NONE

NOTES

1. ALL RESISTANCES IN OHMS,
2. ALL CAPACITANCES IN MICROFARADS,
3. UNLESS OTHERWISE SPECIFIED.



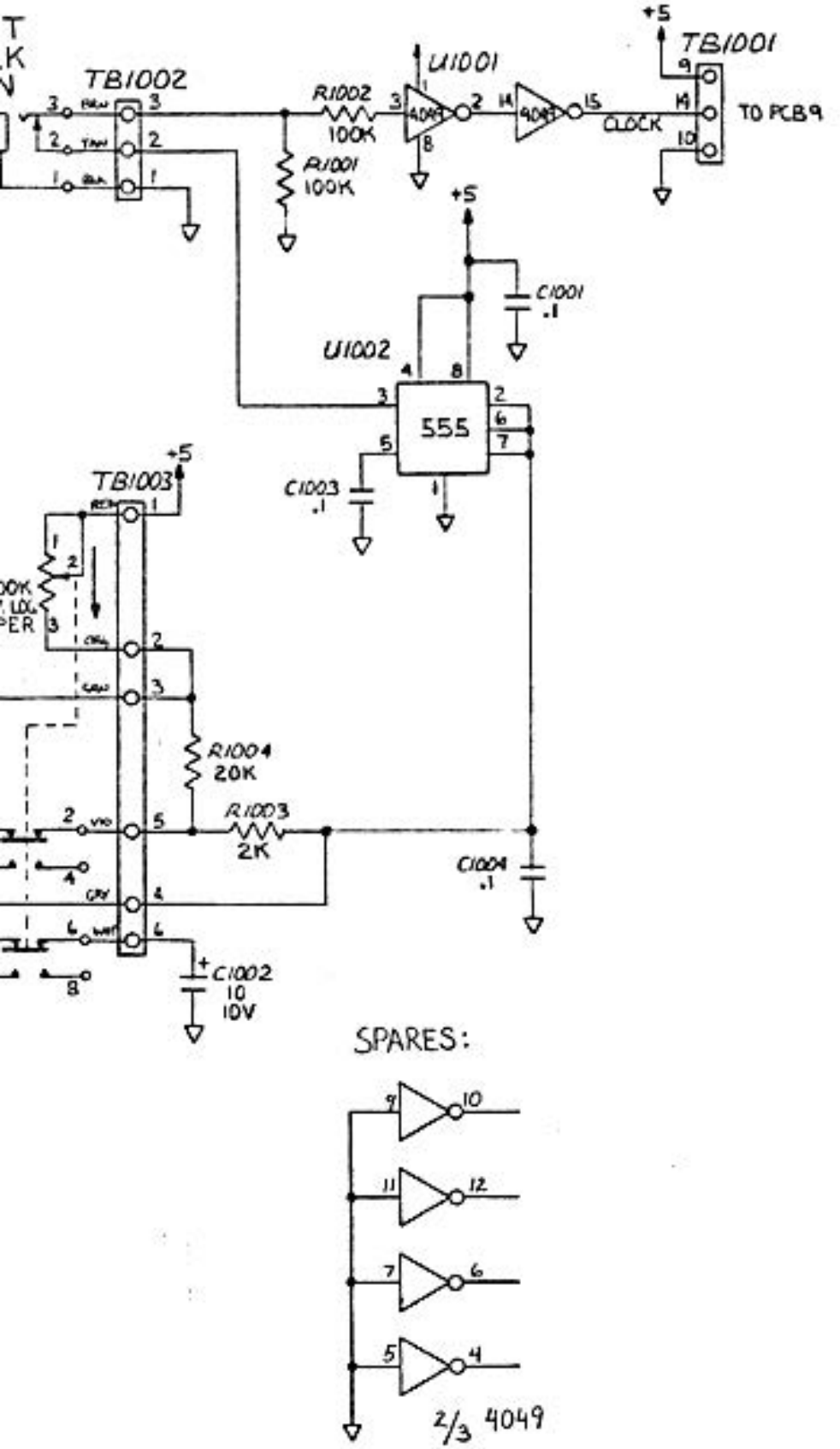
SEQ

TITLE

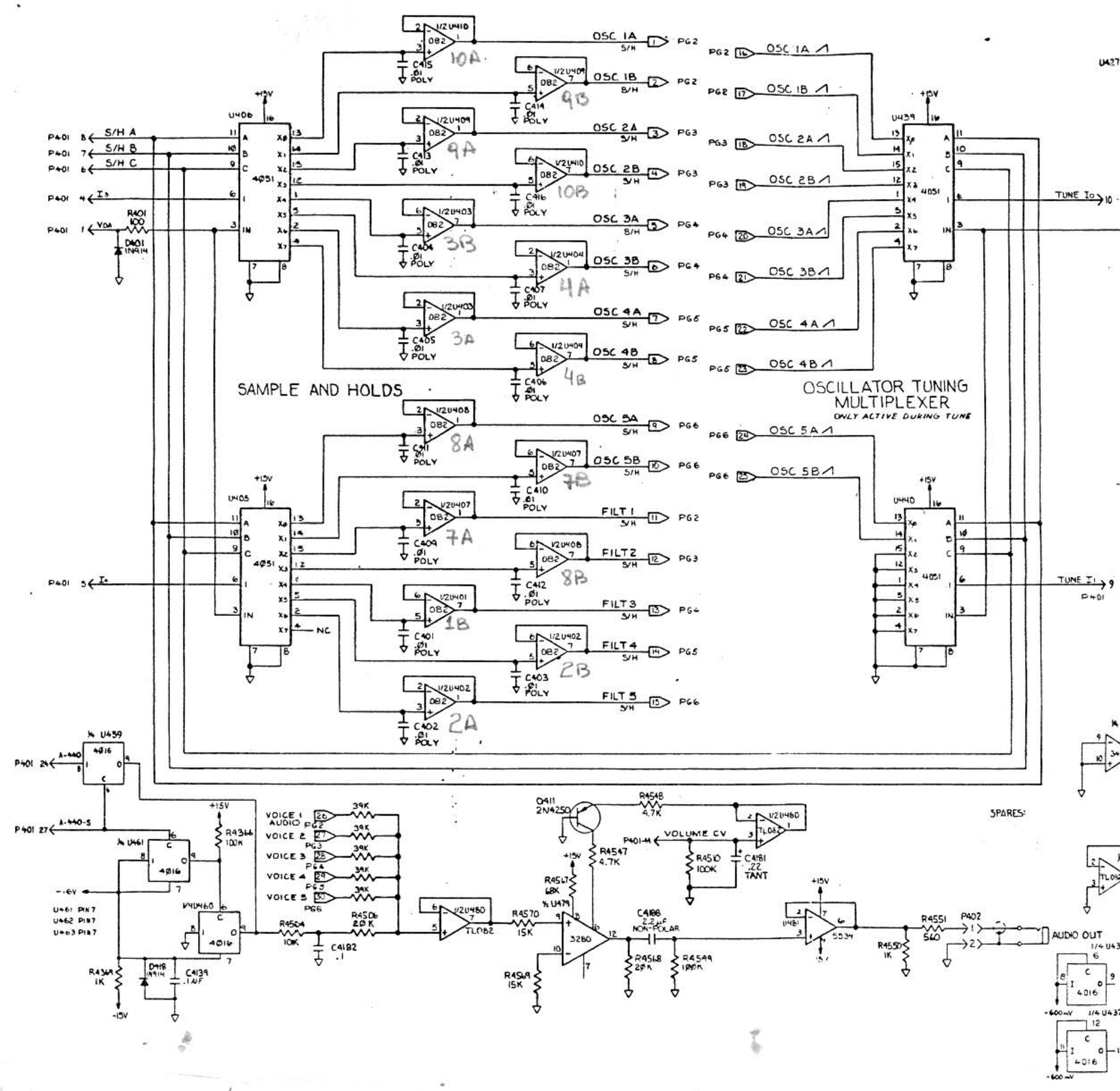
POLY

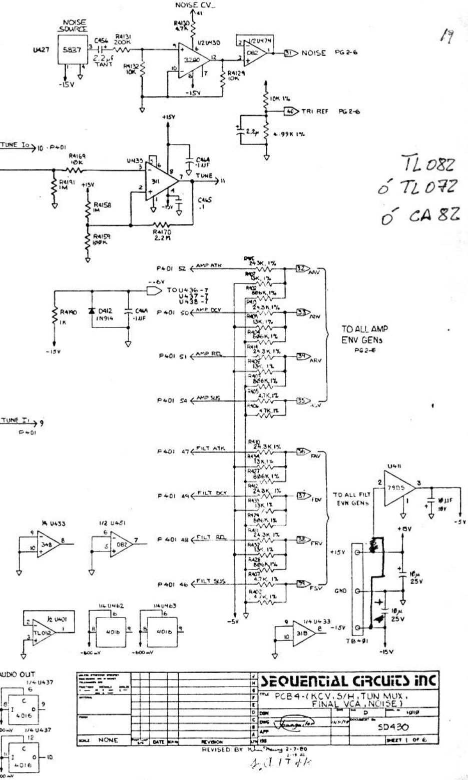
DSN

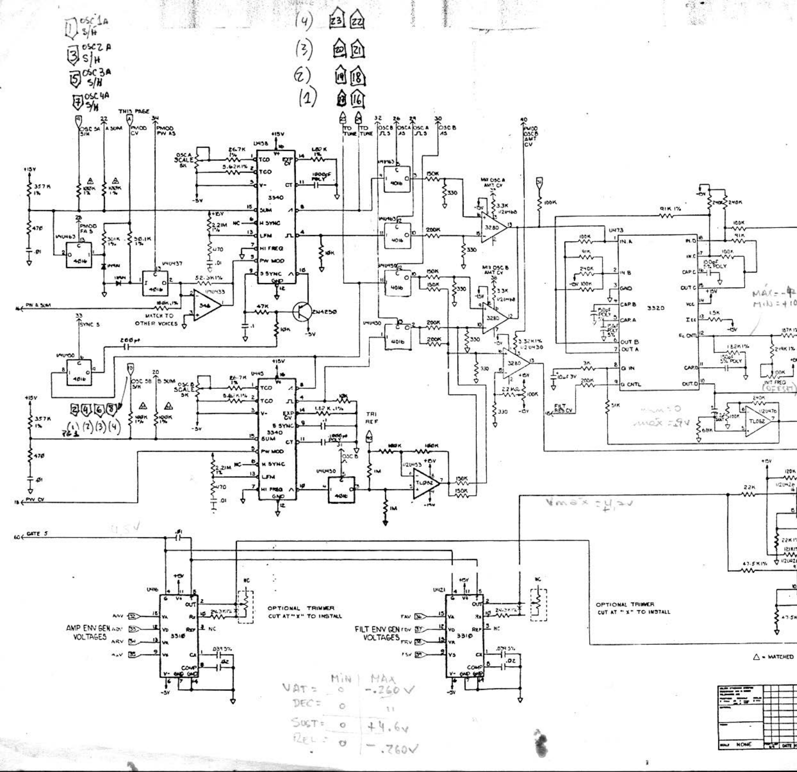
DWG

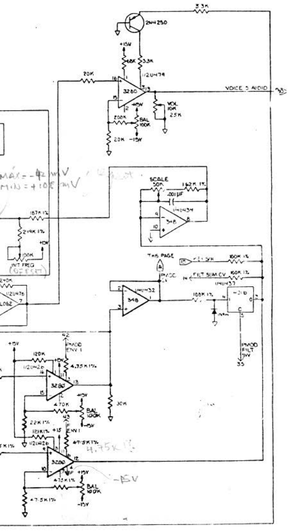


SEQUENTIAL CIRCUITS INC		
TITLE: PCB 10 POLY-SEQUENCER CONTROLLER		
DSN	SIZE C	MODEL NO. 1010
DWG	DATE 5/24/84	DOCUMENT NO.


SAMPLE AND HOLDS
OSCILLATOR TUNING MULTIPLEXER
ONLY ACTIVE DURING TUNE
OSC 1A S/H
OSC 1B S/H
OSC 2A S/H
OSC 2B S/H
OSC 3A S/H
OSC 3B S/H
OSC 4A S/H
OSC 4B S/H
OSC 5A S/H
OSC 5B S/H
FILT 1 S/H
FILT 2 S/H
FILT 3 S/H
FILT 4 S/H
FILT 5 S/H
S/H A
S/H B
S/H C
TUNE Io
TUNE I1
VOLUME CV
AUDIO OUT
SPARES:
VOICE 1 AUDIO
VOICE 2
VOICE 3
VOICE 4
VOICE 5
U406
U405
U439
U440
4051
4016
TL082
3280
5534


19
NOISE CV
NOISE SOURCE
U427 5837
C456 2.2 µf TANT
R4131 200K
R4132 10K
R4130 4.7K
1/2 U430 3280
R4129 10K
1/2 U474 082
NOISE PG 2-6
10K 1%
TRI REF PG 2-6
4.99K 1%
TUNE I0 10 P401
R4169 10K
R4191 1M
U435 311
C464 .1UF
TUNE 11
C465 .1
R4158 1M
R4159 100K
R4170 2.2M
TL 082
ó TL 072
ó CA 82
P401 52 AMP ATK
TO U436-7 U437-7 U438-7
R4190 1K
D412 1N914
C469 .1UF
P401 50 AMP DCY
P401 51 AMP REL
P401 54 AMP SUS
TO ALL AMP ENV GENs PG 2-6
AAV
ADV
ARV
TUNE I1 9 P401
P401 47 FILT ATK
P401 49 FILT DCY
P401 48 FILT REL
P401 46 FILT SUS
FAV
FDV
FRV
FSV
TO ALL FILT ENV GENs
U411 7905
-5V
+8V
+15V
GND
-15V
TB401
1/4 U433 348
1/2 U451 082
1/2 U401 TL062
1/4 U462 4016
1/4 U463 4016
1/4 U433 318
AUDIO OUT
1/4 U437 4016
SEQUENTIAL CIRCUITS INC
PCB4-(KCV, S/H, TUN MUX, FINAL VCA, NOISE)
SD430
SHEET 1 OF 6
SCALE NONE
REVISED BY 2-7-80


OSC 1A S/H
OSC 2A S/H
OSC 3A S/H
OSC 4A S/H
THIS PAGE
OSC 5A S/H
A SUM
PMOD CV
PMOD PW AS
OSC A SCALE 5K
U458
3340
TCO
EXP CV
SUM
H SYNC
LFM
HI FREQ
PW MOD
S SYNC
2N4250
PMOD FA S
PW & SUM
MATCH TO OTHER VOICES
SYNC S
TO TUNE
OSC B ∏S
OSC A AS
OSC A ∏S
OSC B AS
PMOD OSC B AMT CV
MIX OSC A AMT CV
MIX OSC B AMT CV
3280
U473
3320
IN A
IN B
GND
CAP B
CAP A
OUT B
OUT A
Q IN
Q CNTL
IN D
IN C
CAP C
OUT C
VCC
IEE
FC CNTL
CAP D
OUT D
TL082
INIT FREQ (OFFSET)
FILT RES CV
OSC 5B S/H
B SUM
OSC B SCALE 5K
U445
TRI REF
PW CV
GATE 5
U416
3310
AMP ENV GEN VOLTAGES
AAV
ADV
ARV
ASV
OPTIONAL TRIMMER CUT AT "X" TO INSTALL
U421
FILT ENV GEN VOLTAGES
FAV
FDV
FRV
FSV
△ = MATCHED
NONE
DATE
MIN
MAX
VAT = 0 -.260 V
DEC = 0
SUST = 0 +4.6v
REL = 0 -.260V
Vmax = 4.7v



△ = MATCHED RESISTORS (NUMBERED TO MATCH EACH OTHER)

SEQUENTIAL CIRCUITS INC

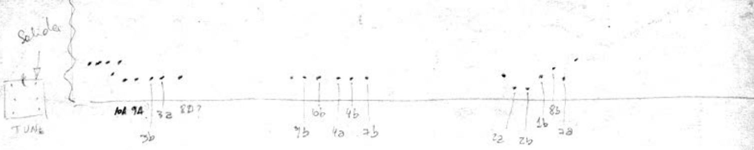
PCB4 (VOICE 5)

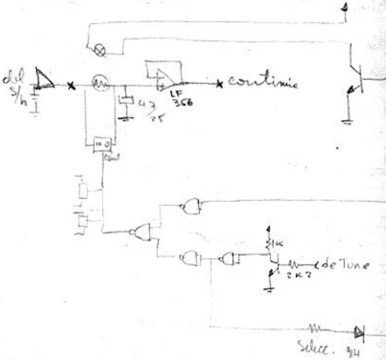
SD435

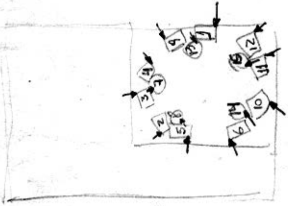
SHEET 1 OF 1

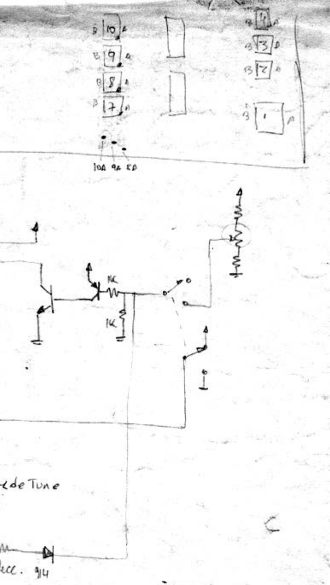
REVISED BY 2-11-80

PARA AFINAR
VCF =
UP A = SCALE
LO A = OFFSET
VARIAS VECES


Salida
TUNE
3b
6b
4b
9b
4a
7b
8b
1b
2b


S/h
LF
356
1K
2K2
de Tune
Selecc.




10A 9A 8A
1K
1K
ideTune
lecc. 914